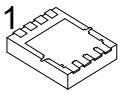
5.0A, 500V DUAL N-CHANNEL ENHANCEMENT MODE POWER MOSFET



PDFN5×6

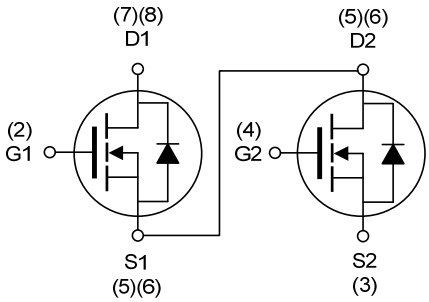
■ DESCRIPTION

The UTC **5NN50-SE1** is a dual N-Channel enhancement mode silicon gate power MOSFET with Fast Body Diode, is designed high voltage, high speed power switching applications such, is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)} \leq 3.1\ \Omega$ @ V_{GS}=10V, I_D=1.5A
* Fast body diode MOSFET technology
* Fast Switching Speed
* Simple Drive Requirement

■ SYMBOL



■ ORDERING INFORMATION

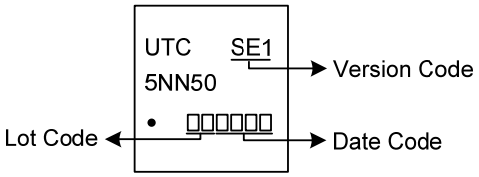
Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
5NN50L-SE1-P5060-R	5NN50G-SE1-P5060-R	PDFN5×6	NC	G1	S2	G2	D2/ S1	D2/ S1	D1	D1	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

5NN50G-SE1-P5060-R

(1)Packing Type	(1) R: Tape Reel
(2)Package Type	(2) P5060: PDFN5×6
(3)Version Code	(3) Version SE1
(4)Green Package	(4) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	5	A
	Pulsed (Note 2)	I_{DM}	10	A
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.2	V/ns
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	38	mJ
Power Dissipation		P_D	20	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 10mH, I_{AS} = 2.8A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C.
4. I_{SD} ≤ 5.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	65	°C/W
Junction to Case	θ_{JC}	6.25	°C/W

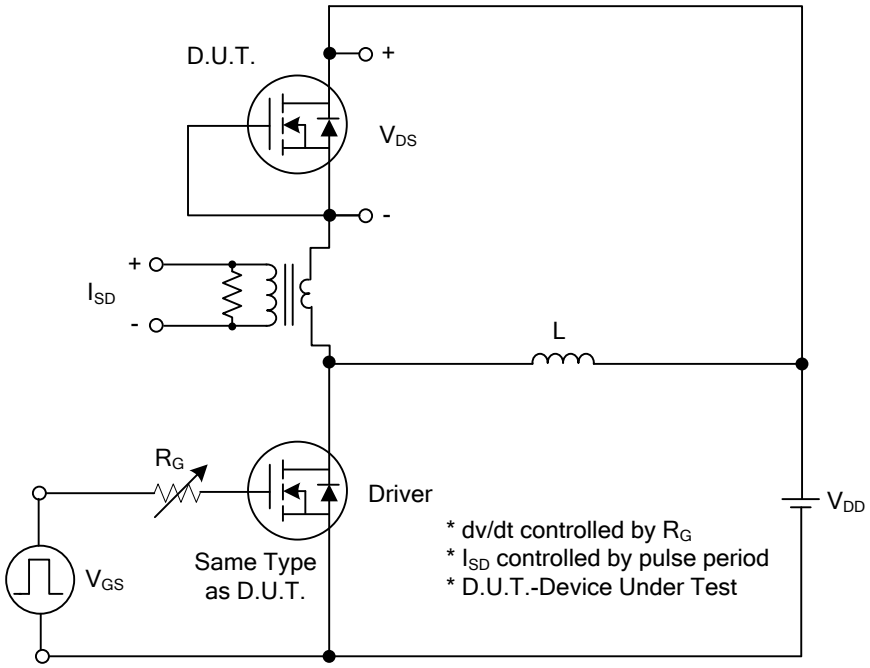
Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

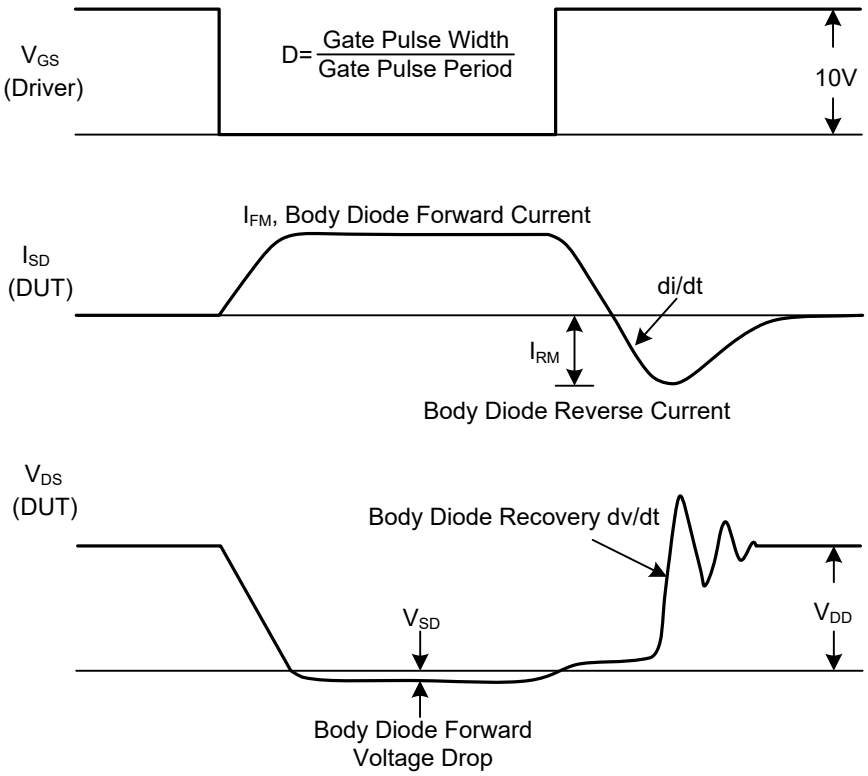
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D= 250μA	500			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=500V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=1.5A			3.1	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0 MHz		253		pF
Output Capacitance		C_{OSS}			37		pF
Reverse Transfer Capacitance		C_{RSS}			3		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=50V, V_{GS}=10V, I_D=2.5A (Note 1, 2)		12		nC
Gateource Charge		Q_{GS}			4		nC
Gate-Drain Charge		Q_{GD}			2		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DS}=100V, V_{GS}=10V, I_D=5.0A, R_G=25Ω (Note 1, 2)		4		ns
Rise Time		t_R			18		ns
Turn-off Delay Time		$t_{D(OFF)}$			17		ns
Fall-Time		t_F			21		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				2	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=5.0A , V_{GS}=0V			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	I_S=5.0A , V_{GS}=0V di/dt=100A/μs (Note1)		216		ns
Reverse Recovery Charge		Q_{rr}			1477		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

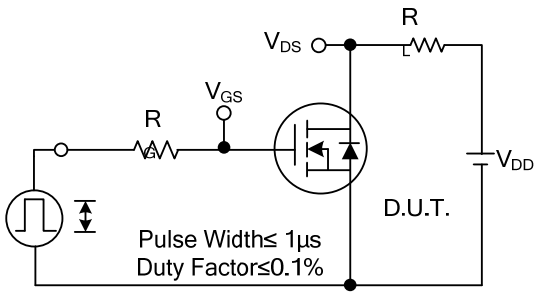


Peak Diode Recovery dv/dt Test Circuit

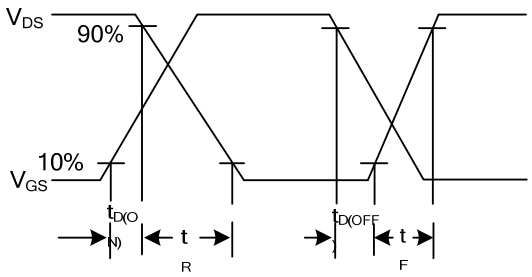


Peak Diode Recovery dv/dt Waveforms

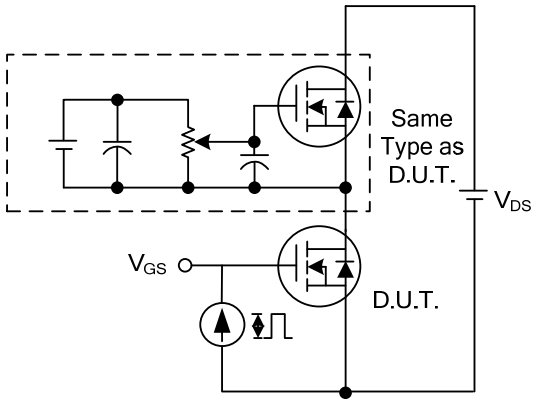
■ TEST CIRCUITS AND WAVEFORMS



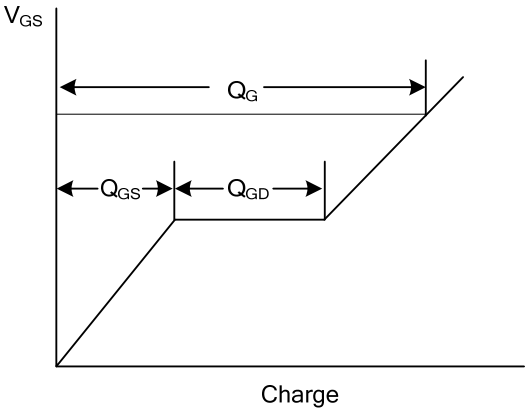
Switching Test Circuit



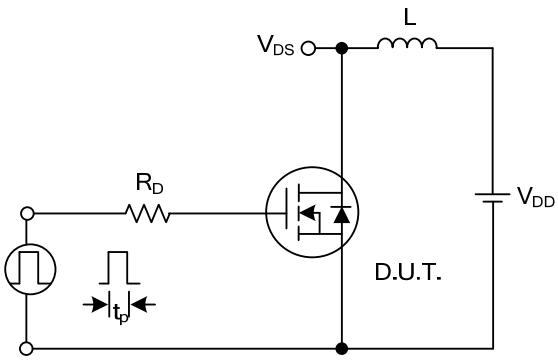
Switching Waveforms



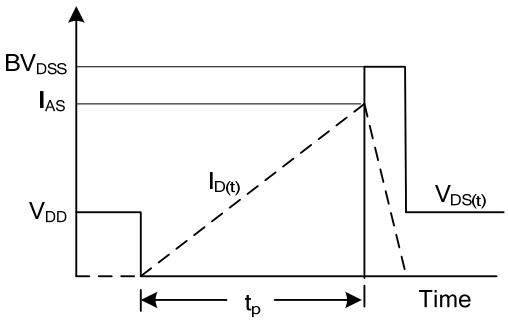
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms