



MGBR40L300C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

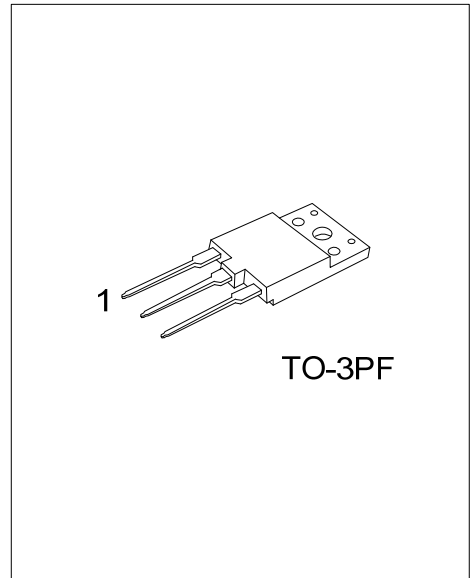
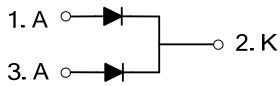
DESCRIPTION

The UTC **MGBR40L300C** is a dual mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

- * Low forward voltage drop
- * High switching speed

SYMBOL



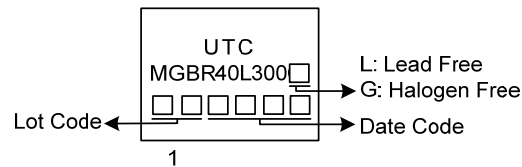
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
MGBR40L300CL-T3F-T	MGBR40L300CG-T3F-T	TO-3PF	A	K	A	Tube

Note: Pin Assignment: A: Anode K: Cathode

<p>MGBR40L300CG-T3F-T</p>	<p>(1) T: Tube (2) T3F: TO-3PF (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V _{RM}	300	V
Working Peak Reverse Voltage		V _{RWM}	300	V
Peak Repetitive Reverse Voltage		V _{RRM}	300	V
Average Rectified Output Current Per Device	Per Leg	I _O	20	A
	Total		40	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	180	A
Operating Junction Temperature		T _J	-65 ~ +150	°C
Storage Temperature		T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Typical Thermal Resistance	θ _{JC}	3	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A=25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	300			V
Forward Voltage Drop	V _{FM}	I _F =20A, T _J =25°C			0.95	V
		I _F =20A, T _J =125°C			0.85	V
Leakage Current (Note 1)	I _{RM}	V _R =300V, T _J =25°C			100	μA
		V _R =300V, T _J =125°C			10	mA

Note: Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

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