



75NM60-U3

Preliminary

Power MOSFET

75A, 600V N-CHANNEL SUPER-JUNCTION MOSFET

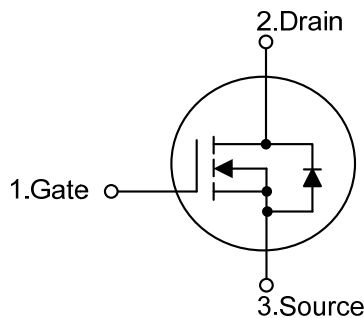
DESCRIPTION

The **UTC 75NM60-U3** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \leq 48 \text{ m}\Omega @ V_{GS}=10\text{V}, I_D=30\text{A}$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL

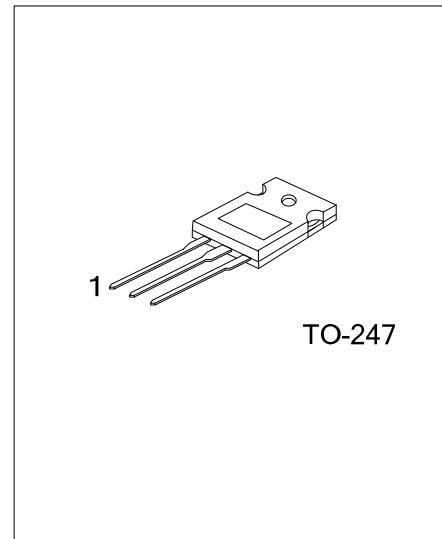


ORDERING INFORMATION

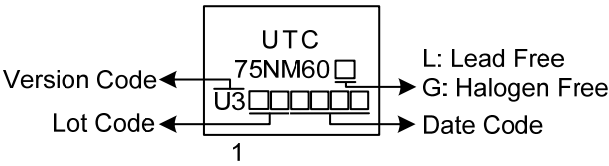
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
75NM60L-U3-T47-T	75NM60G-U3-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>75NM60G-U3-T47-T</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Version Code (4) Green Package 	<ul style="list-style-type: none"> (1) T: Tube (2) T47: TO-247 (3) Version U3 (4) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	75	A
	Pulsed (Note 2)	I_{DM}	225	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	2025	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.7	V/ns
Power Dissipation		P_D	250	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100\text{mH}$, $I_{AS} = 6.4\text{A}$, $V_{DD} = 90\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 30\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	40	$^\circ\text{C}/\text{W}$
Junction to Case	θ_{JC}	0.5	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

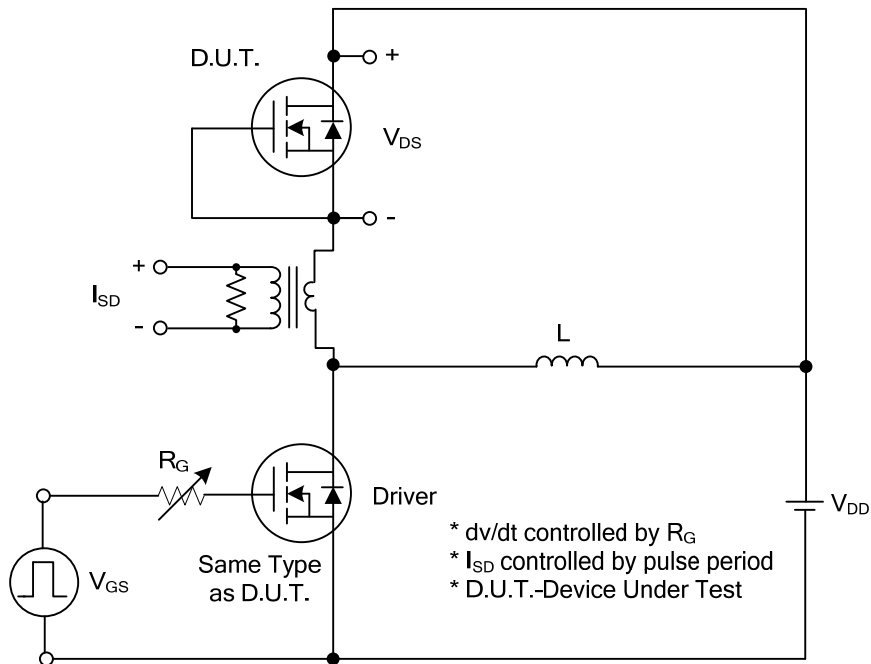
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=600\text{V}$, $V_{GS}=0\text{V}$			10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}$, $V_{DS}=0\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=30\text{A}$			48	m Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=50\text{V}$, $f=1\text{MHz}$		4541		pF
Output Capacitance	C_{OSS}			1884		pF
Reverse Transfer Capacitance	C_{RSS}			134		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	$V_{DS}=480\text{V}$, $V_{GS}=10\text{V}$, $I_D=75\text{A}$ (Note 1, 2)		170		nC
Gate-Source Charge	Q_{GS}			48		nC
Gate-Drain Charge	Q_{DD}			14		nC
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=100\text{V}$, $V_{GS}=10\text{V}$, $I_D=75\text{A}$, $R_G=25\Omega$ (Note 1, 2)		74		ns
Turn-On Rise Time	t_R			349		ns
Turn-Off Delay Time	$t_{D(OFF)}$			405		ns
Turn-Off Fall Time	t_F			135		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				75	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=75\text{A}$, $V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=30\text{A}$, $V_{GS}=0\text{V}$,		664		nS
Body Diode Reverse Recovery Charge	Q_{rr}	$dI_F/dt=100\text{A}/\mu\text{s}$		14		μC

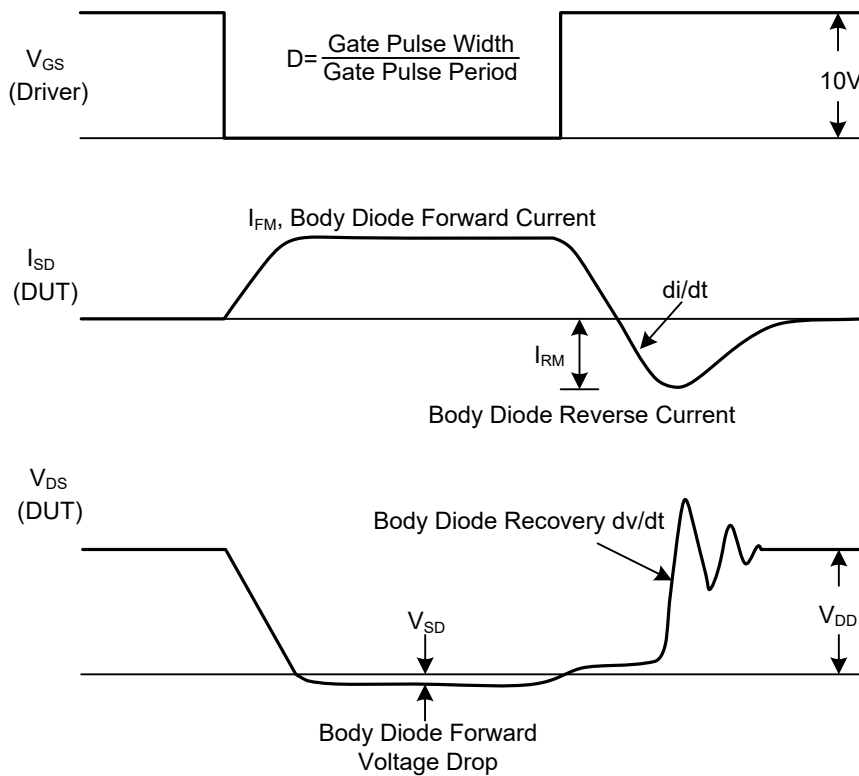
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

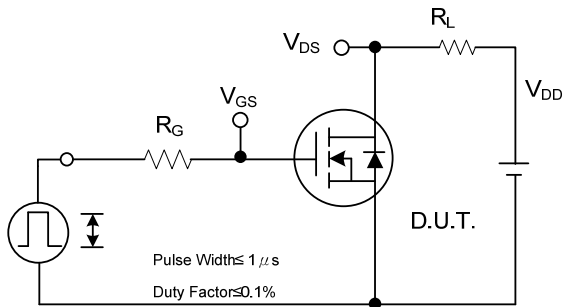


Peak Diode Recovery dv/dt Test Circuit

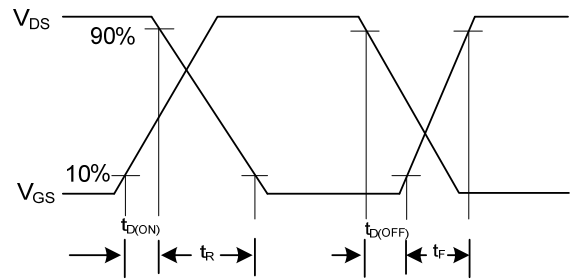


Peak Diode Recovery dv/dt Waveforms

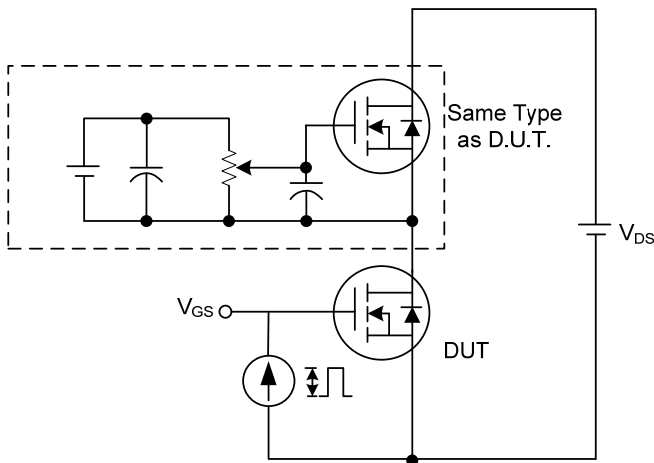
■ TEST CIRCUITS AND WAVEFORMS



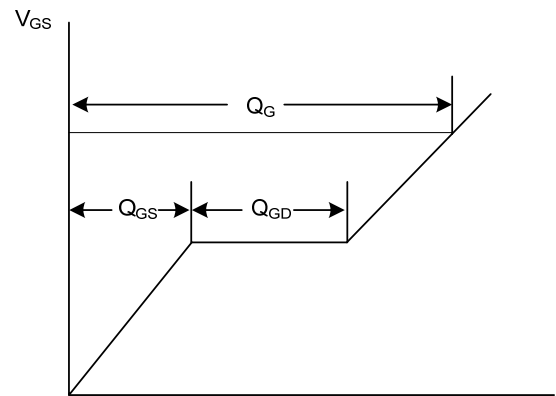
Switching Test Circuit



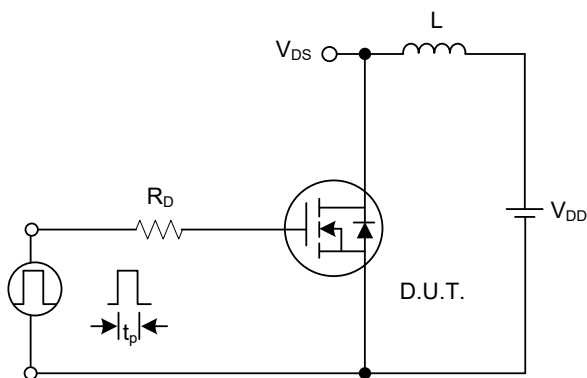
Switching Waveforms



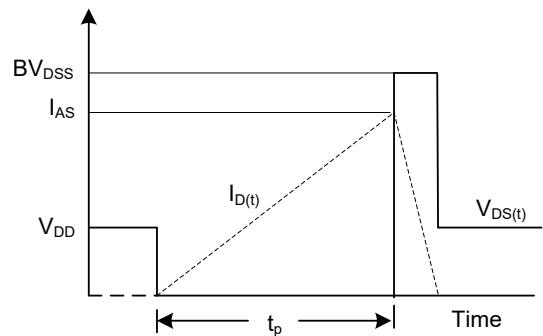
Gate Charge Test Circuit



Charge
Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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