UTG40N65FQ-S

**Preliminary** 

Insulated Gate Bipolar Transistor

# 650V TRENCH GATE FIELD-STOP IGBT

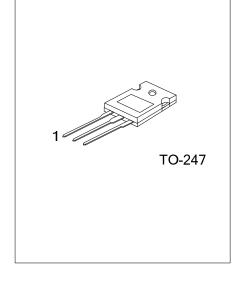
#### ■ DESCRIPTION

The UTC **UTG40N65FQ-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

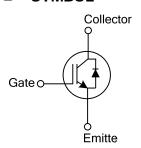
The UTC **UTG40N65FQ-S** is suitable for the resonant or soft switching applications.

### **■ FEATURES**

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage:  $V_{CE(SAT).Typ.}$ =1.4V @ Ic=40A,  $V_{GE}$ =15V ( $T_C$  =25°C)



#### ■ SYMBOL



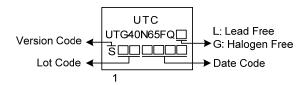
#### ORDERING INFORMATION

Ordering Number		Daakana	Pin Assignment			Da alaina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG40N65FQL-S-T47-T	UTG40N65FQG-S-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UTG40N65FQG-S-T47-T (1)Packing Type (2) T47: T0-247
(3) Version Code (3) Version S
(4) G: Halogen Free and Lead Free, L: Lead Free

#### ■ MARKING



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# ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		V <sub>CES</sub>	650	V	
Gate-Emitter Voltage		\/	±20	V	
Transient Gate-emitter voltage (tp < 5 ms)		$V_{GES}$	±25	V	
Continuous Collector Current	T <sub>C</sub> =25°C	Ic	80	Α	
	T <sub>C</sub> =100°C		40	Α	
Collector Current Pulsed (Note 1)		Ісм	80	Α	
Diada Farward Current	T <sub>C</sub> =25°C	I <sub>F</sub>	40	Α	
Diode Forward Current	T <sub>C</sub> =100°C		20	Α	
Short Circuit Withstand Time		tsc			
$V_{\rm GE} = 15 \text{V}, \ V_{\rm CC} \le 200 \text{V}$				μs	
Allowed number of short circuits < 1000			10		
Time between short circuits: ≥1.0s					
<i>T</i> <sub>VJ</sub> = 25°C					
Power Dissipation (T <sub>C</sub> =25°C)		P <sub>D</sub>	298	W	
Operating Junction Temperature		$T_J$	-40 ~ +150	°C	
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

# **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ.ιс	0.41	°C/W

<sup>2.</sup> Pulse width limited by maximum junction temperature.

# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics	-		_	I	I	ı	_
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>			650			V
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =650V, V <sub>GE</sub> =0V				5	μΑ
G-E Leakage Current	$I_{GES}$	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V				±400	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>		4.5		7.5	V
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =40A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C		1.4	2.1	V
			T <sub>C</sub> =125°C		1.8		V
Dynamic Characteristics							
Input Capacitance	CIES	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1MHz			2.66		nF
Output Capacitance	C <sub>OES</sub>				159		рF
Reverse Transfer Capacitance	C <sub>RES</sub>				41		pF
Switching Characteristics							
Total Gate Charge	$Q_{G}$				119		nC
Gate-Emitter Charge	$Q_GE$	V <sub>CE</sub> =520V, I <sub>C</sub> =40A, V <sub>GE</sub> =15V			31		nC
Gate-Collector Charge	$Q_GC$				58		nC
Turn-On Delay Time	t <sub>DON)</sub>				20		ns
Rise Time	$t_{R}$				77		ns
Turn-Off Delay Time	t <sub>DOFF)</sub>	V <sub>CC</sub> =400V, I <sub>C</sub> =40A, R <sub>G</sub> =5Ω, V <sub>GE</sub> =0~15V, L=500uH			90		ns
Fall Time	$t_{F}$				63		ns
Turn-On Switching Loss	Eon				3		mJ
Turn-Off Switching Loss	E <sub>OFF</sub>	<u> </u>			1.7		mJ
SOURCE- DRAIN DIODE RATINGS A	ND CHARAC	TERISTICS					
Forward Voltage Drop	VF	I <sub>F</sub> =20A				2.5	V
Reverse Recovery Time	t <sub>rr</sub>	-I <sub>F</sub> =20A, dI/dt=100A/μS, V <sub>CC</sub> =400V			77		ns
Reverse Recovery Charge	$Q_{rr}$				1.67		μC

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