UNISONIC TECHNOLOGIES CO., LTD

UTG40N65WAFQ

Preliminary

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

Insulated Gate Bipolar Transistor, it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

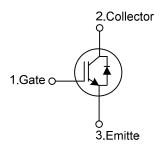
The UTC UTG40N65WAFQ is suitable for the resonant or soft switching applications.

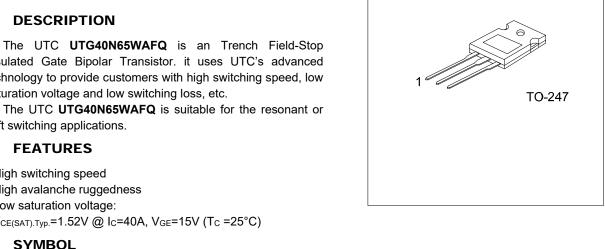
FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage:

V_{CE(SAT).Typ.}=1.52V @ I_C=40A, V_{GE}=15V (T_C =25°C)

SYMBOL



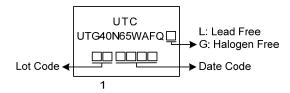


ORDERING INFORMATION

Ordering Number		Darles	Pin Assignment			Da alainan	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG40N65WAFQL-T47-T	UTG40N65WAFQG-T47-T	TO-247	G	С	E	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter UTG40N65WAFQG-T47-T (1) T: Tube (1)Packing Type (2)Package Type (2) T47: TO-247 (3)Green Package (3) G: Halogen Free and Lead Free, L: Lead Free

MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V _{CES}	650	V
Gate-Emitter Voltage		\/	±20	V
Transient Gate-emitter voltage (tp < 5 ms)		V_{GES}	±25	V
Continuous Collector Current	T _C =25°C	Ic	80	Α
	T _C =100°C		40	Α
Collector Current Pulsed (Note 1)		I _{CM}	160	Α
Diode Forward Current	T _C =25°C	I _F	40	Α
Diode Forward Current	T _C =100°C		20	Α
Short Circuit Withstand Time $V_{GE} = 15V, V_{CC} \le 200V$		tsc		
				μs
Allowed number of short circuits < 1000			10	
Time between short circuits: ≥ 1.0s				
$T_{VJ} = 25^{\circ}C$				
Power Dissipation (T _C =25°C)		P _D	298	W
Operating Junction Temperature		TJ	-40 ~ +150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θ.ιс	0.41	°C/W	

^{2.} Pulse width limited by maximum junction temperature.

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV _{CES}			650			V
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V				5	μΑ
G-E Leakage Current	I_{GES}	V _{CE} =0V, V _{GE} =±20V				±100	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=250\mu A,\ V_{CE}=V_{GE}$		4.5		7.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =40A, V _{GE} =15V	T _C =25°C		1.52	2.1	V
			T _C =125°C		1.92		V
Dynamic Characteristics							
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz			2690		pF
Output Capacitance	C _{OES}				216		рF
Reverse Transfer Capacitance	C _{RES}				53		рF
Switching Characteristics							
Total Gate Charge	Q_{G}				121		nC
Gate-Emitter Charge	Q _{GE}	V _{CE} =520V, I _C =40A, V _{GE} =15V			31		nC
Gate-Collector Charge	Q _{GC}				59		nC
Turn-On Delay Time	t _{DON)}	V _{CC} =400V, I _C =40A, R _G =5Ω, V _{GE} =0~15V, L=500uH			17		ns
Rise Time	t _R				40		ns
Turn-Off Delay Time	t _{DOFF)}				86		ns
Fall Time	t _F				131		ns
Turn-On Switching Loss	Eon				1.76		mJ
Turn-Off Switching Loss	E _{OFF}	7			1.19		mJ
SOURCE- DRAIN DIODE RATINGS A	ND CHARAC	TERISTICS					
Forward Voltage Drop	VF	I _F =20A				2.0	V
Reverse Recovery Time	t _{rr}	I _F =40A, dI/dt=100A/μS, V _{CC} =400V			42		ns
Reverse Recovery Charge	Qrr				379		nC
					_		

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