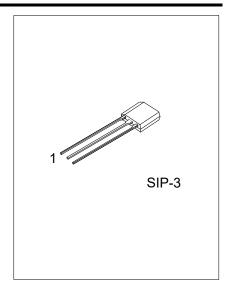
UHC1816 Preliminary CMOS IC

SINGLE OUTPUT HALL EFFECT LATCH

■ DESCRIPTION

The **UHC1816** is an integrated Hall effect latched sensor designed for electronic commutation of brush-less DC motor applications. The device using HVCMOS process includes an on-chip Hall voltage generator for magnetic sensing, a comparator that amplifies the Hall voltage, and a Schmitt trigger to provide switching hysteresis for noise rejection, and open-collector output. An internal band-gap regulator is used to provide temperature compensated supply voltage for internal circuits and allows a wide operating supply range.

If a magnetic flux density larger than threshold Bop, OUT is turned on (low). The output state is held until a magnetic flux density reversal falls below Brp causing OUT to be turned off (high).



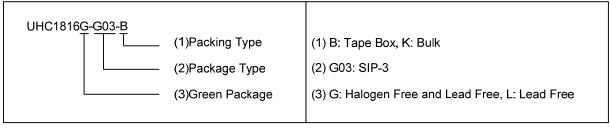
■ FEATURES

- * 3.0V~28V DC operation voltage
- * Temperature compensation
- * Wide operating voltage range
- * Open-Drain pre-driver
- * 25mA maximum sinking output current.

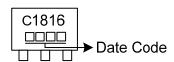
■ ORDERING INFORMATION

Ordering Number		Daalsana	Pin Assignment			Deelsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UHC1816L-G03-B	UHC1816G-G03-B	SIP-3	I	G	0	Tape Box	
UHC1816L-G03-K	UHC1816G-G03-K	SIP-3	I	G	0	Bulk	

Note: Pin Assignment: I: V_{DD} G: GND O: Output



■ MARKING

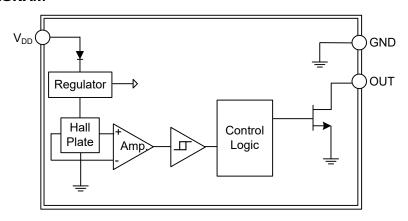


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■ PIN DESCRIPTION

PIN NAME	DESCRIPTION
V_{DD}	Supply voltage
GND	Ground
Output	Output voltage

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V_{CC}	28	V
Reverse V _{CC} Polarity Voltage		V_{RCC}	-26	V
Magnetic Flux Density		В	Unlimited	Gauss
Output Current	Continuous	lo	25	mA
Power Dissipation		P_{D}	400	mW
Ambient Temperature		T _A	-40 ~ +125	°C
Storage Temperature Range		T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **ELECTRICAL CHARACTERISTICS** (V_{DD}=12V , T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{DD}	Operating	3.0		26	٧
Supply Current	I _{DD}	Operating		3.0	4.5	mA
Output Leakage Current	loff	V _{OUT} = 12V		< 0.1	10	μA
Output Saturation Voltage	V _{DS(SAT)}	I _{OUT} =20mA		0.3		V

■ MAGNETIC CHARACTERISTICS (V_{DD}=12V, T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Operate Point, BOP	Вор	B>B _{OP} , V _{OUT} On	5	15	20	Gauss
Release Point, BRP	B _{RP}	B <b<sub>RP, V_{OUT} Off</b<sub>	-20	-15	-5	Gauss
Hysteresis	Вну	BOP - BRP		30		Gauss

Note: 1mT=10 Gauss.

■ DRIVER OUTPUT VS. MAGNETIC POLE

For SIP3

PARAMETER	TEST CONDITIONS	OUTPUT
North Pole	B < Brp	High
South Pole	B > Bop	Low

Note: The magnetic pole is applied facing the branded side of the SIP-3 package.

CHYSTERESIS CHARACTERISTICS

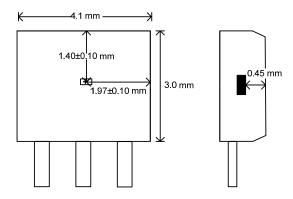


Fig. 1 SENSOR LOCATIONS

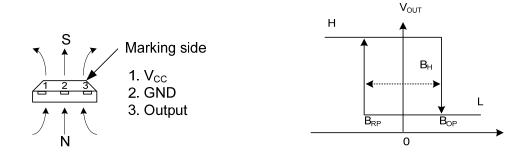
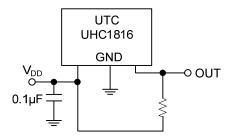


Fig. 2 APPLYING DIRECTION OF MAGNETIC FLUX

■ TYPICAL APPLICATION CIRCUIT



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