



UPGE5N65FQ

Insulated Gate Bipolar Transistor

650V, SMPS N-CHANNEL IGBT

DESCRIPTION

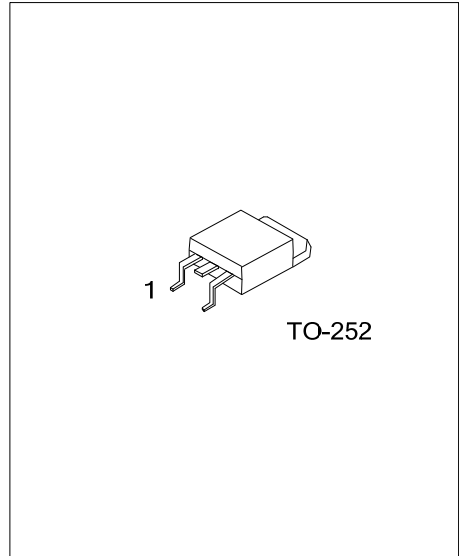
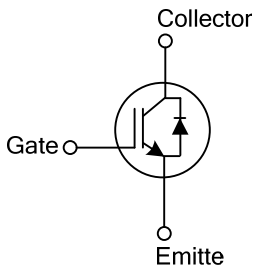
The UTC **UPGE5N65FQ** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC **UPGE5N65FQ** is suitable for high voltage switching, high frequency switch mode power supplies.

FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT),Typ.}=1.76V @ I_C=5.0A, V_{GE}=15V (T_C=25^{\circ}C)$

SYMBOL



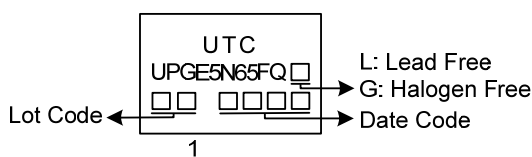
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UPGE5N65FQL-TN3-R	UPGE5N65FQG-TN3-R	TO-252	G	C	E	Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

<p>UPGE5N65FQG-TN3-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free L: Lead Free</p>
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MARKING



UPGE5N65FQ

Insulated Gate Bipolar Transistor

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V_{CES}	650	V
Gate-Emitter Voltage	V_{GES}	± 20	V
Transient Gate-emitter voltage ($t_p < 5$ ms)		± 25	V
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Collector Current Pulsed (Note 1)	I_{CM}	20	A
Diode Forward Current	I_F	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	A
Short Circuit Withstand Time $V_{GE} = 15\text{V}$, $V_{CC} \leq 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{VJ} = 25^\circ\text{C}$	t_{SC}	10	μs
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	36	W
Operating Junction Temperature	T_J	-40 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
Absolute maximum ratings are those values beyond which the device could be permanently damaged.
2. Pulse width limited by maximum junction temperature.

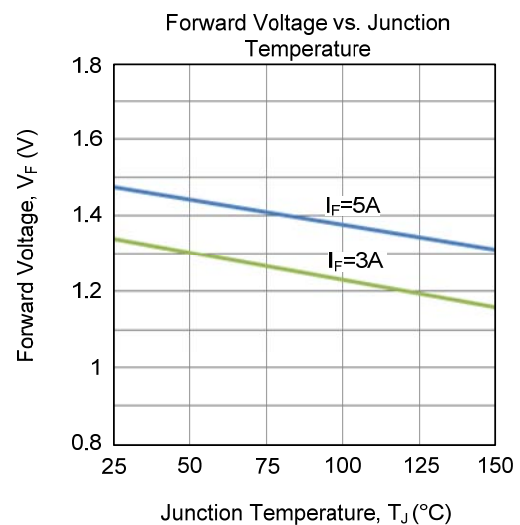
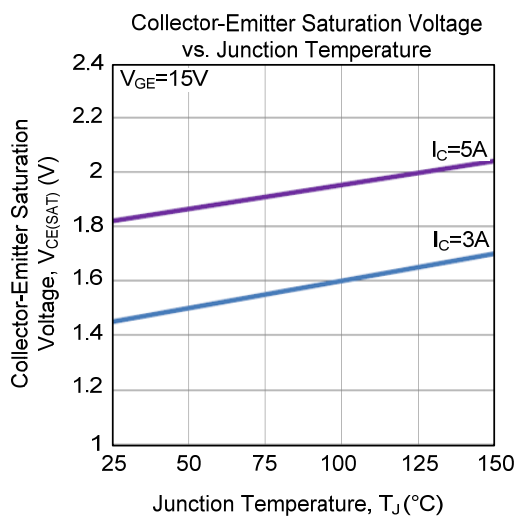
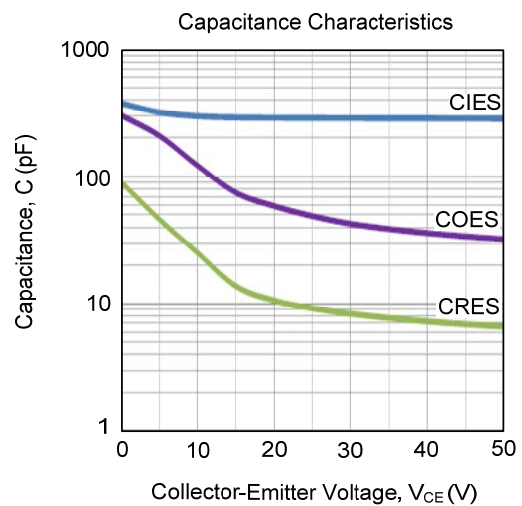
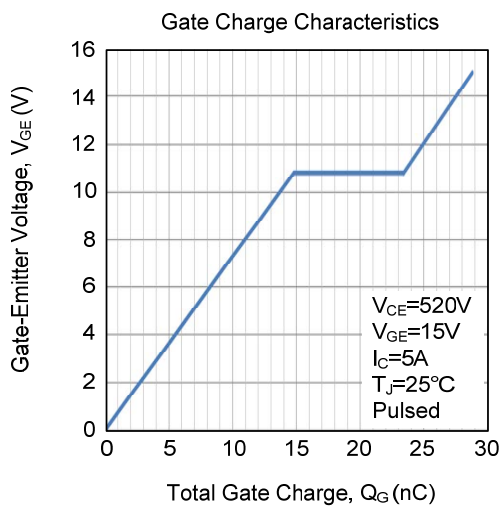
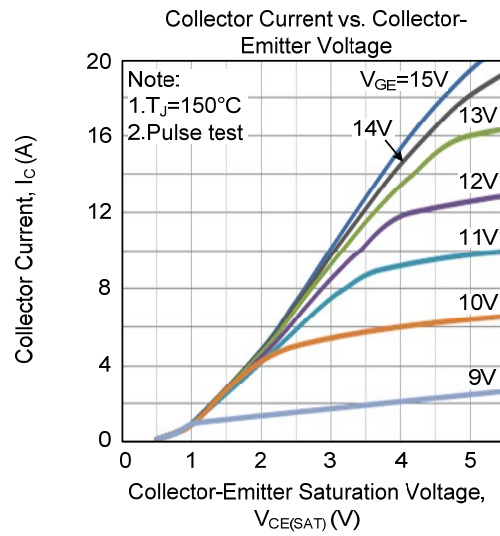
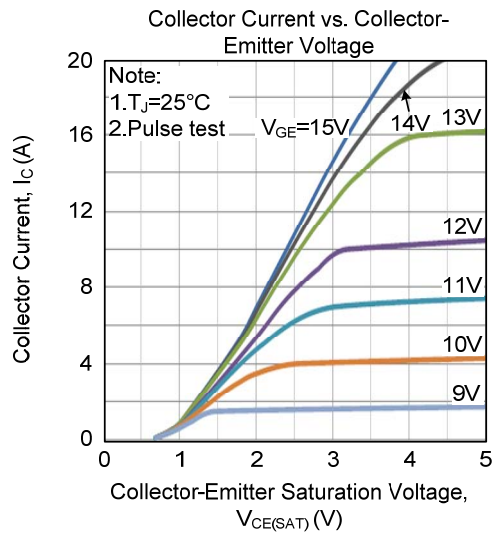
■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	3.47	$^\circ\text{C/W}$

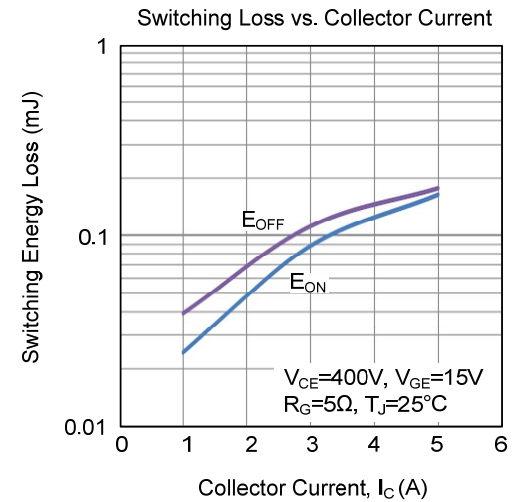
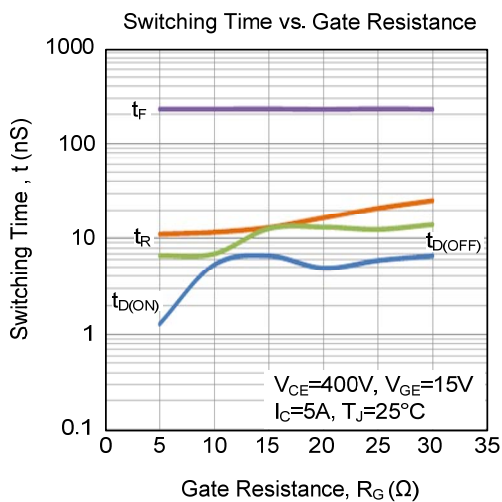
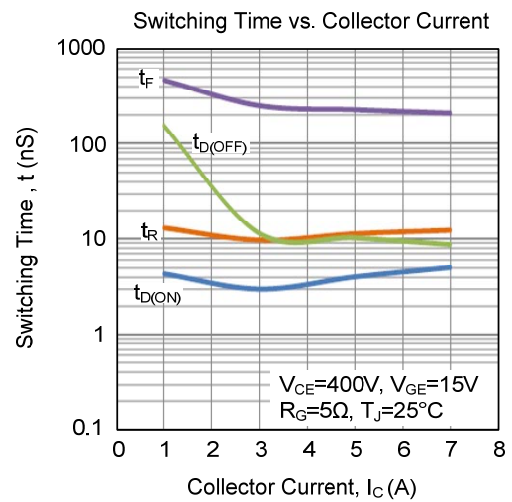
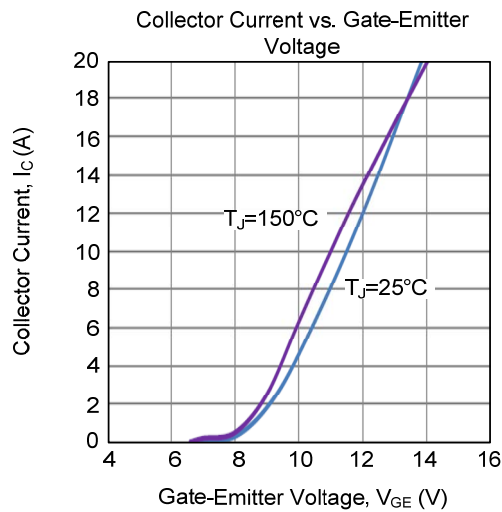
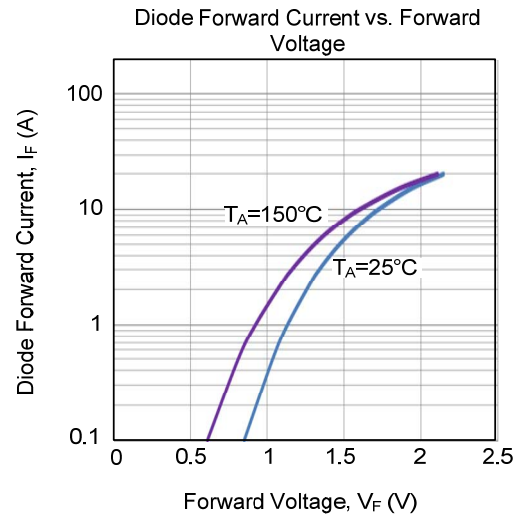
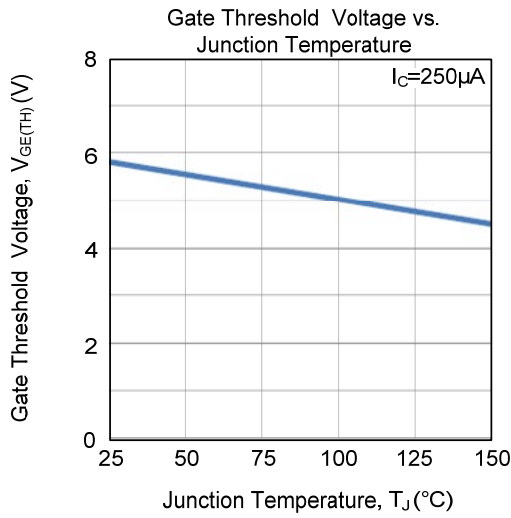
■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off Characteristics						
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C =250μA, V _{GE} =0V	650			V
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V			10	μA
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V			±400	nA
On Characteristics						
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}	4.5		7.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =5.0A, V _{GE} =15V	T _C =25°C	1.76	2.1	V
			T _C =125°C	2.1		V
Dynamic Characteristics						
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz		274		pF
Output Capacitance	C _{OES}			38.8		pF
Reverse Transfer Capacitance	C _{RES}			6		pF
Switching Characteristics						
Total Gate Charge	Q _G	V _{CE} =400V, I _C =5.0A, V _{GE} =15V		28.8		nC
Gate-Emitter Charge	Q _{GE}			14.8		nC
Gate-Collector Charge	Q _{GC}			8.6		nC
Turn-On Delay Time	t _{DON)}	V _{CC} =400V, I _C =5.0A, R _G =25Ω, V _{GE} =0~15V, L=500μH		14.7		ns
Rise Time	t _R			22		ns
Turn-Off Delay Time	t _{DOFF)}			19.2		ns
Fall Time	t _F			218		ns
Turn-On Switching Loss	E _{ON}			0.184		mJ
Turn-Off Switching Loss	E _{OFF}			0.168		mJ
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Forward Voltage Drop	V _F	I _F =3A			2.0	V
Reverse Recovery Time	t _{rr}	I _F =3.0A, dI/dt=100A/μS, V _{CC} =400V		49.2		ns
Reverse Recovery Charge	Q _{rr}			109		nC

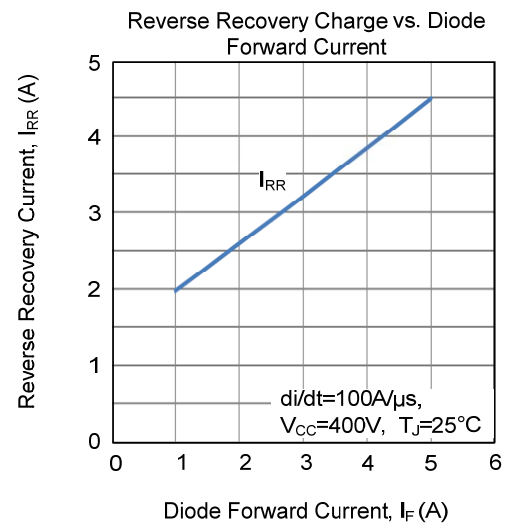
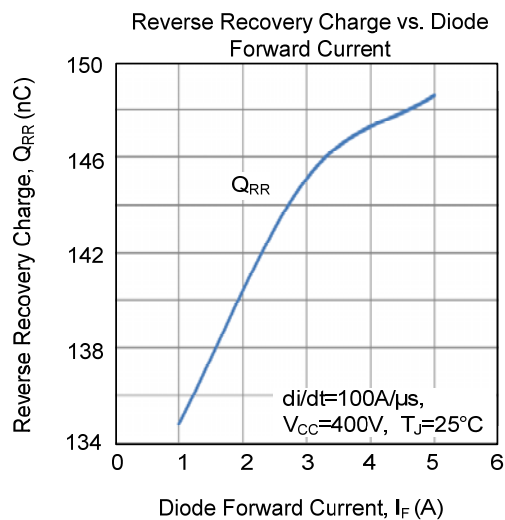
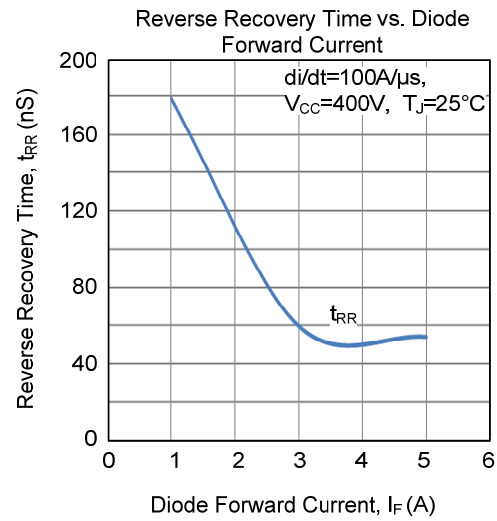
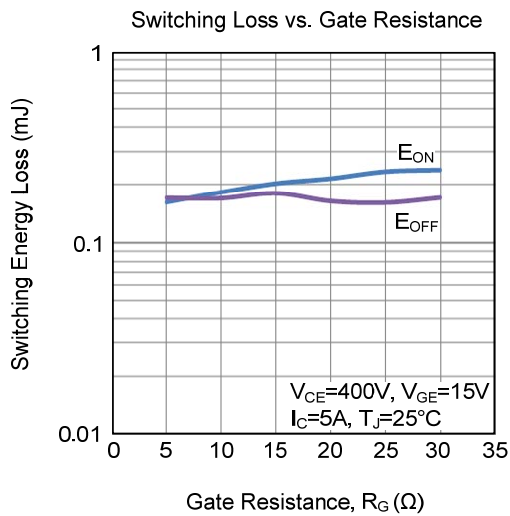
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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