

UNISONIC TECHNOLOGIES CO., LTD

UESD1105

Preliminary

ESD PROTECTION DIODE SINGLE LINE CAN/LIN BUS PROTECTOR

DESCRIPTION

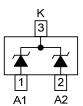
The UTC **UESD1105** has been designed to protect LIN and single lineCAN transceivers from ESD and other harmful transient voltage events. This device provides bidirectional protection for the data line with a single SOT-23 package, giving the system designer a low cost option for improving system reliability and meeting stringent EMI requirements.

FEATURES

* Unidirectional device

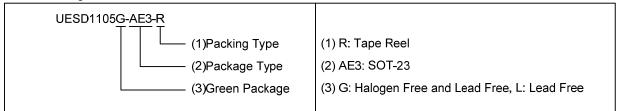
- * Low Reverse Leakage Current (< 100 nA)
- * 300W peak pulse power (8/20µs)

SYMBOL

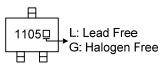


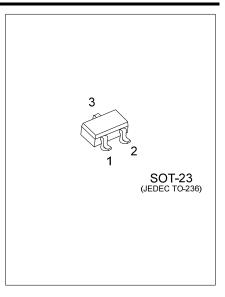
ORDERING INFORMATION

Ordering Number		Dealisera	Pin Assignment			Deelvine	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UESD1105L-AE3-R	UESD1105G-AE3-R	SOT-23	A1	A2	K	Tape Reel	
Note: Pin Assignment: A: Anode K: Cathode							



MARKING





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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
ESD Discharge	UEC61000_4_2	Air Discharge	V	±30	kV
		Contact Discharge	Vesd	±30	kV
Peak Pulse Current		t _p =8/20μs	IPP	8.0 (Note 2)	А
Peak Pulse Power	IEC61000-4-5		P _{PP}	350	W
Operating Junction Temperature		TJ	-55 ~ +150	°C	
Operating Temperature		T _{OPR}	-40 ~ +125	°C	
Storage Temperature			T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **ELECTRICAL CHARACTERISTICS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Stand-Off Voltage	VRWM	(Note 1)	24			V
Reverse Breakdown Voltage	VBR	I⊤=1mA (Note 2)	25.7		28.4	V
Reverse Current	IR	V _R =24V			100	nA
Diada conceitores	Cd	V _R =0V, f=1MHz (Anode to GND)		62		pF
Diode capacitance		V _R =0V, f=1MHz (Anode to Anode)		27		pF
	Vol	I _{РРМ} =5А, t _Р =8/20µs			40	V
Clamping Voltage		I _{РРМ} =8А, t _Р =8/20µs			44	V
Dynamic impedance	Rd	I _{РРМ} =8А, t⊵=8/20µs		0.8		Ω

Notes: 1. Surge protection devices are normally selected according to the working peak reverse voltage (VRWM), which should be equal or greater than the DC or continuous peak operating voltage level.

2. VBR is measured at pulse test current IT.

3. Device stressed with 8/20µs exponential decay waveform according to IEC 61000-4-5.



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