UNISONIC TECHNOLOGIES CO., LTD

UTG80N65ND-S

Preliminary

Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

The UTC **UTG80N65ND-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

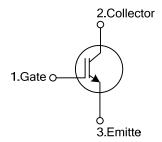
The UTC **UTG80N65ND-S** is suitable for the resonant or soft switching applications.

1 TO-247

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: V_{CE(SAT).Typ.}= 1.6V @ I_C=80A, V_{GE}=15V (T_C =25°C)

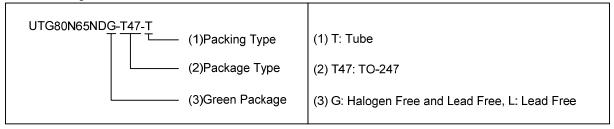
■ SYMBOL



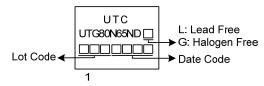
■ ORDERING INFORMATION

Ordering Number		Daalaaaa	Pin Assignment			Da aldin n	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG80N65NDL-T47-T	UTG80N65NDG-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V_{CES}	650	V	
Gate-Emitter Voltage	\/	±20	V	
Transient Gate-emitter voltage (tp < 5 ms)	V _{GES}	±25	V	
Continuous Collector Current		160	Α	
T _C =100°C	Ic	80	Α	
Collector Current Pulsed (Note 1)	I _{CM}	320	Α	
Short Circuit Withstand Time				
V _{GE} = 15V, V _{CC} ≤ 200V				
Allowed number of short circuits < 1000	t _{SC}	10	μs	
Time between short circuits: ≥1.0s				
$T_{\text{VJ}} = 25^{\circ}\text{C}$				
Power Dissipation (T _C =25°C)	P _D	285	W	
Operating Junction Temperature	T_J	-40 ~ +175	°C	
Storage Temperature Range	T _{STG}	-55 ~ +175	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θις	0.44	°C/W

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV _{CES}			650			V
Collector Cut-Off Current	Ices	V _{CE} =650V, V _{GE} =0V				5	μΑ
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nA
On Characteristics							
Gate to Emitter Threshold Voltage	$V_{\text{GE(TH)}}$	I _C =250μA, V _{CE} =V _{GE}				6.5	V
Collector to Emitter Seturation Voltage	\/	I _C =80A, V _{GE} =15V	T _C =25°C		1.6	2.1	V
Collector to Emitter Saturation Voltage	VCE(SAT)		T _C =125°C		1.8		V
Dynamic Characteristics							
Input Capacitance	CIES	V _{CE} =25V, V _{GE} =0V, f=1MHz			5270		pF
Output Capacitance	C _{OES}				280		pF
Reverse Transfer Capacitance	C _{RES}				84		pF
Switching Characteristics	_						
Total Gate Charge	Q_G	V _{CE} =520V, I _C =80A, V _{GE} =15V			221		nC
Gate-Emitter Charge	Q_GE				45		nC
Gate-Collector Charge	Q_{GC}				96		nC
Turn-On Delay Time	t _{DON)}				32		ns
Rise Time	t_R	V _{CC} =400V, I _C =80A, R _G =5Ω, V _{GE} =0~15V, L=500μH			89		ns
Turn-Off Delay Time	t _{DOFF)}				176		ns
Fall Time	t_{F}				50		ns
Turn-On Switching Loss	Eon				4.15		mJ
Turn-Off Switching Loss	E _{OFF}				2.94		mJ

^{2.} Pulse width limited by maximum junction temperature.

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