



UTG75N65ND-S

Preliminary

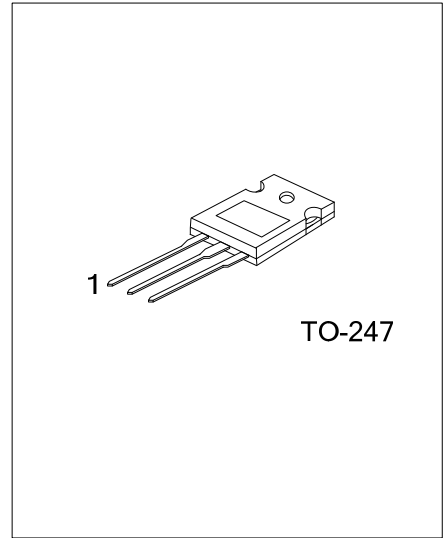
Insulated Gate Bipolar Transistor

650V TRENCH GATE FIELD-STOP IGBT

DESCRIPTION

The UTC **UTG75N65ND-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

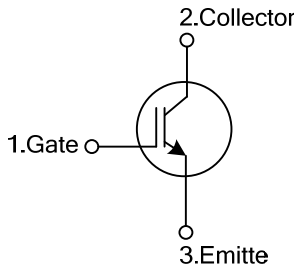
The UTC **UTG75N65ND-S** is suitable for the resonant or soft switching applications.



FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT),Typ.}=1.65V @ I_C=75A, V_{GE}=15V$ ($T_C = 25^\circ C$)

SYMBOL



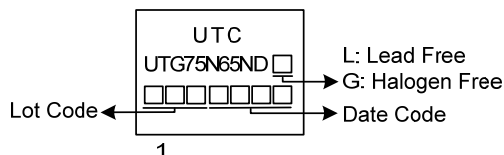
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTG75N65NDL-T47-R	UTG75N65NDG-T47-R	TO-247	G	C	E	Tape Reel

Note: Pin Assignment: G: Gate C: Collector E: Emitter

<p>UTG75N65NDG-T47-R</p>	<p>(1) R: Tape Reel</p> <p>(2) T47: TO-247</p> <p>(3) G: Halogen Free and Lead Free L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V _{CES}	650	V	
Gate-Emitter Voltage	V _{GES}	±20	V	
Transient Gate-emitter voltage (t _p < 5 ms)		±25	V	
Continuous Collector Current	I _C	T _C =25°C	150	A
		T _C =100°C	75	A
Collector Current Pulsed (Note 1)	I _{CM}	300	A	
Short Circuit Withstand Time V _{GE} = 15V, V _{CC} ≤ 200V Allowed number of short circuits < 1000 Time between short circuits: ≥1.0s T _{VJ} = 25°C	t _{sc}	3	μs	
Power Dissipation (T _C =25°C)	P _D	310	W	
Operating Junction Temperature	T _J	-40 ~ +175	°C	
Storage Temperature Range	T _{STG}	-55 ~ +175	°C	

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Absolute maximum ratings are those values beyond which the device could be permanently damaged.

2. Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ _{JC}	0.4	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off Characteristics						
Collector-Emitter Breakdown Voltage	BV _{CES}		650			V
Collector Cut-Off Current	I _{CES}	V _{CE} =650V, V _{GE} =0V			5	μA
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V			±400	nA
On Characteristics						
Gate to Emitter Threshold Voltage	V _{GE(TH)}	I _C =250μA, V _{CE} =V _{GE}	4.5		6.5	V
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _C =75A, V _{GE} =15V	T _C =25°C	1.65	2.1	V
			T _C =125°C	2.0		V
Dynamic Characteristics						
Input Capacitance	C _{IES}	V _{CE} =25V, V _{GE} =0V, f=1MHz		4740		pF
Output Capacitance	C _{OES}			338		pF
Reverse Transfer Capacitance	C _{RES}			67.7		pF
Switching Characteristics						
Total Gate Charge	Q _G	V _{CE} =520V, I _C =75A, V _{GE} =15V		194.5		nC
Gate-Emitter Charge	Q _{GE}			41.3		nC
Gate-Collector Charge	Q _{GC}			98.6		nC
Turn-On Delay Time	t _{DON}	V _{CC} =400V, I _C =75A, R _G =10Ω, V _{GE} =0~15V, L=100uH		11.8		ns
Rise Time	t _R			22		ns
Turn-Off Delay Time	t _{DOFF}			52.9		ns
Fall Time	t _F			174		ns
Turn-On Switching Loss	E _{ON}			0.881		mJ
Turn-Off Switching Loss	E _{OFF}			0.724		mJ

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