



ULV722

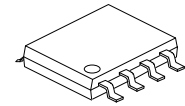
CMOS IC

LOW-POWER RAIL-TO-RAIL I/O CMOS OPERATIONAL AMPLIFIER

DESCRIPTION

The UTC **ULV722** (dual) is a low cost rail to rail input and output OP AMP. The UTC **ULV722** is low voltage, and low power supply current, that can be designed into a wide range of applications. The UTC **ULV722** is designed to provide optimal performance in low voltage and low noise systems. It provides rail-to-rail output swing into heavy loads.

Low quiescent current 1.5mA per channel at 5V can supply 8.5V/ μ s slew rate. The UTC **ULV722** suits for Sensors, Active Filters, Audio, A/D Converters, Test Equipment, Communications, Battery-Powered Instrumentation and photodiode amplifiers, Cellular and Cordless Phones, Laptops and PDAs.



SOP-8

FEATURES

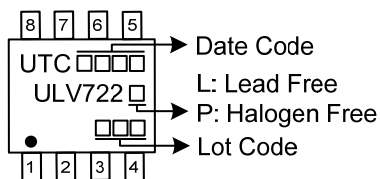
- * Supply Voltage: 2.1V ~ 5.5V
- * Supply Current/Amplifier: 2.1 mA (Max.)
- * Input Offset Voltage: 4mV (Max)
- * Rail-to-Rail Input and Output
- * Slew Rate: 8.5V/ μ s (Typ.)

ORDERING INFORMATION

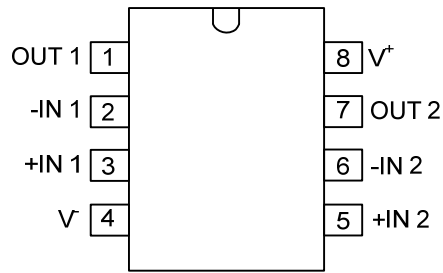
Ordering Number		Package	Packing
Lead Free	Halogen Free		
ULV722L-S08-R	ULV722G-S08-R	SOP-8	Tape Reel

<p>ULV722G-S08-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) S08: SOP-8</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



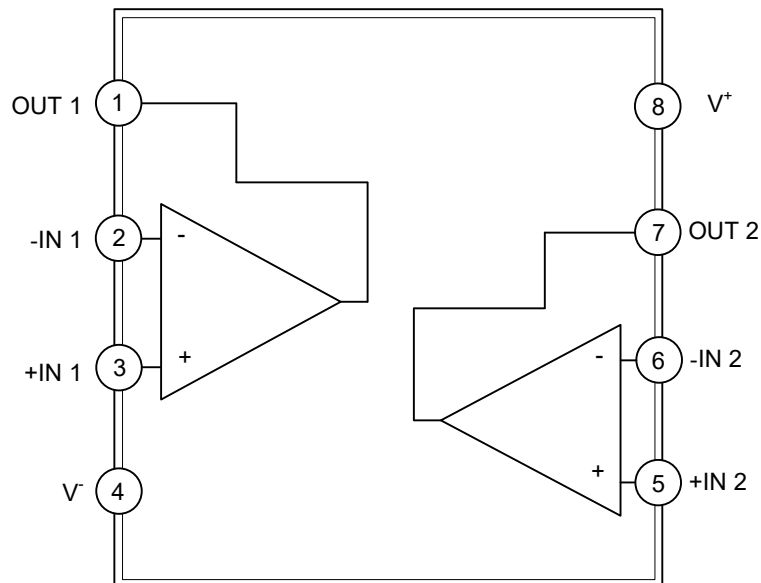
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	OUT 1	Output of 1 AMP
2	-IN 1	Inverting Input of 1 AMP
3	+IN 1	Non-inverting input of 1 AMP
4	V ⁻	Negative power supply
5	+IN 2	Non-inverting input of 2 AMP
6	-IN 2	Inverting input of 2 AMP
7	OUT 2	Output of 2 AMP
8	V ⁺	Positive power supply

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage, V ⁺ to V ⁻	V _S	6	V
Input Common Mode Voltage Range	V _{CM}	V ⁻ -0.3 ~ V ⁺ +0.3	V
Junction Temperature	T _J	+150	°C
Operating Temperature Range	T _{OPR}	-40 ~ +125	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

Over operating free-air temperature range (Unless otherwise specified)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V ⁺ - V ⁻	2.1		5.5	V
Operating Free-Air Temperature	T _{OPR}	-40		+125	°C

■ ELECTRICAL CHARACTERISTICS

(V_S=5V, V_{CM}=V_S/2, R_L=600Ω, T_A=+25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Current/Amplifier	I _Q	I _{OUT} =0		1.5	2.1	mA
Power Supply Rejection Ratio	PSRR	V _S =2.1V ~ 5.5V, V _{CM} < (V ⁻)+0.5V	68	90		dB
Input Offset Voltage	V _{OS}			1.5	4	mV
Input Offset Voltage Drift	ΔV _{OS} /ΔT			2.1		μV/°C
Input Bias Current	I _B			5		pA
Input Offset Current	I _{OS}			5		pA
Common-Mode Voltage Range	V _{CM}		V ⁻ - 0.1		V ⁺ +0.1	V
Common-Mode Rejection Ratio	CMRR	V _S =5.5V, V _{CM} =-0.1V~4V	67	83		dB
		V _S =5.5V, V _{CM} =-0.1V~5.6V	60	75		dB
Large Signal Voltage Gain	A _V	V _O =0.15V~4.85V, R _L =600Ω	80	89		dB
		V _O =0.05V~4.95V, R _L =10kΩ	94	102		dB
Output Voltage	V _O	R _L =600Ω	V _{OH}		V ⁺ -0.14	V
			V _{OL}		0.007	V
		R _L =10kΩ	V _{OH}		V ⁺ -0.04	V
			V _{OL}		0.003	V
Short-Circuit Current	I _{SC}	Sourcing	53	75		mA
		Sinking	53	85		mA
Slew Rate	SR			8.5		V/μs
Gain-Bandwidth Product	GBW			7		MHz
Input Voltage Noise Density	e _n	f = 1kHz		15		nV/ √Hz

■ TYPICAL APPLICATION CIRCUIT

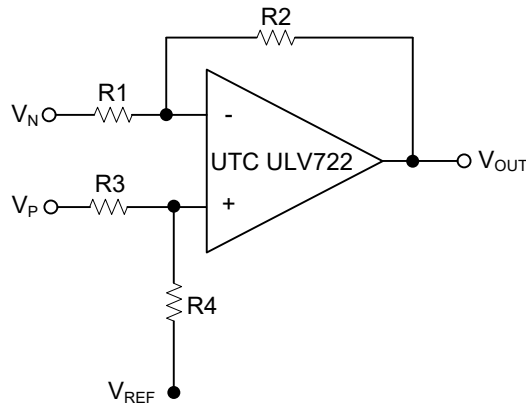


Figure 1. Differential Amplifier

Figure 1 is the differential amplifier. If the resistors ratios are equal ($R4/R3=R2/R1$), then $V_{OUT}=(V_P-V_N) \times R2/R1 + V_{REF}$.

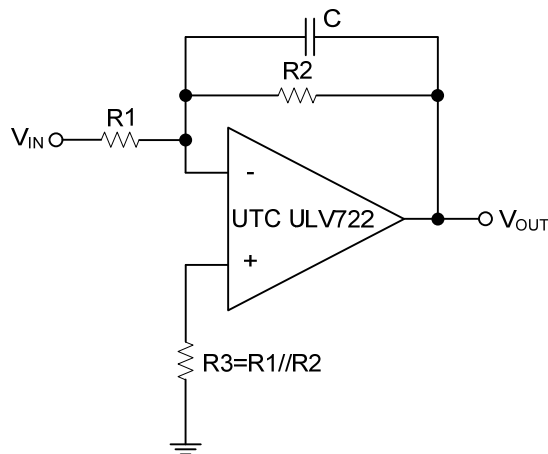
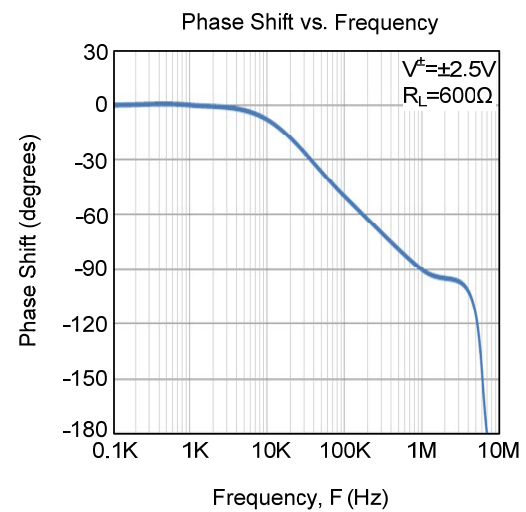
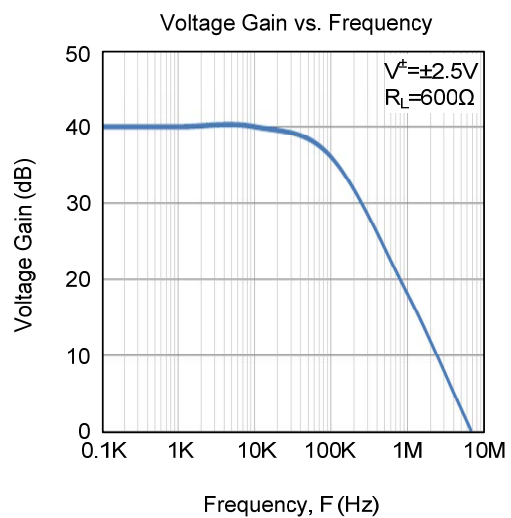
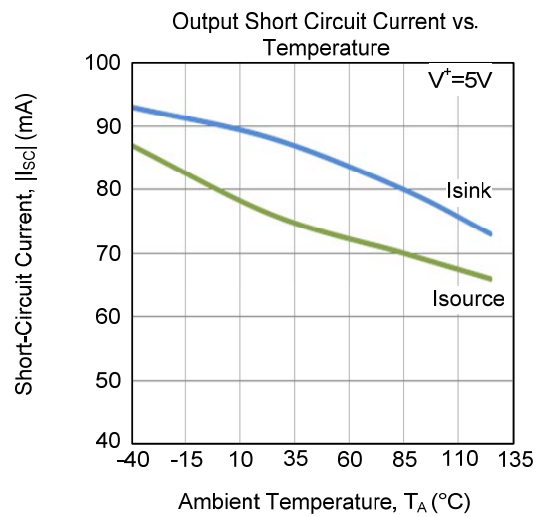
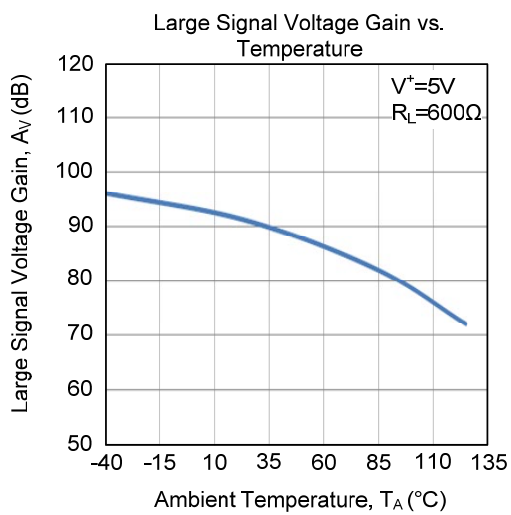
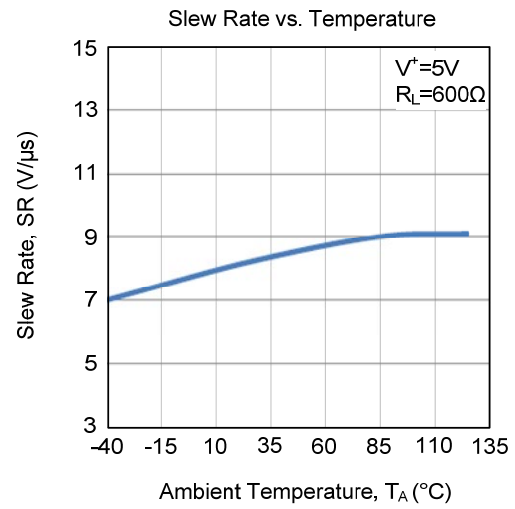
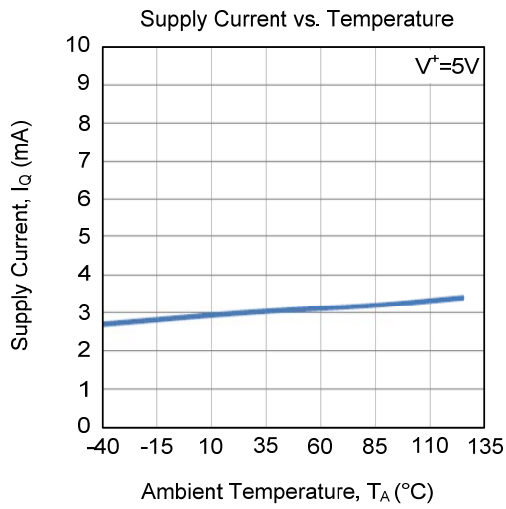


Figure 2. Low Pass Active Filter

Figure 2 is the low pass filter. It's DC gain is $-R2/R1$ and the $-3dB$ corner frequency is $1/2\pi R_2 C$.

TYPICAL CHARACTERISTICS



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