

U74LS279A

Preliminary

LINEAR INTEGRATED CIRCUIT

QUADRUPLE S-R LATCHES

DESCRIPTION

The UTC **U74LS279A** offers 4 basic $\overline{S}-\overline{R}$ flip-flop latches. Under conventional operation, the $\overline{S}-\overline{R}$ inputs are normally held high. When the \overline{S} input is pulsed low, the Q output will be set high. When \overline{R} is pulsed low, the Q output will be reset low. Normally, the $\overline{S}-\overline{R}$ inputs should not be taken low simultaneously. The Q output will be unpredictable in this condition.



ORDERING INFORMATION

Ordering	Daakaaa	Docking	
Lead Free	Halogen Free	Раскаде	Packing
U74LS279AL-D16-T	U74LS279AG-D16-T	DIP-16	Tube



MARKING



Preliminary

■ PIN CONFIGURATION



FUNCTION TABLE

INF	OUTPUT	
S (Note 1)	Q	
Н	Н	Q_0
L	Н	Н
Н	L	L
L	L	H (Note 2)

Notes: H=high level, L=low level

1. for latches with double S inputs

 Q_0 = the level of Q before the indicated input conditions were established 2. The configuration is nonstable: that is, it may be persist

when the \overline{S} and $\ \overline{R}$ inputs return to their inactive(high) level H= both $\ \overline{S}$ inputs high

L=one or both \overline{S} inputs low

LOGIC SYMBOL





LOGIC DIAGRAM







■ **ABSOLUTE MAXIMUM RATING** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{cc}	7	V
Input Voltage	V _{IN}	7	V
Storage Temperature	T _{STG}	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

RECOMMENDED OPERATING COMDITIONS

PARAMETER	SYMBOL	CONDITIONS	MIN	TPY	MAX	UNIT
Supply Voltage	Vcc		4.75	5	5.25	V
High-Level Input Voltage	VIH		2			V
Low-Level Input Voltage	VIL				0.8	V
High-Level Output Current	I _{ОН}				-0.8	mA
Low-Level Output Current	I _{OL}				16	mA
Pulse duration, low	tw		20			ns
Operating free-air Temperature	Ta		-40		+125	°C

ELECTRICAL CHARACTERISTICS (Unless otherwise specified)

			-	T _A =25°C			-40°C~+125°C		
PARAMETER	STMBOL	TEST CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Input Voltage	V _{IK}	V _{CC} = MIN, I _{IN} =-12mA			-1.5			-1.5	V
High-Level Output Voltage	V _{OH}	V _{CC} = MIN, Vı∟=0.8V, I _{OH} =-0.8mA	2.4	3.4		2.2		3.5	V
Low-Level Output Voltage	V _{OL}	V _{CC} = MIN, V _{IH} =2V, I _{OL} =-16mA		0.2	0.4	-0.1		0.5	V
Input Leakage Current	I _{I(LEAK)}	V _{CC} = MAX, V _{IN} =5.5V			-1.5			-1.5	μA
High-level Input Leakage Current	Ін	V _{CC} = MAX, V _{IN} =2.4V			40			40	μA
Low-level Input Leakage Current	IIL	V _{CC} = MAX, V _{IN} =0.4V			-1.6			-1.6	μA
Continuous V _{CC} or GND Current	Icc	V _{CC} = MAX		18	30		18	30	μA

SWITCHING CHARACTERISTICS (Unless otherwise specified)

		TEST CONDITIONS		T _A =25°C			-40°C~+125°C			
PARAMETER	STMBUL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Propagation delay from input (\overline{S}) to output (Q) $t_{PHL}/t_{PLH} = V_{CC}=5 V, C_{L}=15pF$		D -4000		12	22	1		27	ns	
		$R_{L}=400\Omega$		9	15	1		19	ns	
	【PHL/【PLH	V _{CC} =5 V, C _L =15pF			12	22	1		26	ns
	$R_L = 2K\Omega$		13	21	1		26	ns		
Propagation delay from input $(\overline{\mathbf{P}})$ to	tou	V 5 V. C 15pE	R _L =400Ω		15	27	1		30	ns
output (Q)	PHL	v _{CC} −o v, CL−10pi	R _L =2KΩ		15	27	1		30	ns

■ OPERATING CHARACTERISTICS (T_A=25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Capacitance	CIN	V_{CCA} = V_{CCB} =3.3V, V_{IN} =3.3V or GND		2.5		pF
Input/Output Capacitance	C _{I/O}	V_{CCA} = V_{CCB} =3.3V, V_{IN} =3.3V or GND		5		pF



TEST CIRCUIT AND WAVEFORMS



Equivalent of All Inputs



Typical of All Outputs

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