# UNISONIC TECHNOLOGIES CO., LTD

UTG50N120-S

**Preliminary** 

Insulated Gate Bipolar Transistor

# 1200V TRENCH GATE FIELD-STOP IGBT

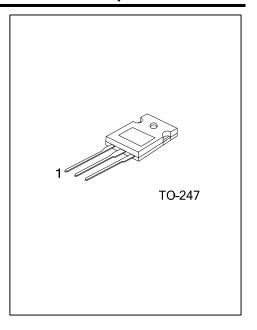
#### **■** DESCRIPTION

The UTC **UTG50N120-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

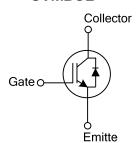
The UTC **UTG50N120-S** is suitable for the resonant or soft switching applications.

### **■ FEATURES**

- \* High switching speed
- \* High avalanche ruggedness
- \* Low saturation voltage:  $V_{CE(SAT).Typ.}$ =1.45V @  $I_C$ =50A,  $V_{GE}$ =15V ( $T_C$  =25°C)



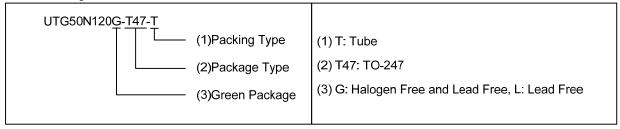
#### ■ SYMBOL



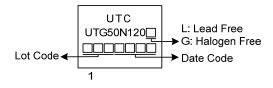
### ORDERING INFORMATION

Ordering Number		Daalaasa	Pin Assignment			Da alsia si	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG50N120L-T47-T	UTG50N120G-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



### ■ MARKING



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### ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		$V_{CES}$	1200	V
Gate-Emitter Voltage		\/	±20	V
Transient Gate-emitter voltage (tp < 5 ms)		$V_{GES}$	±25	V
Continuous Collector Current	T <sub>C</sub> =25°C	I <sub>C</sub>	100	Α
	T <sub>C</sub> =100°C		50	Α
Collector Current Pulsed (Note 1)		I <sub>CM</sub>	200	Α
Diode Forward Current	T <sub>C</sub> =25°C	l <sub>F</sub>	100	Α
	T <sub>C</sub> =100°C		50	Α
Short Circuit Withstand Time $V_{GE} = 15V, V_{CC} \le 200V$		tsc		
Allowed number of short circuits < 1000			10	μs
Time between short circuits: ≥1.0s  T <sub>VJ</sub> = 25°C				
Power Dissipation (T <sub>C</sub> =25°C)		$P_{D}$	285	W
Operating Junction Temperature		$T_J$	-40 ~ +175	°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +175	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	$\theta_{JC}$	0.44	°C/W

<sup>2.</sup> Pulse width limited by maximum junction temperature.

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub>=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDIT	MIN	TYP	MAX	UNIT	
Off Characteristics							
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>			1200			V
Collector Cut-Off Current	I <sub>CES</sub>	V <sub>CE</sub> =1200V, V <sub>GE</sub> =0V				5	μA
G-E Leakage Current	I <sub>GES</sub>	V <sub>CE</sub> =0V, V <sub>GE</sub> =±20V				±100	nA
On Characteristics	•			•		•	
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I <sub>C</sub> =250μA, V <sub>CE</sub> =V <sub>GE</sub>		2.5		6.5	V
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =50A, V <sub>GE</sub> =15V	T <sub>C</sub> =25°C		1.45	1.7	V
	V CE(SAT)	10-30A, VGE-13V	T <sub>C</sub> =125°C		1.8		V
Dynamic Characteristics	+	1					
Input Capacitance	C <sub>IES</sub>	_		3620		pF	
Output Capacitance	C <sub>OES</sub>	V <sub>CE</sub> =25V, V <sub>GE</sub> =0V, f=1		227		pF	
Reverse Transfer Capacitance	C <sub>RES</sub>			152		pF	
Switching Characteristics							
Total Gate Charge	$Q_G$				327		nC
Gate-Emitter Charge	$Q_GE$	V <sub>CE</sub> =600V, I <sub>C</sub> =50A, V <sub>GE</sub> =15V			26		nC
Gate-Collector Charge	$Q_{GC}$				220		nC
Turn-On Delay Time	t <sub>DON)</sub>				21.2		ns
Rise Time	t <sub>R</sub>				30.9		ns
Turn-Off Delay Time	t <sub>DOFF)</sub>	$V_{CC}$ =600V, $I_{C}$ =50A, $R_{G}$ =5 $\Omega$ ,			367.5		ns
Fall Time	t <sub>F</sub>	V <sub>GE</sub> =0~15V. L=500uH		206.5		ns	
Turn-On Switching Loss	Eon			2.82		mJ	
Turn-Off Switching Loss	Eoff			4.834		mJ	
SOURCE- DRAIN DIODE RATINGS AND	CHARACTE	RISTICS					
Forward Voltage Drop	V <sub>F</sub>	I <sub>F</sub> =20A				2.0	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =50A, dI/dt=100A/μS, V <sub>CC</sub> =600V			71.6		ns
Reverse Recovery Charge	Q <sub>rr</sub>				4.84		μC

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