# UNISONIC TECHNOLOGIES CO., LTD

10NM120 Power MOSFET

## 10A, 1200V N-CHANNEL SUPER-JUNCTION MOSFET

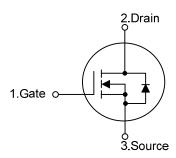
#### **■** DESCRIPTION

The UTC **10NM120** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

#### **■ FEATURES**

- \*  $R_{DS(ON)} \le 0.9 \Omega @ V_{GS} = 10V, I_D = 5.0A$
- \* High Switching Speed

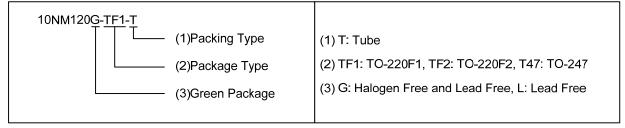




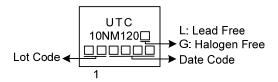
#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
10NM120L-TF1-T	10NM120G-TF1-T	TO-220F1	G	D	S	Tube	
10NM120L-TF2-T	10NM120G-TF2-T	TO-220F2	G	D	S	Tube	
10NM120L-T47-T	10NM120G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



## ■ MARKING



TO-220F1

TO-220F2

TO-247

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## ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	1200	V	
Gate-Source Voltage		$V_{GSS}$	±30	V	
Continuous Drain Current	Continuous	$I_D$	10	Α	
	Pulsed	$I_{DM}$	20	Α	
Single Pulsed Avalanche Energy		E <sub>AS</sub>	270	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.75	V/ns	
Power Dissipation	TO-220F1/TO-220F2	1	30	W	
	TO-247	$P_{D}$	100	W	
Junction Temperature		T <sub>J</sub> +150		°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L=150mH,  $I_{AS}$ =1.9A,  $V_{DD}$ =50V,  $R_{G}$ =25 $\Omega$ , Starting  $T_{J}$  = 25 $^{\circ}$ C
- 4.  $I_{SD} \le 10A$ , di/dt $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$

## **■ THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F1/TO-220F2	0	62.5	°C/W
	TO-247	$\theta_{JA}$	40	°C/W
Junction to Case	TO-220F1/TO-220F2	0	4.16	°C/W
	TO-247	θις	1.25	°C/W

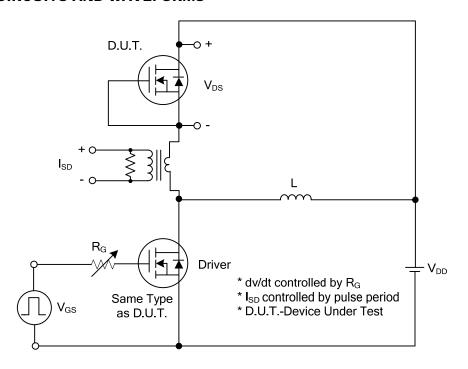
## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	$I_D=250\mu A, V_{GS}=0V$	1200			V	
Drain-Source Leakage Current		$I_{DSS}$	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V			10	μΑ	
Gate-Source Leakage Current	Forward	_	$V_{GS}$ =+30V, $V_{DS}$ =0V			+100	nΑ	
	Reverse	I <sub>GSS</sub>	$V_{GS}$ =-30V, $V_{DS}$ =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$			4.5	V	
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =5.0A			0.9	Ω	
DYNAMIC PARAMETERS		_			-			
Input Capacitance		C <sub>ISS</sub>			1295		pF	
Output Capacitance		Coss	$V_{GS}$ =0V, $V_{DS}$ =50V, f=1.0MHz		66		pF	
Reverse Transfer Capacitance		$C_{RSS}$			2.9		pF	
SWITCHING PARAMETERS								
Total Gate Charge		$Q_G$	\\ -060\\ \\ -10\\   -10\		58		nC	
Gate to Source Charge		$Q_GS$	V <sub>DS</sub> =960V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A (Note 1, 2)		15		nC	
Gate to Drain Charge		$Q_GD$	(Note 1, 2)		20		nC	
Turn-ON Delay Time		$t_{D(ON)}$			15		ns	
Rise Time		$t_R$	$V_{DD}$ =100V, $V_{GS}$ =10V, $I_{D}$ =10A,		22		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$	R <sub>G</sub> =25Ω (Note 1, 2)		152		ns	
Fall-Time		$t_{F}$			38		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Body-Diode Continuous Current		$I_S$				10	Α	
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				20	Α	
Drain-Source Diode Forward Voltage		$V_{SD}$	I <sub>S</sub> =10A, V <sub>GS</sub> =0V			1.4	V	
Body Diode Reverse Recovery Time		t <sub>rr</sub>	$I_S$ =10A, $V_{GS}$ =0V, $dI_F/dt$ =100A/ $\mu$ s		640		ns	
Reverse Recovery Charge		Qrr	(Note 1)		11.5		μC	

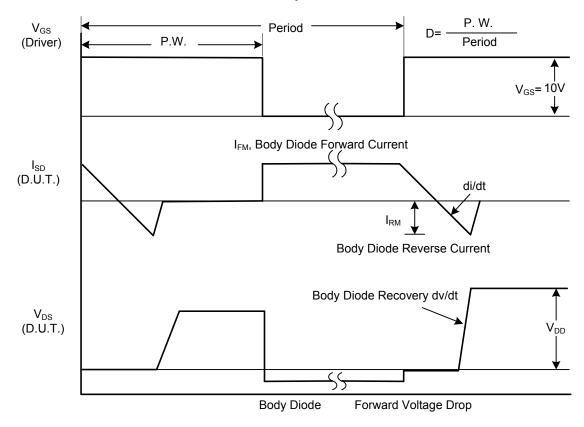
Notes: 1. Pulse Test: Pulse width ≤ 1200µs, Duty cycle ≤ 2%.

<sup>2.</sup> Essentially independent of operating temperature.

### ■ TEST CIRCUITS AND WAVEFORMS

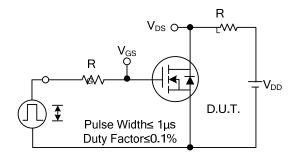


## Peak Diode Recovery dv/dt Test Circuit

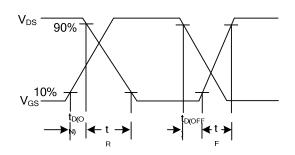


Peak Diode Recovery dv/dt Waveforms

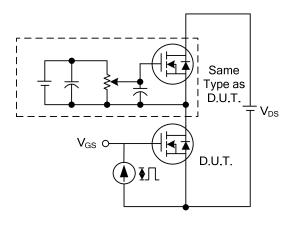
## TEST CIRCUITS AND WAVEFORMS



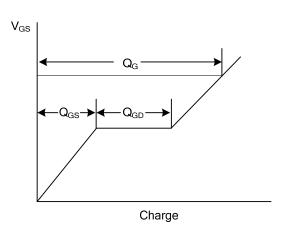
**Switching Test Circuit** 



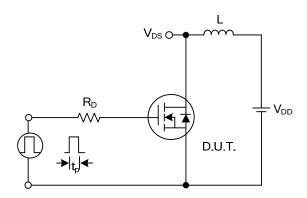
Switching Waveforms



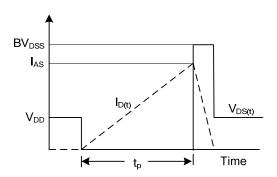
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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