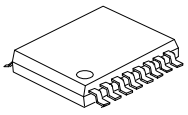
LOW RON 8-CHANNEL ANALOG MULTIPLEXER/DEMULTIPLEXER



TSSOP-16

■ DESCRIPTION

The **U74LVX4051R** is a low-Ron 8-channel analog switch, suitable for use as an analog or digital multiplexer/demultiplexer. In 3V and 5V systems these can achieve high-speed operation with the low power dissipation that is a feature of CMOS.

The **U74LVX4051R** has three digital select inputs (S1 to S3), eight independent inputs/outputs (Y0 to Y7) and a common input/output (Z). All eight switches share an enable input (E). A HIGH on E causes all switches into the high impedance OFF-state, independent of Sn.

For example, if V_{CC}=3V, GND=0V and V_{EE}=-3V, signals between -3V and +3V can be switched from the logical circuit using a signal 3V power supply.

All input pins are equipped with a newly developed input protection circuit that avoids the need for a diode on the plus side (forword side from the input to the V_{CC}). As a result, for example, 5V signals can be permitted on the inputs even when the power supply voltage to the circuits is off. As a result of this input power protection, the **U74LVX4051R** can be used in a variety of applications, including in the system which has two power supplies, and in battery bachup circuits.

■ FEATURES

* Very low ON resistance:
  R_{ON}=0.8Ω(Typ.)(V_{CC}-V_{EE}=3.0V)
  R_{ON}=0.6Ω(Typ.)(V_{CC}-V_{EE}=4.5V)
  R_{ON}=0.5Ω(Typ.)(V_{CC}-V_{EE}=6.0V)
* Wide supply voltage range from 2.5 V to 6 V
* Input level: V_{IL}=0.8V(Max.)(V_{CC}=3V)
  V_{IH}=2.0V(Min.)(V_{CC}=3V)
* Power down protection is provided on all control inputs
* Pin and function compatible with U74HC4051

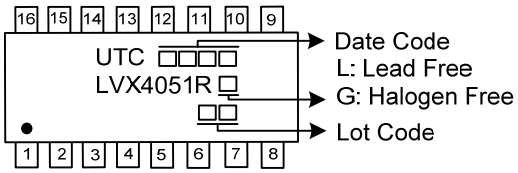
■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74LVX4051RL-P16-R	U74LVX4051RG-P16-R	TSSOP-16	Tape Reel

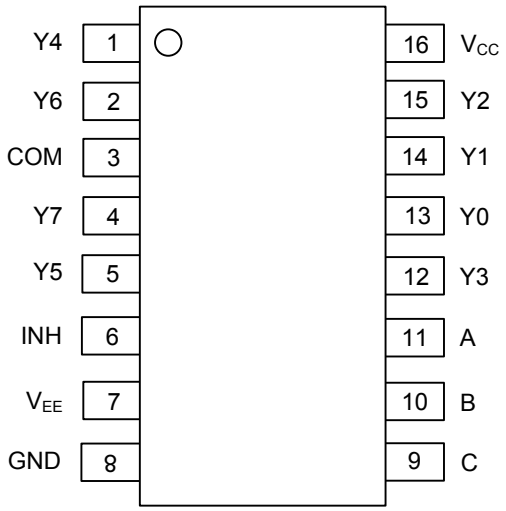
U74LVX4051RG-P16-R
(1)Packing Type
(2)Package Type
(3)Green Package

(1) R: Tape Reel
(2) P16: TSSOP-16
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ PIN CONFIGURATION

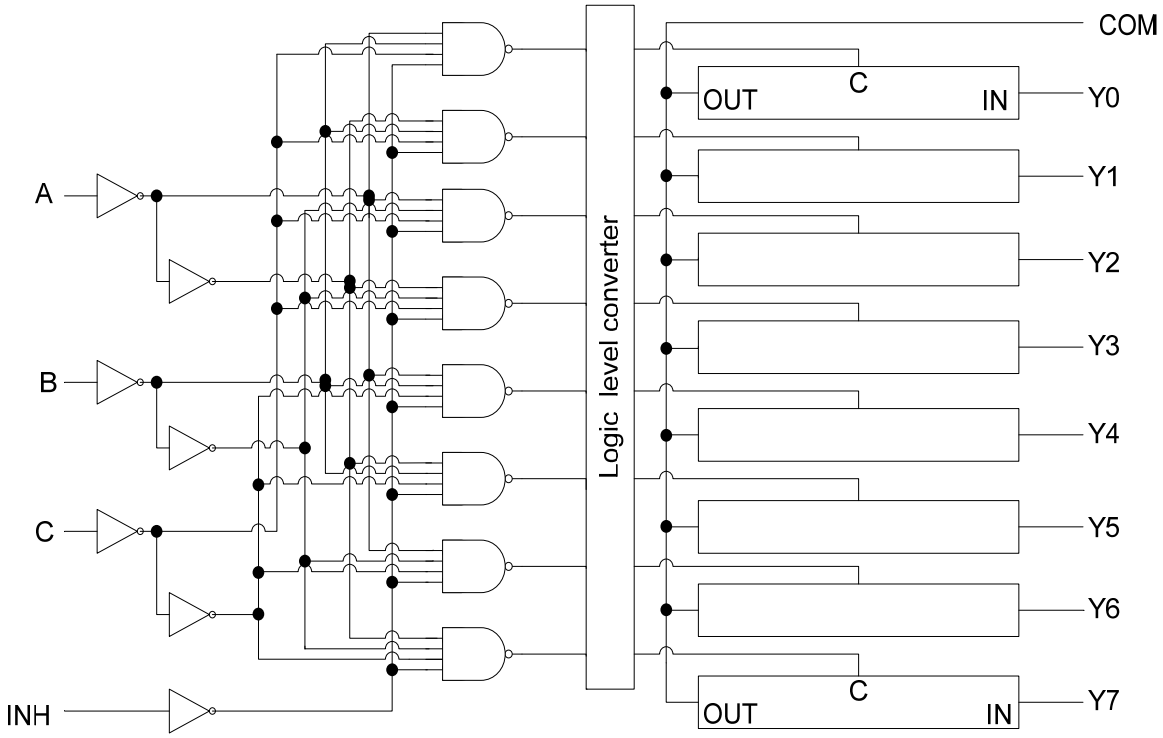


■ FUNCTION TABLE

CONTROL INPUTS				"ON" Channel
INH	C	B	A	
L	L	L	L	Y0
L	L	L	H	Y1
L	L	H	L	Y2
L	L	H	H	Y3
L	H	L	L	Y4
L	H	L	H	Y5
L	H	H	L	Y6
L	H	H	H	Y7
H	X	X	X	None

Note: H: HIGH voltage level; L: LOW voltage level; X: Don't care

■ LOGIC DIAGRAM (Positive Logic)



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Power Supply Voltage	V_{CC}	-0.5 ~ +7.0	V
	V_{CC}~V_{EE}	-0.5 ~ +7.0	
Control Input Voltage	V_{IN}	-0.5 ~ +7.0	V
Switch I/O voltage	$V_{I/O}$	V_{EE}-0.5 ~ V_{CC}+0.5	V
Input diode current	I_{IK}	-50	mA
I/O diode Current	I_{IOK}	±50	mA
Switch through current	I_T	±100	mA
DC V_{CC} or ground current	I_{CC}	±200	mA
Power dissipation	P_D	450	mW
Storage Temperature	T_{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Supply Voltage	V_{CC}		2.5		6.0	V
	V_{EE}		-4.0		0	
	V_{CC}~V_{EE}		2.5		6.0	
Input Voltage	V_{IN}		0		6.0	V
Switch I/O Voltage	$V_{I/O}$		V_{EE}		V_{CC}	V
Input Rise and Fall time	dt/dv	V_{CC}=3.3V±0.3	0		100	ns/V
		V_{CC}=5V±0.5	0		20	
Operating Temperature	T_A		-40		+125	°C

■ DC ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input Voltage	High-level	V_{IH}	V_{CC}=3V		2.0			V
			V_{CC}=4.5V		3.15			
			V_{CC}=6V		4.2			
	Low-level	V_{IL}	V_{CC}=3V				0.8	
			V_{CC}=4.5V				1.35	
			V_{CC}=6V				1.8	
ON Resistance (Peak)		R_{ON}	V_{IN}=V_{IL} or V_{IH}	V_{CC}=3V, V_{EE}=GND		0.8	4.0	Ω
			$V_{I/O}$=V_{CC} to V_{EE}	V_{CC}=4.5V, V_{EE}=GND		0.6	3.0	
			$I_{I/O}$=100mA	V_{CC}=3V, V_{EE}= -3V		0.5	2.0	
ON Resistance (Flatness)		R_{ON}	V_{IN}=V_{IL} or V_{IH}	V_{CC}=3V, V_{EE}=GND		0.6	3.0	Ω
			$V_{I/O}$=V_{CC} to V_{EE}	V_{CC}=4.5V, V_{EE}=GND		0.4	2.0	
			$I_{I/O}$=100mA	V_{CC}=3V, V_{EE}= -3V		0.3	1.5	
Difference of ON Resistance Between Switches		ΔR_{ON}	V_{IN}=V_{IL} or V_{IH}	V_{CC}=3V, V_{EE}=GND		0.4	1.0	Ω
			$V_{I/O}$=V_{CC} to V_{EE}	V_{CC}=4.5V, V_{EE}=GND		0.3	0.6	
			$I_{I/O}$=2mA	V_{CC}=3V, V_{EE}= -3V		0.2	0.4	
Input/Output Leakage Current(switch off)		I_{OFF}	V_{OS}=V_{CC} or GND, V_{IS} =GND or V_{CC}, V_{IN}=V_{IH} OR V_{IL}	V_{CC}=3V, V_{EE}= GND			±0.5	μA
				V_{CC}=3V, V_{EE}= -3V			±1	
Quiescent Supply Current		I_{CC}	V_{IN}= V_{CC} or GND	V_{CC}=3V, V_{EE}= GND			4.0	μA
				V_{CC}=3V, V_{EE}= -3V			8.0	μA
Input/Output Leakage Current (Switch On, Output Open)		I_{IN}	V_{OS}=V_{CC} or GND, V_{IN}=V_{IH} or V_{IL}	V_{CC}=3V, V_{EE}= GND			±0.5	μA
				V_{CC}=3V, V_{EE}= -3V			±1	
Control Input Current		I_{IN}	V_{IN}= V_{CC} or GND	V_{CC}=6V, V_{EE}=GND			±1	μA

■ AC ELECTRICAL CHARACTERISTICS (T_A=25°C, Input t_R/t_F =3ns, GND=0V, C_L=50pF)

See Fig. 1, Fig. 2 and Fig. 3 for test circuit and waveforms.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Phase Difference Between Input and Output	t_{PLH}/t_{PHL}	V_{CC}=3V, V_{EE}=GND			25	ns
		V_{CC}=4.5V, V_{EE}=GND			20	
		V_{CC}=3V, V_{EE}=-3V			15	
Output Enable Time (Note 1)	t_{PZL}/t_{PZH}	V_{CC}=3V, V_{EE}=GND			50	ns
		V_{CC}=4.5V, V_{EE}=GND			45	
		V_{CC}=3V, V_{EE}=-3V			40	
Output Disable Time (Note 1)	t_{PLZ}/t_{PHZ}	V_{CC}=3V, V_{EE}=GND			30	ns
		V_{CC}=4.5V, V_{EE}=GND			25	
		V_{CC}=3V, V_{EE}=-3V			20	
Control Input Capacitance (Note 2)	C_{IN}			5		pF
COMMON Terminal Capacitance (Note 2)	C_{IS}	V_{CC}=3V, V_{EE}=-3V		11		pF
SWITCH Terminal Capacitance (Note 2)	C_{OS}	V_{CC}=3V, V_{EE}=-3V		6		pF
Feedthrough Capacitance (Note 2)	C_{IOS}	V_{CC}=3V, V_{EE}=-3V		3		pF
Power Dissipation Capacitance (Note 3)	C_{PD}	V_{CC}=6V, V_{EE}=GND		14		pF

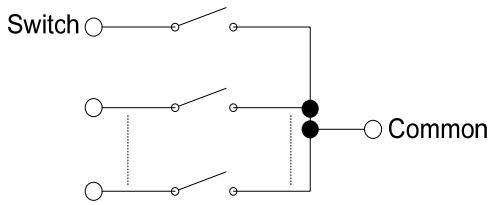
Note: 1. R_L=1k

2. C_{IN}, C_{IS}, C_{OS} and C_{IOS} are guaranteed by the design.
3. CPD is defined as the value of the internal equivalent capacitance of IC which is calculated from the operating current consumption without load.
   Average operating current can be obtained by the equation.
   $I_{CC}(OPR)=C_{PD}\times V_{CC}\times f_{IN}+V_{CC}$

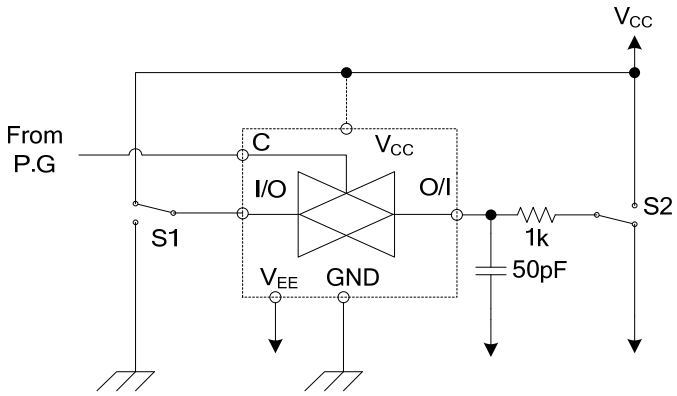
■ ANALOG SWITCH CHARACTERISTICS (GND=0V, T_A=25°C) (Note)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Sine Wave Distortion	THD	R_L=10k, C_L=50pF, f_{IN}=1k	V_{IN}=2Vp-p, V_{CC}=3V, V_{EE}=0V		0.1		%
			V_{IN}=4Vp-p, V_{CC}=4.5V, V_{EE}=0V		0.03		
			V_{IN}=6Vp-p, V_{CC}=3V, V_{EE}=-0.3V		0.05		
Frequency Response (Switch On)	f_{MAX}	Adjust fIN voltage to obtain 0dBm at V_{OS}. Increase fin frequency until dB meter reads -3dB. R_L=50Ω,C_L=10pF, f_{IN}=1MHz, sine wave (Figure 4)	V_{CC}=3V, V_{EE}=0V		20		MHz
			V_{CC}=4.5V, V_{EE}=0V		25		
			V_{CC}=3V, V_{EE}=-3V		33		
Feed Through Attenuation (Switch Off)		V_{IN} is centered at (V_{CC}-V_{EE})/2. Adjust input for 0dBm. R_L=600Ω,C_L=50pF, f_{IN}=1MHz, sine wave (Figure 5)	V_{CC}=3V, V_{EE}=0V		-45		dB
			V_{CC}=4.5V, V_{EE}=0V		-45		
			V_{CC}=3V, V_{EE}=-3V		-45		
		R_L=50Ω,C_L=10pF, f_{IN}=1MHz, sine wave	V_{CC}=3V, V_{EE}=0V		-50		dB
			V_{CC}=4.5V, V_{EE}=0V		-50		
			V_{CC}=3V, V_{EE}=-3V		-50		
Crosstalk (Control input to Signal Output)		R_L=600Ω,C_L=50pF, f_{IN}=1MHz, square wave(t_r=t_f=6ns) (Figure 6)	V_{CC}=3V, V_{EE}=0V		90		mV
			V_{CC}=4.5V, V_{EE}=0V		150		
			V_{CC}=3V, V_{EE}=-3V		150		
Crosstalk (Between Any Switches)		Adjust V_{IN} to obtain 0dBm at input. R_L=600Ω,C_L=50pF, f_{IN}=1MHz, sine wave (Figure 7)	V_{CC}=3V, V_{EE}=0V		-45		dB
			V_{CC}=4.5V, V_{EE}=0V		-45		
			V_{CC}=3V, V_{EE}=-3V		-45		

Note: These characteristics are determined by design of devices.



■ TEST CIRCUIT AND WAVEFORMS



TEST	S1	S2
t_{PLH}/t_{PHL}	pulse	Open
t_{PHZ}/t_{PZH}	V_{CC}	GND
t_{PLZ}/t_{PZL}	GND	V_{CC}

Fig. 1 Load circuitry for switching times.

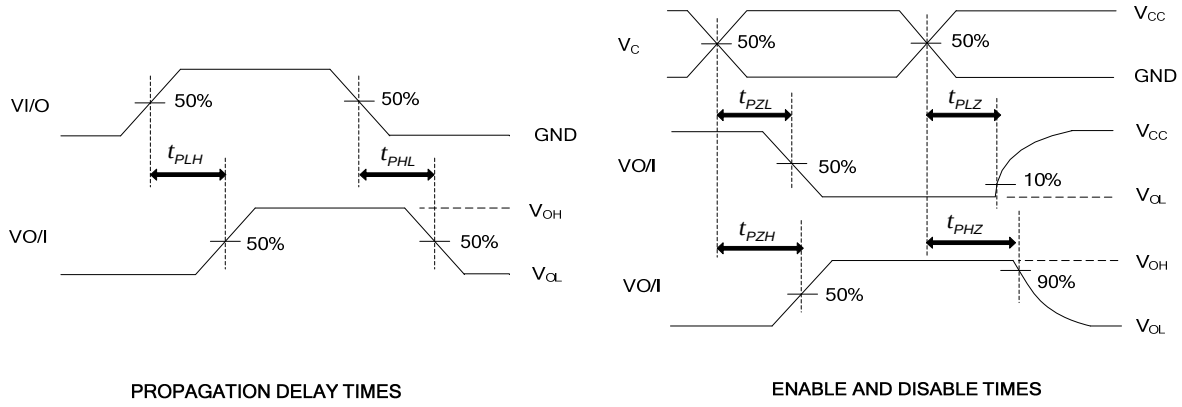


Fig. 2 Propagation delay from input to output and enable, disable times.

■ AC TEST CIRCUIT

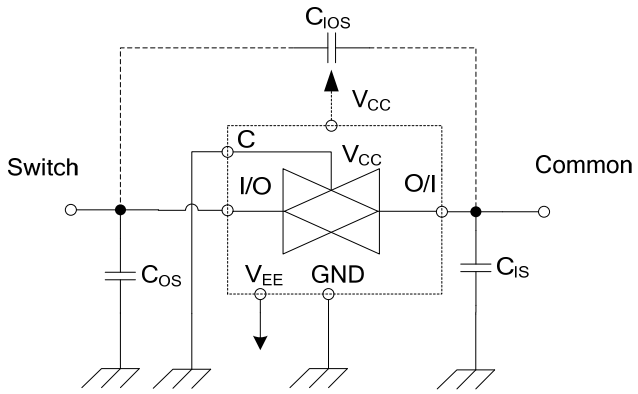


Fig. 3 C_{IOS}, C_{IS}, C_{OS}

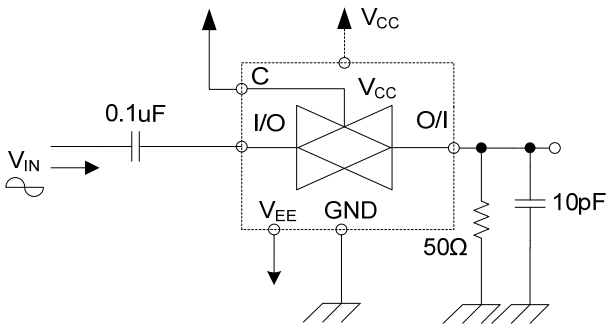


Fig. 4 Frequency Response (switch on)

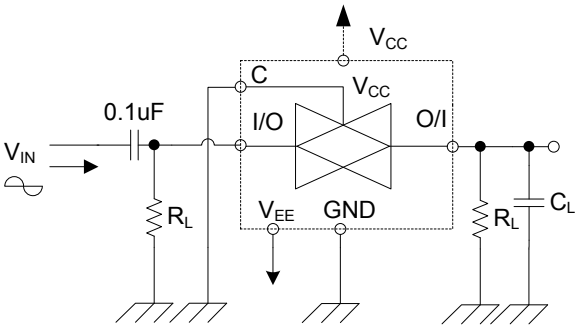


Fig. 5 Feedthrough

■ AC TEST CIRCUIT (Cont.)

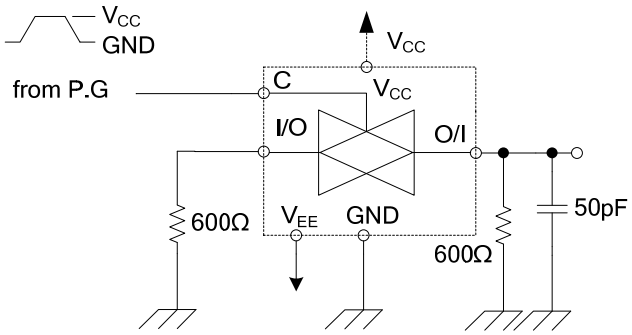


Fig. 6 Cross Talk (control input to output signal)

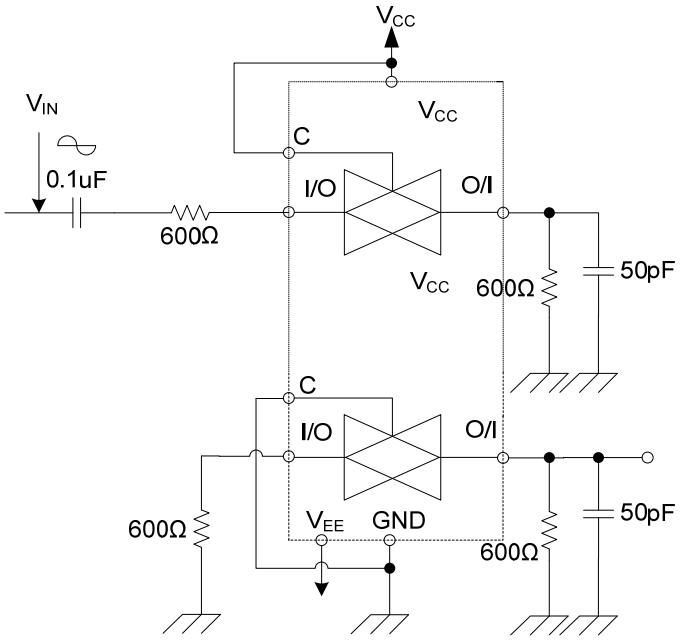


Fig. 7 Cross Talk (between any two switches)