UNISONIC TECHNOLOGIES CO., LTD

UPGE60N60

Preliminary

Insulated Gate Bipolar Transistor

600V, SMPS N-CHANNEL IGBT

■ DESCRIPTION

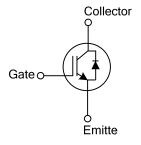
The UTC **UPGE60N60** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

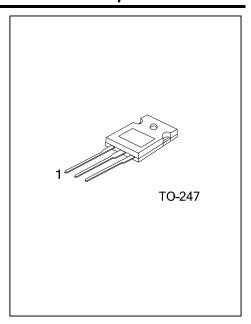
The UTC **UPGE60N60** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * $V_{CE(SAT)} \le 2.3 V @ I_C=60A, V_{GE}=15V$
- * High switching speed
- * High input impedance
- * Low conduction loss

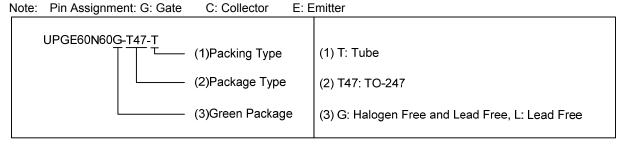
■ SYMBOL



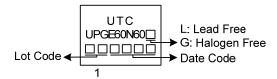


ORDERING INFORMATION

Ordering	Dealtons	Pin Assignment			Daakina		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UPGE60N60L-T47-T	UPGE60N60G-T47-T	TO-247	G	С	E	Tube	



■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage		V _{CES}	600	V	
Gate to Emitter Voltage Continuous		V_{GES}	±20	V	
Continuous Collector Current	T _C =25°C	I _C	120	Α	
	T _C =100°C		60	Α	
Collector Current Pulsed (Note 2)		I _{CM}	240	Α	
Continuous Forward Current	T _C =25°C	- I _F	60	Α	
	T _C =100°C		30	Α	
Forward Current Pulsed		I _{FM}	120	Α	
Peak Diode Recovery dv/dt (Note 3)		dv/dt	5.4	V/ns	
Power Dissipation		P_{D}	270	W	
Junction Temperature		TJ	-55 ~ +150	°C	
Storage Temperature Range		T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. $I_F \le 30A$, di/dt $\le 200A/\mu s$, $V_{CC} \le BV_{CES}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Case	θ_{JC}	0.46	°C/W

■ **ELECTRICAL CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS									
Collector-Emitter Breakdown Voltage	BV _{CES}	I _C =250μA, V _{GE} =0V		600			V		
Collector-Emitter Leakage Current	I _{CES}	V _{CE} =600V, V _{GE} =0V				10	μΑ		
Gate to Emitter Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±400	nΑ		
ON CHARACTERISTICS									
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	I _C =250μA, V _{CE} =V _{GE}		4.0		6.0	V		
O-lla stan Fasittan Ostanatian Mallana		L -COA \/ -45\/	TJ=25°C		1.9	2.3	V		
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	I _C =60A, V _{GE} =15V	TJ=150°C			2.2	V		
DYNAMIC CHARACTERISTICS									
Input Capacitance	C _{IES}	V _{CE} =50V, V _{GE} =0V, f=1MHz			1250		pF		
Output Capacitance	C _{OES}				165		pF		
Reverse Transfer Capacitance	C _{RES}				27		pF		
SWITCHING CHARACTERISTICS									
Total Gate Charge	Q_G	I _C =60A, V _{CE} =100V, V _{GE} =10V			59		nC		
Gate-Emitter Charge	Q_GE				20		nC		
Gate-Collector Charge	Q_{GC}				23		nC		
Current Turn-On Delay Time	t _{D(ON)}	I_{C} =60A, V_{CE} =50V, V_{GE} =15V, R_{G} =10 Ω			10		ns		
Current Rise Time	t_R				20		ns		
Current Turn-Off Delay Time	t _{D(OFF)}				48		ns		
Current Fall Time	t _F				82		ns		
DRAIN-SOURCE DIODE CHARACTER	ISTICS								
Forward Voltage Drop	V_{FM}	I _F =60A				2.6	V		
Reverse Recovery Time	t _{rr}	I _F =30A, dI/dt=100A/μS, V _{CC} =400V			99		ns		
Reverse Recovery Charge	Q _{rr}				130		nC		

Note: Pulse Test: Pulse width \leq 50 μ s.

■ TEST CIRCUIT AND WAVEFORMS

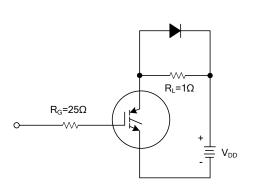


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

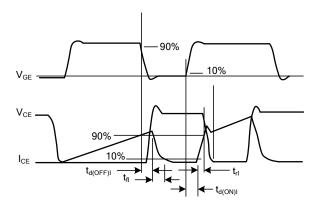


Fig 2. SWITCHING TEST WAVEFORMS

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