



UPG20N65

Insulated Gate Bipolar Transistor

650V, SMPS N-CHANNEL IGBT

■ DESCRIPTION

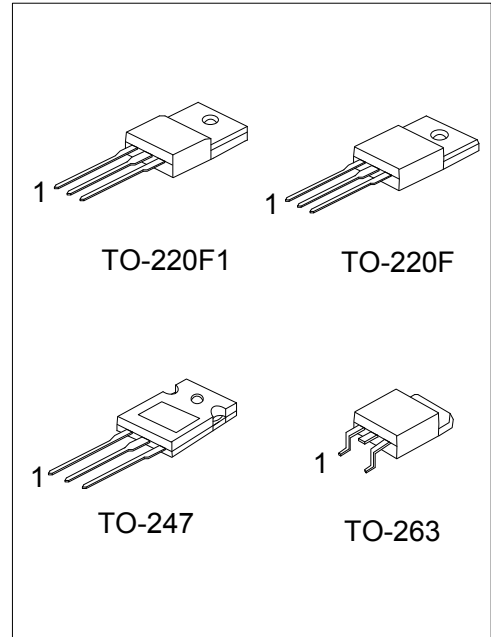
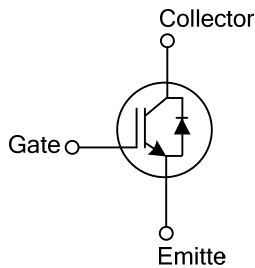
The UTC **UPG20N65** is a N-channel IGBT. it uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC **UPG20N65** is suitable for high voltage switching, high frequency switch mode power supplies.

■ FEATURES

- * $V_{CE(SAT)} \leq 2.6V @ I_C=20A, V_{GE}=15V$
- * High switching speed
- * High input impedance
- * Low conduction loss

■ SYMBOL



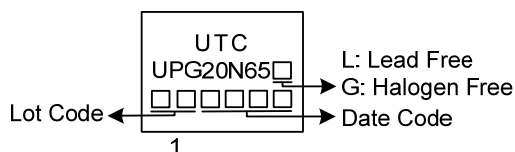
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UPG20N65L-TF1-T	UPG20N65G-TF1-T	TO-220F1	G	C	E	Tube
UPG20N65L-TF3-T	UPG20N65G-TF3-T	TO-220F	G	C	E	Tube
UPG20N65L-TQ2-T	UPG20N65G-TQ2-T	TO-263	G	C	E	Tube
UPG20N65L-TQ2-R	UPG20N65G-TQ2-R	TO-263	G	C	E	Tape Reel
UPG20N65L-T47-T	UPG20N65G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

<p>UPG20N65G-TF1-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TF1: TO-220F1, TF3: TO-220F, TQ2: TO-263, T47: TO-247</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



UPG20N65

Insulated Gate Bipolar Transistor

■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Collector-Emitter Voltage	V_{CES}	650	V	
Gate to Emitter Voltage Continuous	V_{GES}	± 20	V	
Continuous Collector Current	I_C	$T_C=25^\circ\text{C}$	40	A
		$T_C=100^\circ\text{C}$	20	A
Collector Current Pulsed (Note 2)	I_{CM}	80	A	
Peak Diode Recovery dv/dt (Note 3)	dv/dt	5.8	V/ns	
Power Dissipation	P_D	TO-220F	34	W
		TO-220F1		
		TO-263		
		TO-247		
Junction Temperature	T_J	-55 ~ +150	$^\circ\text{C}$	
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $I_F \leq 9.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{CC} \leq BV_{CES}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Case	θ_{JC}	TO-220F	3.67	$^\circ\text{C}/\text{W}$
		TO-220F1		
		TO-263		
		TO-247		
		1.31	$^\circ\text{C}/\text{W}$	
		0.62	$^\circ\text{C}/\text{W}$	

■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Collector-Emitter Breakdown Voltage	BV_{CES}	$I_C=250\mu\text{A}$, $V_{GE}=0\text{V}$	650			V
Collector-Emitter Leakage Current	I_{CES}	$V_{CE}=650\text{V}$, $V_{GE}=0\text{V}$			10	μA
Gate to Emitter Leakage Current	I_{GES}	$V_{CE}=0\text{V}$, $V_{GE}=\pm 20\text{V}$			± 400	nA
ON CHARACTERISTICS						
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	$I_C=250\mu\text{A}$, $V_{CE}=V_{GE}$	4.0		6.0	V
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C=20\text{A}$, $V_{GE}=15\text{V}$	$T_J=25^\circ\text{C}$	2.1	2.6	V
			$T_J=150^\circ\text{C}$	2.5		V
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{IES}	$V_{CE}=25\text{V}$, $V_{GE}=0\text{V}$, $f=1\text{MHz}$		800		pF
Output Capacitance	C_{OES}			100		pF
Reverse Transfer Capacitance	C_{RES}			16		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	$I_C=20\text{A}$, $V_{CE}=100\text{V}$, $V_{GE}=10\text{V}$		38		nC
Gate-Emitter Charge	Q_{GE}			11		nC
Gate-Collector Charge	Q_{GC}			12		nC
Current Turn-On Delay Time	$t_{D(ON)}$	$I_C=20\text{A}$, $V_{CE}=100\text{V}$, $V_{GE}=15\text{V}$, $R_G=10\Omega$		8		ns
Current Rise Time	t_R			17		ns
Current Turn-Off Delay Time	$t_{D(OFF)}$			48		ns
Current Fall Time	t_F			68		ns
DRAIN-SOURCE DIODE CHARACTERISTICS						
Forward Voltage Drop	V_{FM}	$I_F=20\text{A}$			2.1	V
Reverse Recovery Time	t_{rr}	$I_F=20\text{A}$, $di/dt=100\text{A}/\mu\text{s}$, $V_{CC}=100\text{V}$		80		ns
Reverse Recovery Charge	Q_{rr}			120		nC

Note: Pulse Test: Pulse width $\leq 50\mu\text{s}$.



■ TEST CIRCUIT AND WAVEFORMS

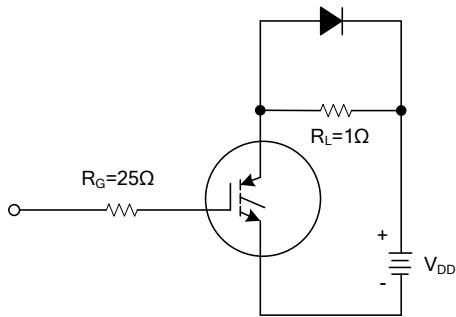


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

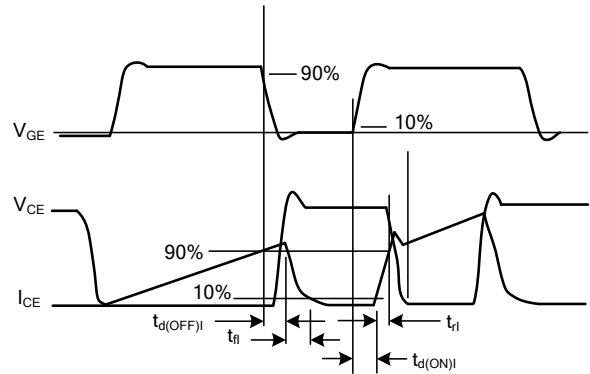
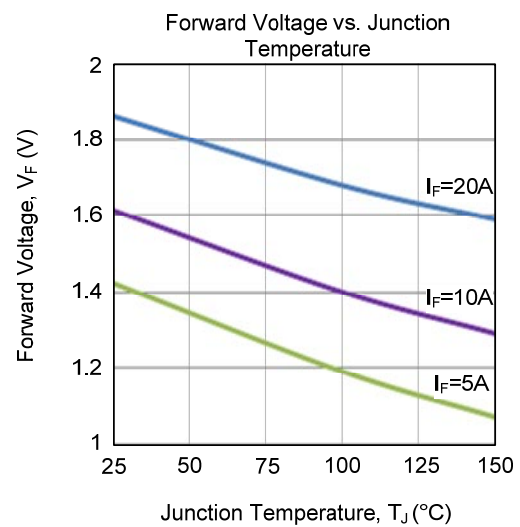
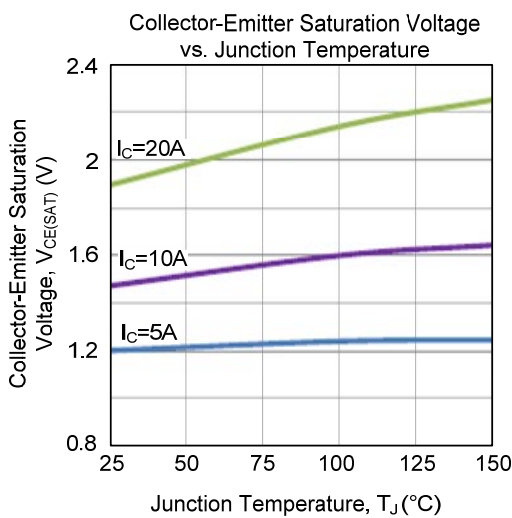
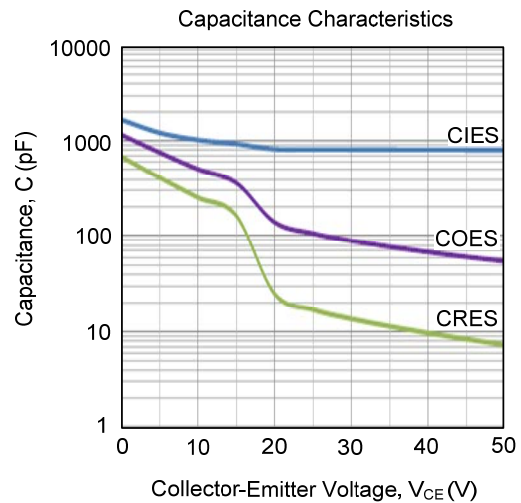
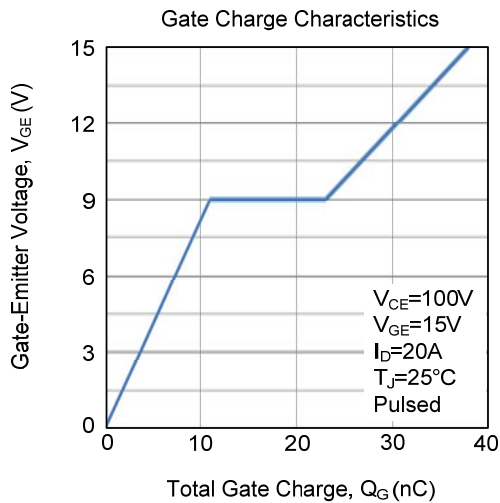
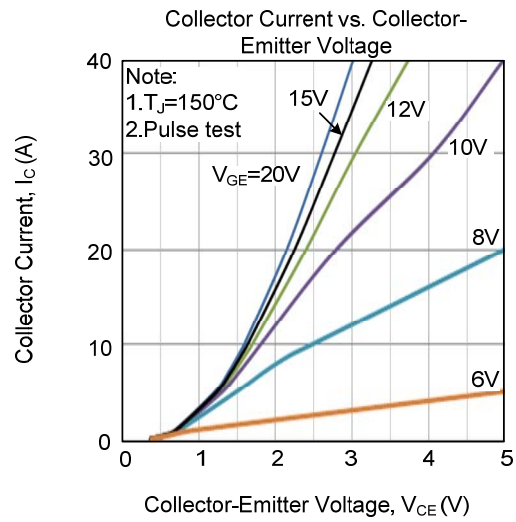
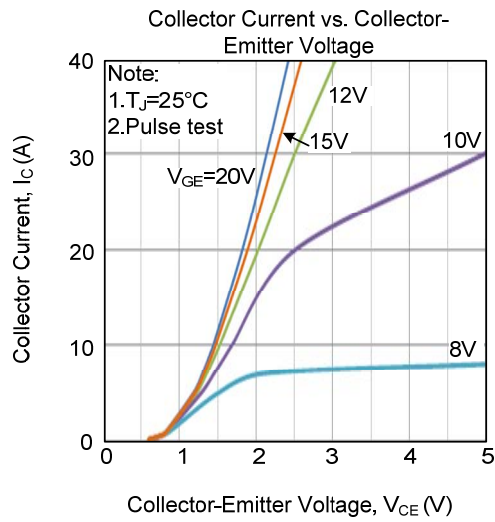
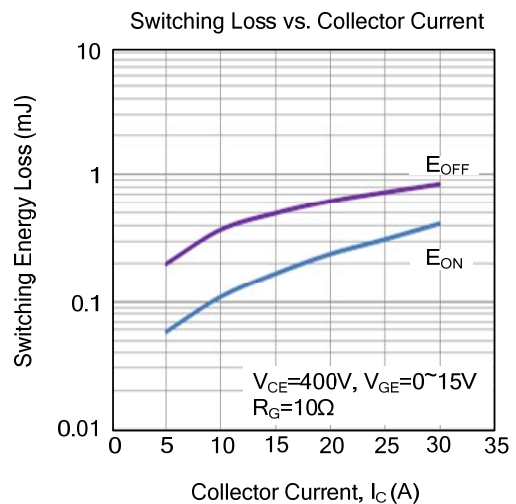
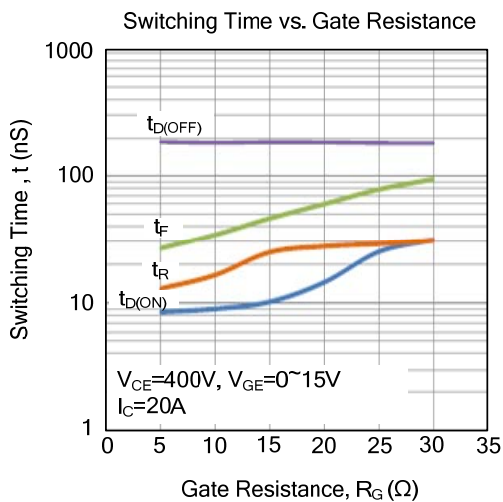
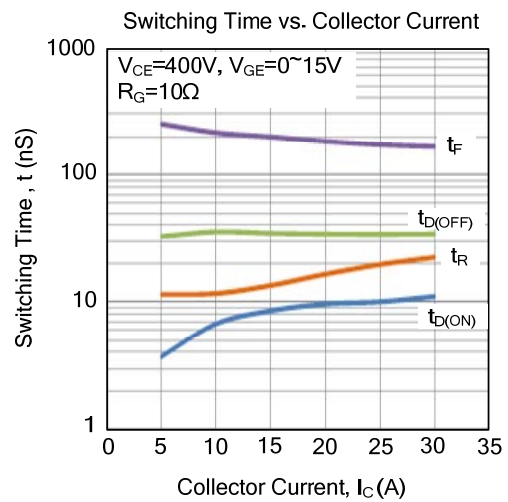
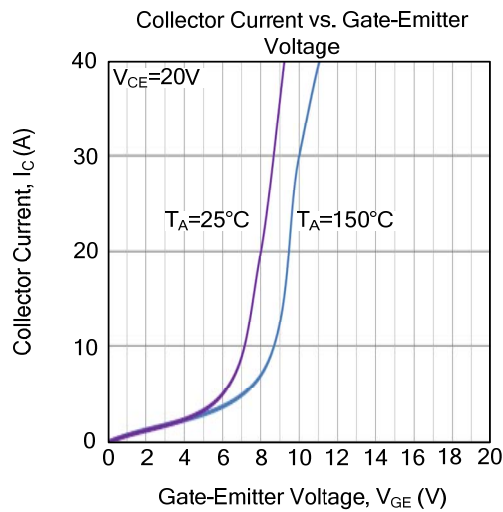
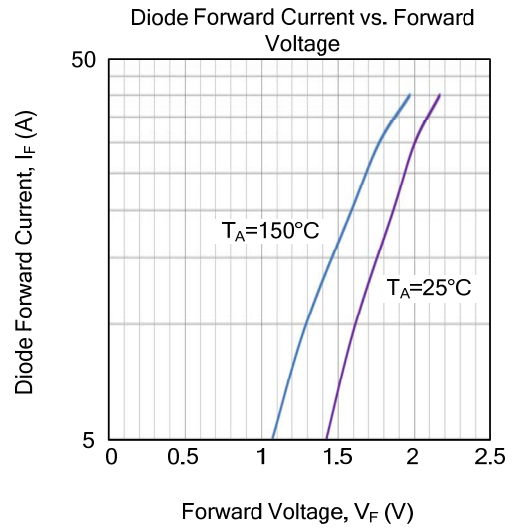
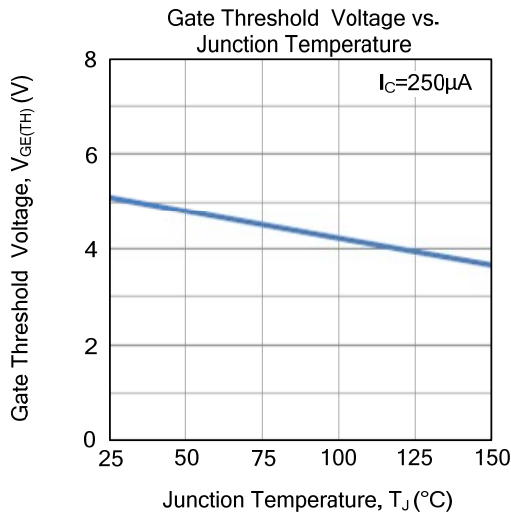


Fig 2. SWITCHING TEST WAVEFORMS

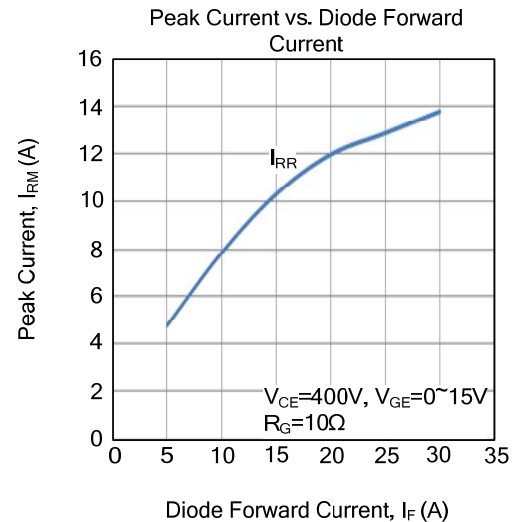
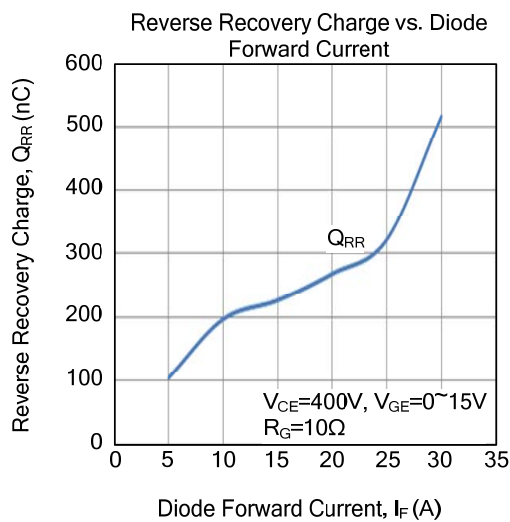
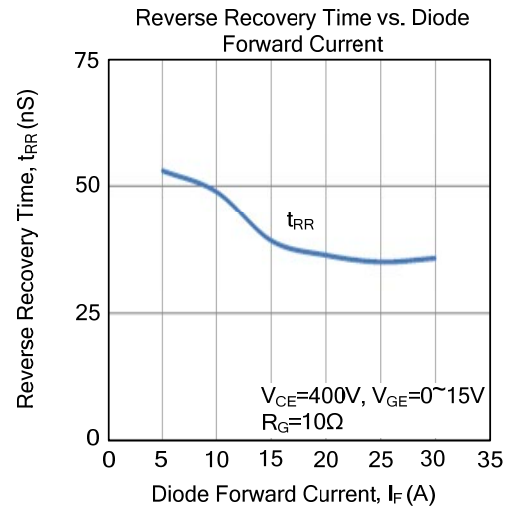
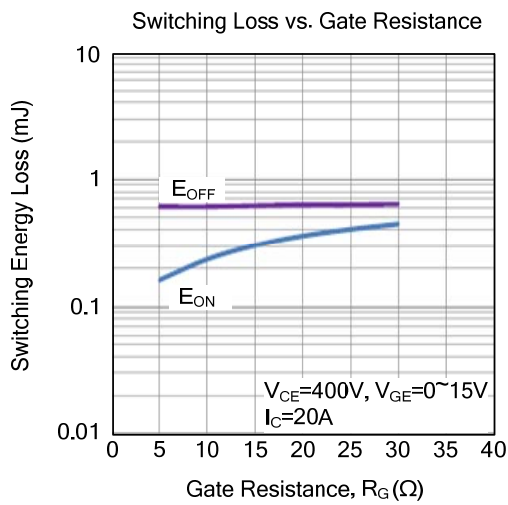
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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