USS40A

**Preliminary** 

LINEAR INTEGRATED CIRCUIT

# BIPOLAR LATCH TYPE HALL EFFECT FOR HIGH-TEMPERATURE OPERATION

### DESCRIPTION

The UTC **USS40A** is a semiconductor integrated circuit utilizing the Hall effect. It designed to operate in the alternating magnetic field especially at low supply voltage and operation over extended temperature ranges to +125°C.

This Hall IC is suitable for application to various kinds of sensors, contact-less switches, such as Speed sensor, Position sensor, Rotation sensor, Contact-less sensor, and Motor control.

### ■ FEATURES

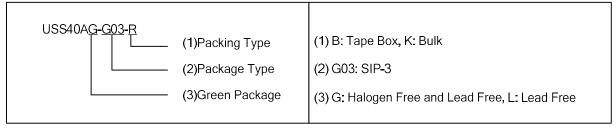
- \* Wide Temperature Operation Range of -30°C ~+125°C
- \* Alternating Magnetic Field Operation
- \* Built-in Protection Diode
- \* TTL and MOS IC are Directly Drivable by the Output
- \* The life is Semi Permanent because it Employs Contact-Less Parts

# SIP-3

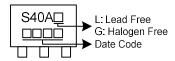
### **■ ORDERING INFORMATION**

Ordering Number		Daakana	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
USS40AL-G03-B	USS40AG-G03-B	SIP-3	- 1	G	0	Tape Box	
USS40AL-G03-K	USS40AG-G03-K	SIP-3	- 1	G	0	Bulk	

Note: Pin Assignment: I: V<sub>CC</sub> O: V<sub>OUT</sub> G: GND

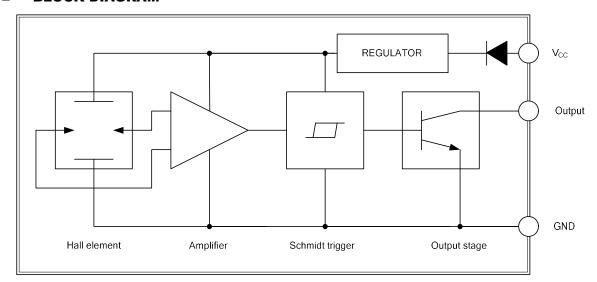


## MARKING



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# **■ BLOCK DIAGRAM**



### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V_{CC}$	28	V
Supply Current	Icc	10	mA
Circuit Current	lo	20	mA
Power Dissipation	$P_{D}$	400	mW
Operating Temperature	T <sub>OPR</sub>	-30 ~ +125	°C
Storage Temperature	T <sub>STG</sub>	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT	
Supply Voltage Range	$V_{DD}$	Operating			28	V	
Law Laval Output Valtage	V <sub>OL</sub>	V <sub>CC</sub> = 16V, I <sub>OUT</sub> =12mA, B > 110 Gauss		0.2	0.4	V	
Low-Level Output Voltage		V <sub>CC</sub> =4.5V, I <sub>OUT</sub> =12mA, B > 110 Gauss		0.3	0.4	V	
Output Leakage Current	I <sub>LEAK</sub>	V <sub>CC</sub> =16V, B=<-110Gauss		1	10	μΑ	
Summit Cumment	Icc	V <sub>CC</sub> =16V		6	10	mA	
Supply Current		V <sub>CC</sub> =4.5V		5.5	10	mA	
Output Switching Time	$T_R$	$V_{CC}$ =16V, R <sub>L</sub> =10KΩ, C <sub>L</sub> =10pF			5	μS	
Output Switching Time	$T_F$	$V_{CC}$ =16V, R <sub>L</sub> =10KΩ, C <sub>L</sub> =10pF			1	μS	
MAGNETIC CHARACTERISTICS							
Operate Point	B <sub>OP</sub>	At T <sub>A</sub> =25°C		45	110	Gauss	
Release Point	$B_RP$	At T <sub>A</sub> =25°C	-110	-45		Gauss	
Hysteresis	B <sub>HYS</sub>	At T <sub>A</sub> =25°C	50	90	220	Gauss	

Notes: 1. Bop=operate point (output turns ON); BRP = release point (output turns OFF); BHYS = hysteresis(Bop - BRP). As used here, negative flux densities are defined as less than zero (algebraic convention). Typical values are at  $T_A=25^{\circ}C$  and Vcc=12V.

<sup>2. 1</sup>mT=10 gauss.

# PACKAGE INFORMATION

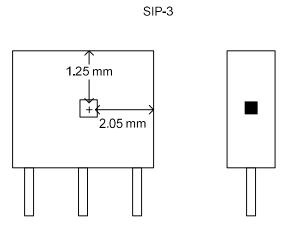


Fig. 1 SENSOR LOCATIONS

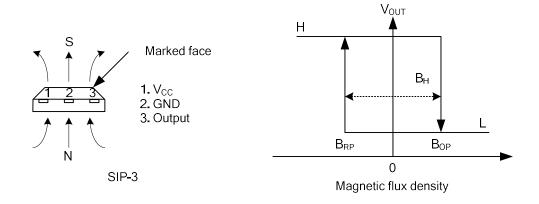
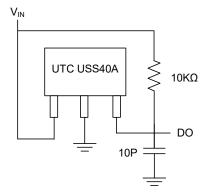
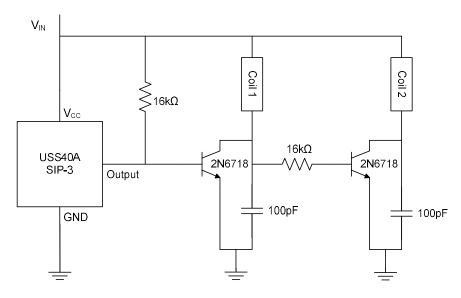


Fig. 2 APPLYING DIRECTION OF MAGNETIC FLUX

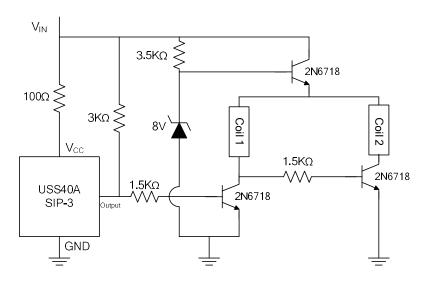
# **■ TEST CIRCUIT**



### **■ TYPICAL APPLICATION CIRCUIT**



FOR DC FAN 1



FOR DC FAN 2

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