



## 4N150-P

Preliminary

Power MOSFET

### 4.0A, 1500V N-CHANNEL POWER MOSFET

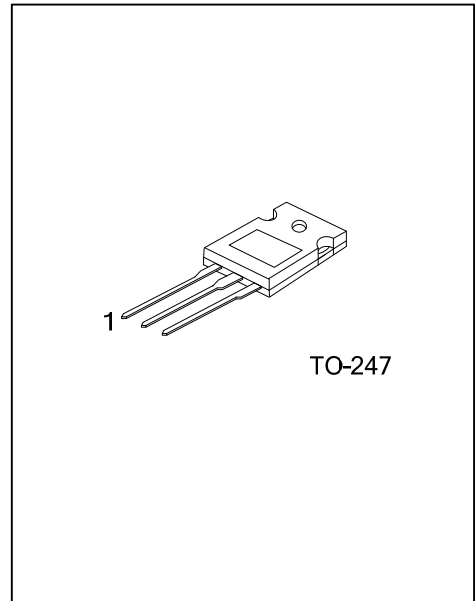
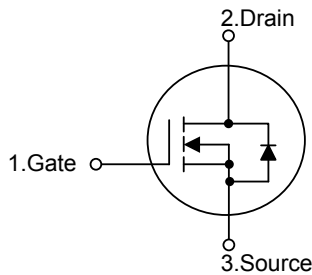
#### DESCRIPTION

The **UTC 4N150-P** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

#### FEATURES

- \*  $R_{DS(ON)} \leq 5.0 \Omega @ V_{GS}=10V, I_D=2.0A$
- \* High Switching Speed

#### SYMBOL



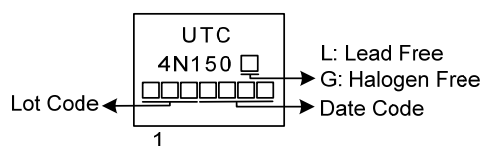
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
4N150L-T47-T	4N150G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>4N150G-T47-T</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) T: Tube</p> <p>(2) T47: TO-247</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	1500	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	4	A
	Pulsed (Note 2)	$I_{DM}$	8	A
Peak Diode Recovery dv/dt (Note 3)		dv/dt	1.9	V/ns
Power Dissipation		$P_D$	155	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $I_{SD} \leq 4.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	50	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	0.8	$^\circ\text{C}/\text{W}$

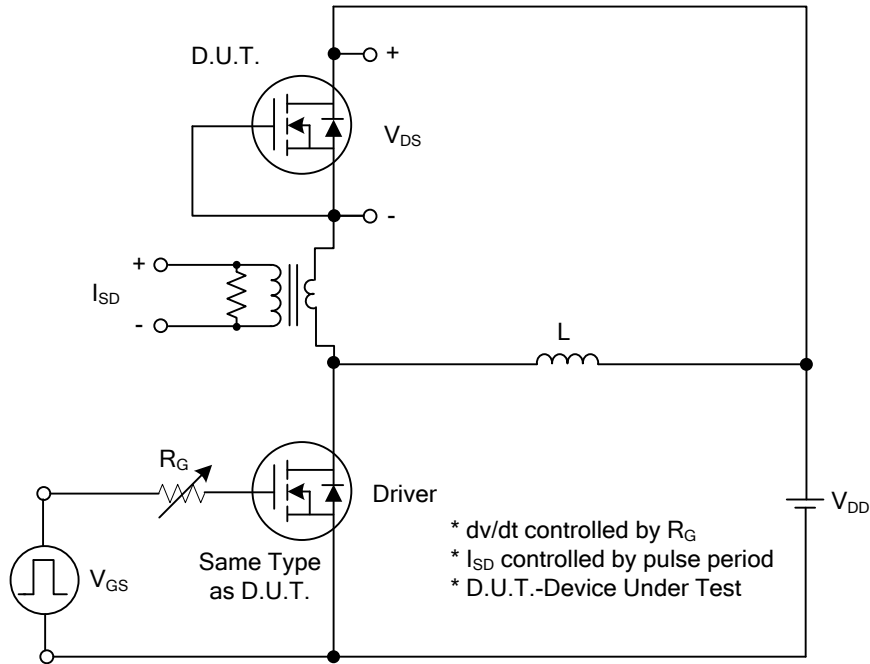
■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>							
Drain-Source Breakdown Voltage		$BV_{DSS}$	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	1500			V
Drain-Source Leakage Current		$I_{DSS}$	$V_{DS}=1500\text{V}$ , $V_{GS}=0\text{V}$			10	$\mu\text{A}$
Gate-Source Leakage Current	Forward	$I_{GSS}$	$V_{GS}=30\text{V}$ , $V_{DS}=0\text{V}$			100	nA
	Reverse					-100	nA
<b>ON CHARACTERISTICS</b>							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	$V_{GS}=10\text{V}$ , $I_D=2.0\text{A}$			5.0	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>							
Input Capacitance		$C_{ISS}$	$V_{GS}=0\text{V}$ , $V_{DS}=25\text{V}$ , $f=1.0\text{MHz}$		1609		pF
Output Capacitance		$C_{OSS}$			124.6		pF
Reverse Transfer Capacitance		$C_{RSS}$			12		pF
<b>SWITCHING CHARACTERISTICS</b>							
Total Gate Charge (Note 1)		$Q_G$	$V_{DS}=1200\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=4.0\text{A}$ $I_G=1\text{mA}$ (Note 1, 2)		45.4		nC
Gate-Source Charge		$Q_{GS}$			13		nC
Gate-Drain Charge		$Q_{GD}$			14.3		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	$V_{DS}=100\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=4.0\text{A}$ , $R_G=6\Omega$ (Note 1, 2)		13.7		ns
Rise Time		$t_R$			14.7		ns
Turn-off Delay Time		$t_{D(OFF)}$			43.2		ns
Fall-Time		$t_F$			32.8		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>							
Maximum Body-Diode Continuous Current		$I_S$				4	A
Maximum Body-Diode Pulsed Current		$I_{SM}$				8	A
Drain-Source Diode Forward Voltage (Note 1)		$V_{SD}$	$V_{GS}=0\text{V}$ , $I_S=4.0\text{A}$			1.4	V
Reverse Recovery Time (Note 1)		$t_{rr}$	$V_{GS}=0\text{V}$ , $I_S=4.0\text{A}$ ,		1060		ns
Reverse Recovery Charge		$Q_{rr}$	$dI_F/dt=100\text{A}/\mu\text{s}$ (Note1)		22		$\mu\text{C}$

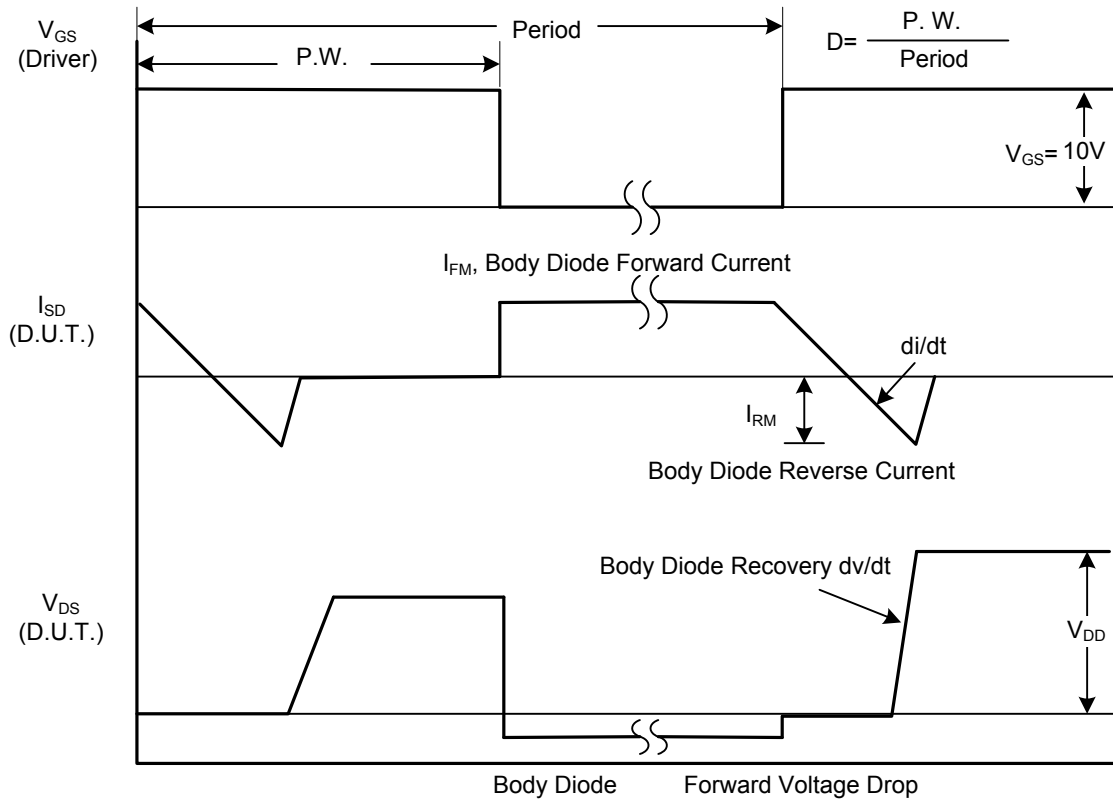
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

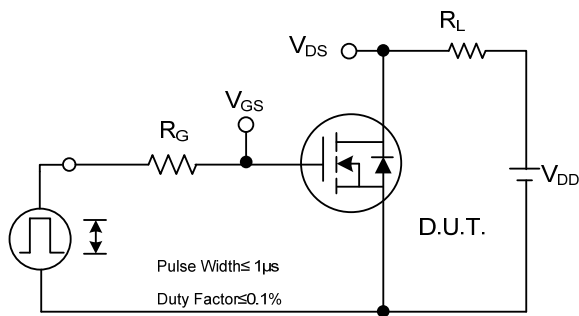


Peak Diode Recovery  $dv/dt$  Test Circuit

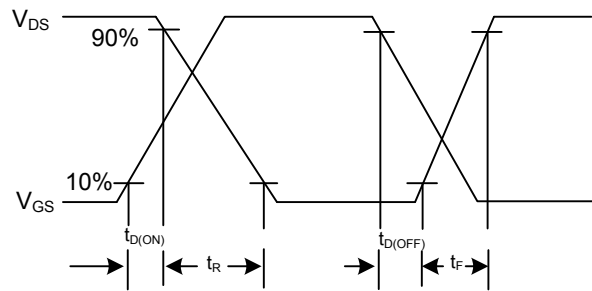


Peak Diode Recovery  $dv/dt$  Waveforms

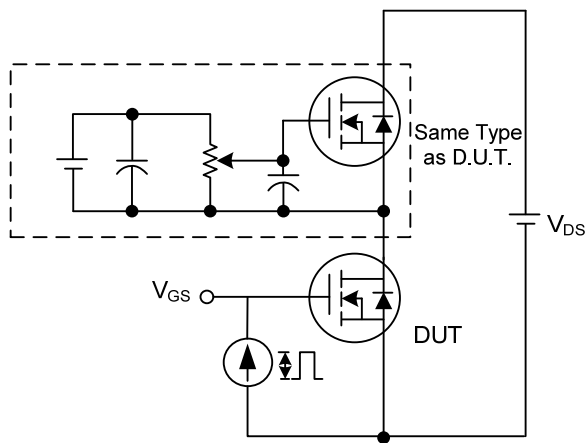
■ TEST CIRCUITS AND WAVEFORMS



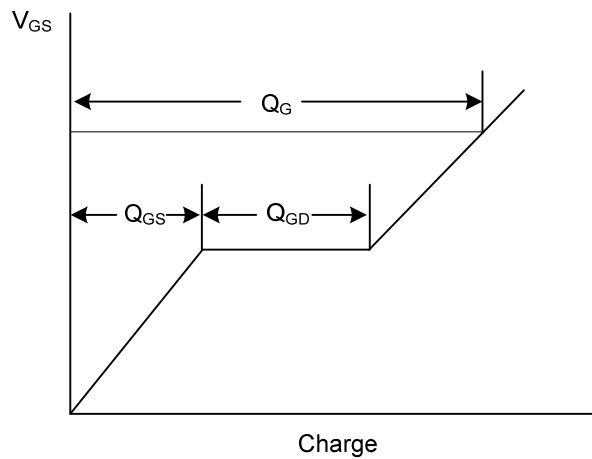
Switching Test Circuit



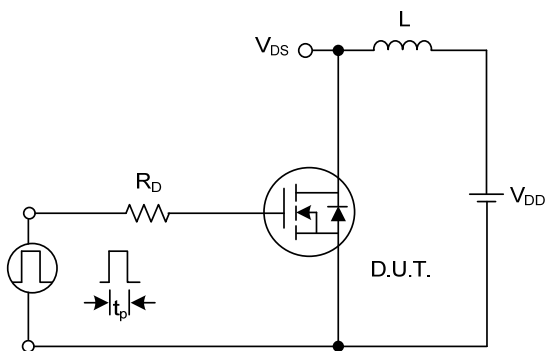
Switching Waveforms



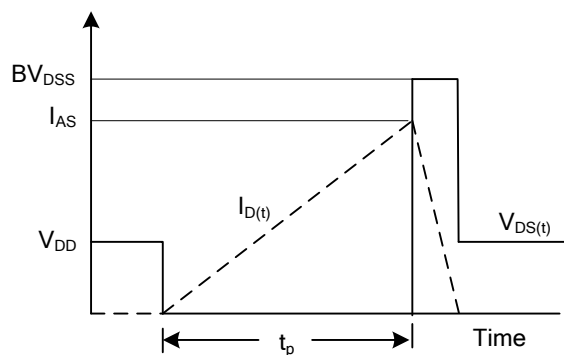
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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