

## BU406S

## NPN PLANAR TRANSISTOR

# NPN EXPITAXIAL PLANAR TRANSISTOR

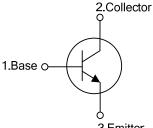
### DESCRIPTION

The UTC BU406S is a NPN expitaxial planar transistor, designed for using in general purpose amplifier and switching applications.

### **FEATURES**

\* High voltage

#### **SYMBOL**



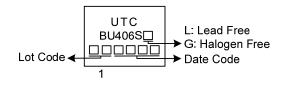
## 3.Emitter

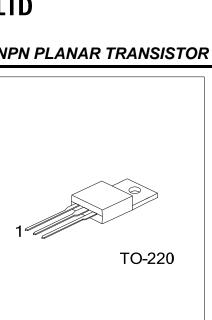
### ORDERING INFORMATION

Ordering Number		Deskare	Pin Assignment			Decking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
BU406SL-TA3-T	BU406SG-TA3-T	TO-220	В	С	Е	Tube	
Note: Pin Assignment: B: Base	C: Collector E: Emitter						

BU406SG-TA3-T	
(1) Packing Type	(1) T: Tube
(2) Package Type	(2) TA3: TO-220
(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

#### MARKING





## ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATING	UNIT	
Collector Base Voltage		V <sub>CBO</sub>	200	V	
Collector to Emitter Voltage		V <sub>CEO</sub>	110	V	
Emitter-Base Voltage		V <sub>EBO</sub>	6	V	
	DC		7	А	
Collector Current	Pulse	I <sub>C</sub>	10	А	
Base Current		Ι <sub>Β</sub>	2	А	
Collector Dissinction	T <sub>A</sub> =25°C		2	W	
Collector Dissipation	T <sub>C</sub> =25°C	Pc	65	W	
Junction Temperature		TJ	150	°C	
Storage Temperature		T <sub>STG</sub>	-65 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Emitter Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =100μA, I <sub>C</sub> =0	5			V
Collector Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =100μA, I <sub>E</sub> =0	200			V
Collector Emitter Sustaining Voltage (Note)	BV <sub>CEO</sub>	I <sub>C</sub> =30mA, I <sub>B</sub> =0	110			V
Collector Cutoff Current	I <sub>CEO</sub>	V <sub>CE</sub> =110V, I <sub>B</sub> =0			100	μA
Collector Cutoff Current	I <sub>CES</sub>	V <sub>CE</sub> =200V, V <sub>EB</sub> =0			100	μA
		V <sub>CE</sub> =150V, V <sub>EB</sub> =0			50	μA
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> =6V, I <sub>C</sub> =0			1	mA
Collector-Emitter Saturation Voltage (Note)	V <sub>CE(SAT)</sub>	I <sub>C</sub> =5A, I <sub>B</sub> =0.5A		0.22	0.5	V
Base Emitter On Voltage	V <sub>BE(ON)</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =5A		1.05	1.5	V
DC Current Cain (Nata)	h <sub>FE1</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =500mA	60		120	
DC Current Gain (Note)	h <sub>FE2</sub>	V <sub>CE</sub> =5V, I <sub>C</sub> =2A	50			
Current Gain Bandwidth Product	f⊤	V <sub>CE</sub> =10V, I <sub>C</sub> =500mA, f=1MHz	10			MHz
Turn-Off Time	t <sub>OFF</sub>	I <sub>C</sub> =5A, I <sub>B</sub> =500mA			0.7	μS

Note: Pulse Test:  $P_W \leq 300 \mu s$ , Duty Cycle  $\leq 2\%$ .



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