

BTA25

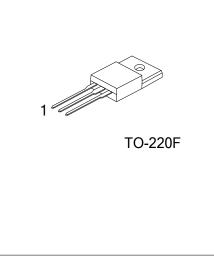
25A TRIACS

DESCRIPTION

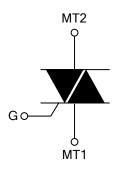
UNISONIC TECHNOLOGIES CO., LTD

TRIAC Advance The UTC BTA25 is a 25A triacs which can be operated in 4 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA25 is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.



SYMBOL



ORDERING INFORMATION

Ordering	Deekere	Pin	Assignm	Decking					
Lead Free	Halogen Free	Package	1	2	3	Packing			
BTA25L-x-xx-TF3-T	BTA25G-x-xx-TF3-T	TO-220F	MT1	MT2	G	Tube			
Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate									

Note: Pin Assignment: MIT1: MIT1 M12: M12 G: Gate

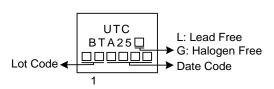
BTA25G-x-xx-TF3-T)Packing Type	(1) T: Tube
)Package Type	(2) TF3: TO-220F
(3))Sensitivity and type	(3) refer to SENSITIVITY AND TYPE
(4))Voltage	(4) 6: 600V, 8: 800V
(5))Green Package	(5) G: Halogen Free and Lead Free, L: Lead Free

SENSITIVITY AND TYPE

PART NUMBER	VOLT	ΓAGE	SENSITIVITY	TYPE
	600V	800V	SENSITIVITY	ITPE
В	\bigcirc	\bigcirc	50mA	STANDARD

(): Available

MARKING



ABSOLUTE MAXIMUM RATINGS

PARAME	TER		SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave) T _C =75°C		I _{T(RMS)}	25	А	
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz	t=20ms	les.	250	А
Cycle, T _J initial=25°C)	F=60 Hz	t=16.7ms	I _{TSM}	260	А
I ² t Value for Fusing	t _P =10ms		l ² t	340	A ² s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	TJ=125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	TJ=25°C	$V_{\text{DSM}}/V_{\text{RSM}}$	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	TJ=125°C	I _{GM}	4	А
Average Gate Power Dissipation T _J =125°C		P _{G(AV)}	1	W	
Operating Junction Temperature		TJ	-40 ~ +125	°C	
Storage Junction Temperature			T _{STG}	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	60	°C/W
Junction to Case (AC)	θ _{JC}	0.8	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise specified)

FOR STANDARD TYPE (4 QUADRANTS)

		TEST CONDITIONS		В			
PARAMETER	SYMBOL			MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT} V _D =12V, R _L =33Ω I-II-III IV		- -			50	mA
		IV			100	mA	
Gate Trigger Voltage	V_{GT}		ALL			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D = V_{DRM}$, $R_L = 3.3 k\Omega$, $T_J = 125 °C$	ALL	0.2			V
Holding Current Note 2)	I _H	I _T =500mA				80	mA
Latching Current	L	1 1 2 1	I-III-IV			70	mA
	ΙL	I_L $I_G=1.2 I_{GT}$ II	II			160	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V_D =67% V_{DRM} , Gate Open, T _J =125°C		500			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation(Note 2)	(dV/dt)c	(dl/dt)c=13.3A/ms, T _J = 125°C		10			V/µs

STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	V _{TM}	I _{TM} =35A, t _P =380µs	TJ=25°C			1.55	V
Threshold Voltage (Note 2)	V _{TO}		TJ=125°C			0.85	V
Dynamic Resistance (Note 2)	RD		TJ=125°C			16	mΩ
Repetitive Peak Off-State	I _{DRM}		TJ=25°C			5	μA
Current	I _{RRM}	V _{DRM} =V _{RRM}	Tյ=125°C			3	mA

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.



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