

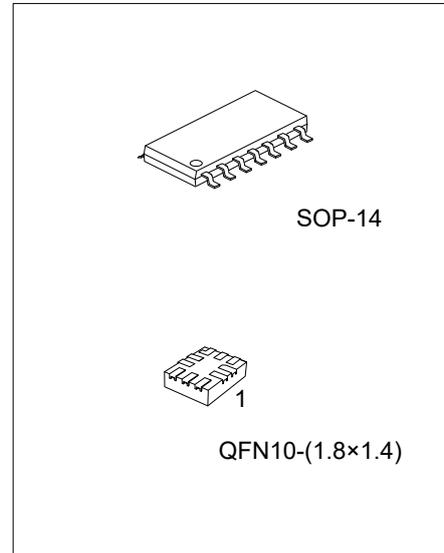


U74AVC2T245

Preliminary

CMOS IC

2-BIT DUAL-SUPPLY BUS TRANSCEIVER WITH CONFIGURABLE VOLTAGE TRANSLATION AND 3-STATE OUTPUTS



DESCRIPTION

The UTC **U74AVC2T245** is a dual-bit dual-supply transceiver that enables bidirectional level translation. The A port is designed to track V_{CCA} . V_{CCA} accepts any supply voltage from 1.2V to 3.6V. The B port is designed to track V_{CCB} . V_{CCB} accepts any supply voltage from 1.2V to 3.6V. This allows for universal low-voltage bidirectional translation between any of the 1.2V, 1.5V, 1.8V, 2.5V and 3.3V voltage nodes.

The UTC **U74AVC2T245** is designed for asynchronous communication between two data buses. The logic levels of the direction-control (DIR) input and the output-enable (\overline{OE}) activate either the B-port outputs or the A-port outputs or place both output ports into the high-impedance mode. The device transmits data from the A bus to the B bus when the B-port outputs are activated and from the B bus to the A bus when the A-port outputs are activated. The input circuitry on both A and B ports always is active and must have a logic HIGH or LOW level applied to prevent excess I_{CC} and I_{CCZ} .

The UTC **U74AVC2T245** is designed so that the control pins (DIR1, DIR2, and \overline{OE}) are supplied by V_{CCA} .

This device is fully specified for partial-power-down applications using I_{OFF} . The I_{OFF} circuitry disables the outputs, preventing any damaging current backflow through the device when it is powered down.

The V_{CC} isolation feature ensures that if either V_{CC} input is at GND, both ports are in the high-impedance state.

To ensure the high-impedance state during power up or power down, \overline{OE} must be connected to V_{CC} through a pull-up resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

FEATURES

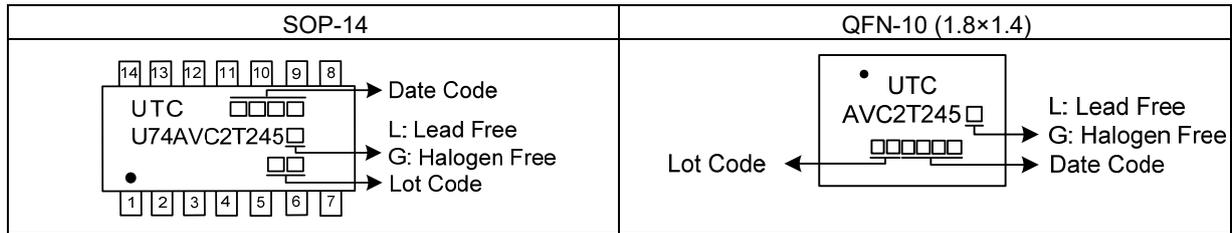
- * Operation Voltage Range: 1.2~3.6V
- * Control Inputs VIH/VIL Levels Are Referenced to V_{CCA} Voltage
- * I_{OFF} Supports Partial Power Down Mode Operation
- * I/Os Are 4.6V Tolerant

ORDERING INFORMATION

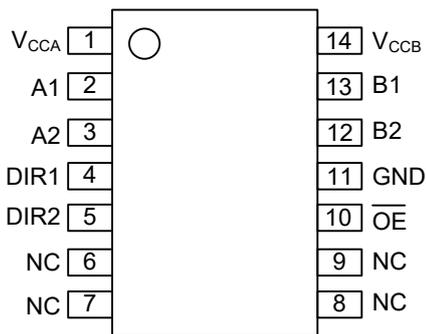
Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AVC2T245L-S14-R	U74AVC2T245G-S14-R	SOP-14	Tape Reel
U74AVC2T245L-Q10-1814-R	U74AVC2T245G-Q10-1814-R	QFN-10(1.8x1.4)	Tape Reel

<p>U74AVC2T245G-S14-R</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) R: Tape Reel (2) S14: SOP-14, Q10-1814: QFN-10(1.8x1.4) (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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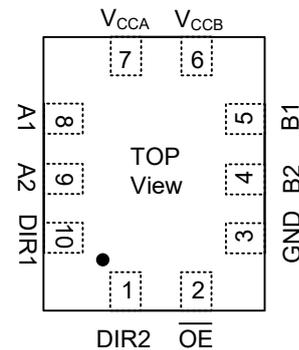
MARKING



PIN CONFIGURATION



SOP-14



QFN-10 (1.8×1.4)

PIN DESCRIPTION

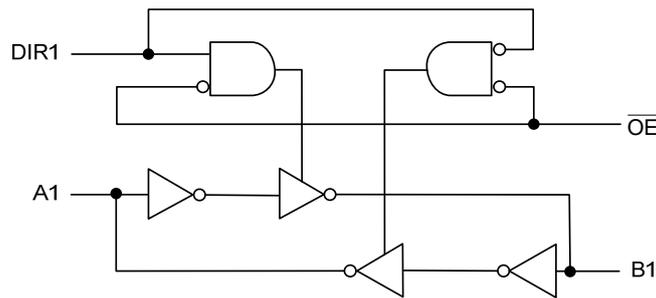
PIN NO.		PIN NAME	TYPE	DESCRIPTION
SOP-14	QFN-10 (1.8×1.4)			
1	7	V _{CCA}		A-port power supply voltage. 1.2V ≤ V _{CCA} ≤ 3.6V
2	8	A1	I/O	Input/output A1. Referenced to V _{CCA}
3	9	A2	I/O	Input/output A2. Referenced to V _{CCA}
4	10	DIR1	I	Direction-control input for '1' ports
5	1	DIR2	I	Direction-control input for '2' ports
6 ~ 9	-	NC		No Connection.
10	2	OE	I	3-state output-mode enables. Pull OE high to place outputs in 3-state mode. Referenced to V _{CCA}
11	3	GND	I/O	Ground
12	4	B2	I/O	Input/output B2. Referenced to V _{CCB}
13	5	B1	I/O	Input/output B1. Referenced to V _{CCB}
14	6	V _{CCB}		B-port power supply voltage. 1.2V ≤ V _{CCB} ≤ 3.6V

■ FUNCTION TABLE

INPUTS		OUTPUT		OPERATION
\overline{OE}	DIRn	A PORT	B PORT	
L	L	Enabled	Hi-Z	Bn data to An data
L	H	Hi-Z	Enabled	An data to Bn data
H	X	Hi-Z	Hi-Z	Isolation

L: low voltage level; H: high voltage level; X: don't care

■ LOGIC DIAGRAM



■ ABSOLUTE MAXIMUM RATING ($T_A=25^\circ\text{C}$, unless otherwise specified) (Note 1)

PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Supply Voltage	V_{CCA}, V_{CCB}		-0.5 ~ 4.6	V
Input Voltage (Note 2)	V_{IN}	I/O ports (A port)	-0.5 ~ 4.6	V
		I/O ports (B port)	-0.5 ~ 4.6	V
		Control inputs	-0.5 ~ 4.6	V
Voltage range applied to any output in the high-impedance or power-off state (Note 2)	V_{OUT}	A port	-0.5 ~ 4.6	V
		B port	-0.5 ~ 4.6	V
Voltage range applied to any output in the high or low state (Note 2, 3)	V_{OUT}	A port	-0.5 ~ $V_{CCA}+0.5$	V
		B port	-0.5 ~ $V_{CCB}+0.5$	V
Continuous Output Current	I_{OUT}		± 50	mA
Continuous Current Through V_{CCA}, V_{CCB} or GND	I_{CC}		± 100	mA
Input Clamp Current	I_{IK}	$V_{IN}<0$	-50	mA
Output Clamp Current	I_{OK}	$V_{OUT}<0$	-50	mA
Storage Temperature Range	T_{STG}		-65 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The input voltage and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

3. The output positive-voltage rating may be exceeded up to 4.6V maximum if the output current rating is observed.

■ RECOMMENDED OPERATING CONDITIONS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V_{CCA}, V_{CCB}		1.2		3.6	V
Input Voltage	V_{IN}		0		3.6	V
Output Voltage	V_{OUT}	Active state	0		V_{CC}	V
		3-state	0		3.6	
Operating Temperature	T_A		-40		+125	$^\circ\text{C}$
Input Transition Rise or Fall Rate	$\Delta t/\Delta v$				5	ns/V

■ ELECTRICAL CHARACTERISTICS (Note 1, 2, 3)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
High-level input voltage	Data inputs (Note 4)	V_{IH}	$V_{CCI}=1.2V\sim 1.95V$	$V_{CCI}\times 0.65$			V	
			$V_{CCI}=1.95V\sim 2.7V$	1.6			V	
			$V_{CCI}=2.7V\sim 3.6V$	2			V	
	DIR (Referenced to V_{CCA}) (Note 5)		$V_{CCI}=1.2V\sim 1.95V$	$V_{CCA}\times 0.65$				V
			$V_{CCI}=1.95V\sim 2.7V$	1.6				V
			$V_{CCI}=2.7V\sim 3.6V$	2				V
Low-level output voltage	Data inputs (Note 4)	V_{IL}	$V_{CCI}=1.2V\sim 1.95V$			$V_{CCI}\times 0.35$	V	
			$V_{CCI}=1.95V\sim 2.7V$			0.7	V	
			$V_{CCI}=2.7V\sim 3.6V$			0.8	V	
	DIR (Referenced to V_{CCA}) (Note 5)		$V_{CCI}=1.2V\sim 1.95V$			$V_{CCA}\times 0.35$		V
			$V_{CCI}=1.95V\sim 2.7V$			0.7		V
			$V_{CCI}=2.7V\sim 3.6V$			0.8		V
High-Level Output Voltage		V_{OH}	$V_{CCA}=V_{CCB}=1.2V\sim 3.6V$ $I_{OH}=-100\mu A, V_I=V_{IH}$	$V_{CCA}-0.2$			V	
			$V_{CCA}=V_{CCB}=1.2V$ $I_{OH}=-3mA, V_I=V_{IH}$		0.95		V	
			$V_{CCA}=V_{CCB}=1.4V$ $I_{OH}=-6mA, V_I=V_{IH}$	1.05			V	
			$V_{CCA}=V_{CCB}=1.65V$ $I_{OH}=-8mA, V_I=V_{IH}$	1.2			V	
			$V_{CCA}=V_{CCB}=2.3V$ $I_{OH}=-9mA, V_I=V_{IH}$	1.75			V	
			$V_{CCA}=V_{CCB}=3.0V$ $I_{OH}=-12mA, V_I=V_{IH}$	2.3			V	
Low-Level Output Voltage		V_{OL}	$V_{CCA}=V_{CCB}=1.2V\sim 3.6V$ $I_{OL}=100\mu A, V_I=V_{IL}$			0.2	V	
			$V_{CCA}=V_{CCB}=1.2V$ $I_{OL}=3mA, V_I=V_{IL}$		0.25		V	
			$V_{CCA}=V_{CCB}=1.4V$ $I_{OL}=6mA, V_I=V_{IL}$			0.35	V	
			$V_{CCA}=V_{CCB}=1.65V$ $I_{OL}=8mA, V_I=V_{IL}$			0.45	V	
			$V_{CCA}=V_{CCB}=2.3V$ $I_{OL}=9mA, V_I=V_{IL}$			0.55	V	
			$V_{CCA}=V_{CCB}=3.0V$ $I_{OL}=12mA, V_I=V_{IL}$			0.7	V	
Input Leakage Current	Control inputs	$I_{I(LEAK)}$	$V_{CCA}=V_{CCB}=1.2\sim 3.6V$ $V_{IN}=V_{CCA}$ or GND		± 0.025	± 0.25	μA	
Power OFF Leakage Current	A or B port	I_{OFF}	$V_{CCA}=0V, V_{CCB}=0\sim 3.6V$ V_{IN} or $V_{OUT}=0\sim 3.6V$		± 0.1	± 1	μA	
			$V_{CCA}=0\sim 3.6V, V_{CCB}=0V$ V_{IN} or $V_{OUT}=0\sim 3.6V$		± 0.1	± 1	μA	
Output OFF-state current	A or B port	I_{OZ}	$V_{CCA}=V_{CCB}=3.6V$ $V_{OUT}=V_{CCO}$ or GND $V_{IN}=V_{CCI}$ or GND, $\overline{OE}=V_{IH}$		± 0.5	± 2.5	μA	

■ ELECTRICAL CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Quiescent Supply Current		I_{CCA}	$V_{CCA}=V_{CCB}=1.2\sim 3.6V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$			8	μA
			$V_{CCA}=0V$, $V_{CCB}=0\sim 3.6V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$	-2			μA
			$V_{CCA}=0\sim 3.6V$, $V_{CCB}=0V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$			8	μA
Quiescent Supply Current		I_{CCB}	$V_{CCA}=V_{CCB}=1.2\sim 3.6V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$			8	μA
			$V_{CCA}=0V$, $V_{CCB}=0\sim 3.6V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$			8	μA
			$V_{CCA}=0\sim 3.6V$, $V_{CCB}=0V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$	-2			μA
Quiescent Supply Current & Quiescent Supply Current		$I_{CCA}+I_{CCB}$	$V_{CCA}=V_{CCB}=1.2\sim 3.6V$ $V_{IN}=V_{CCI}$ or GND, $I_O=0$			16	μA
Input Capacitance	Control inputs	C_{IN}	$V_{CCA}=V_{CCB}=3.3V$ $V_{IN}=3.3V$ or GND		3.5		pF
Output Capacitance	A or B port	C_{IO}	$V_{CCA}=V_{CCB}=3.3V$ $V_{IN}=3.3V$ or GND		6		pF

Notes: 1. V_{CCI} is the V_{CC} associated with the input port.

2. V_{CCO} is the V_{CC} associated with the output port.

3. All unused data inputs of the device must be held at V_{CCI} or GND to ensure proper device operation.

4. For V_{CCI} values not specified in the data sheet, $V_{IHMIN}=V_{CCI}\times 0.7V$, $V_{ILMAX}=V_{CCI}\times 0.3V$

5. For V_{CCI} values not specified in the data sheet, $V_{IHMIN}=V_{CCA}\times 0.7V$, $V_{ILMAX}=V_{CCA}\times 0.3V$

■ SWITCHING CHARACTERISTICS

(Over recommended operating free-air temperature range, $V_{CCA}=1.2V$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output (B)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		2.5		ns
		$V_{CCB}=1.5V\pm 0.1V$		2.1		ns
		$V_{CCB}=1.8V\pm 0.15V$		1.9		ns
		$V_{CCB}=2.5V\pm 0.2V$		1.9		ns
		$V_{CCB}=3.3V\pm 0.3V$		1.9		ns
Propagation delay from input (B) to output (A)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		2.5		ns
		$V_{CCB}=1.5V\pm 0.1V$		2.2		ns
		$V_{CCB}=1.8V\pm 0.15V$		2.0		ns
		$V_{CCB}=2.5V\pm 0.2V$		1.8		ns
		$V_{CCB}=3.3V\pm 0.3V$		1.7		ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.8		ns
		$V_{CCB}=1.5V\pm 0.1V$		3.1		ns
		$V_{CCB}=1.8V\pm 0.15V$		2.7		ns
		$V_{CCB}=2.5V\pm 0.2V$		2.6		ns
		$V_{CCB}=3.3V\pm 0.3V$		3.0		ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.7		ns
		$V_{CCB}=1.5V\pm 0.1V$		3.7		ns
		$V_{CCB}=1.8V\pm 0.15V$		3.7		ns
		$V_{CCB}=2.5V\pm 0.2V$		3.7		ns
		$V_{CCB}=3.3V\pm 0.3V$		3.7		ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		4.4		ns
		$V_{CCB}=1.5V\pm 0.1V$		3.6		ns
		$V_{CCB}=1.8V\pm 0.15V$		3.5		ns
		$V_{CCB}=2.5V\pm 0.2V$		3.3		ns
		$V_{CCB}=3.3V\pm 0.3V$		4.1		ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		4.2		ns
		$V_{CCB}=1.5V\pm 0.1V$		4.2		ns
		$V_{CCB}=1.8V\pm 0.15V$		4.3		ns
		$V_{CCB}=2.5V\pm 0.2V$		4.1		ns
		$V_{CCB}=3.3V\pm 0.3V$		4.2		ns

■ SWITCHING CHARACTERISTICS (Cont.)

(Over recommended operating free-air temperature range, $V_{CCA}=1.5V\pm0.1V$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output (B)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		2.2		ns
		$V_{CCB}=1.5V\pm0.1V$	0.3		5.7	ns
		$V_{CCB}=1.8V\pm0.15V$	0.2		4.9	ns
		$V_{CCB}=2.5V\pm0.2V$	0.1		4.0	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		3.9	ns
Propagation delay from input (B) to output (A)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		2.0		ns
		$V_{CCB}=1.5V\pm0.1V$	0.6		5.7	ns
		$V_{CCB}=1.8V\pm0.15V$	0.4		5.5	ns
		$V_{CCB}=2.5V\pm0.2V$	0.2		5.2	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		5.1	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.4		ns
		$V_{CCB}=1.5V\pm0.1V$	1.1		6.7	ns
		$V_{CCB}=1.8V\pm0.15V$	0.9		6.7	ns
		$V_{CCB}=2.5V\pm0.2V$	0.7		6.7	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		6.7	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		2.5		ns
		$V_{CCB}=1.5V\pm0.1V$	1.1		8.2	ns
		$V_{CCB}=1.8V\pm0.15V$	1.1		8.2	ns
		$V_{CCB}=2.5V\pm0.2V$	1.1		8.2	ns
		$V_{CCB}=3.3V\pm0.3V$	1.1		8.2	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		4.1		ns
		$V_{CCB}=1.5V\pm0.1V$	1.2		7.3	ns
		$V_{CCB}=1.8V\pm0.15V$	0.8		7.3	ns
		$V_{CCB}=2.5V\pm0.2V$	0.4		7.3	ns
		$V_{CCB}=3.3V\pm0.3V$	1.0		7.4	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		3.3		ns
		$V_{CCB}=1.5V\pm0.1V$	0.3		7.4	ns
		$V_{CCB}=1.8V\pm0.15V$	0.2		6.1	ns
		$V_{CCB}=2.5V\pm0.2V$	0.3		5.6	ns
		$V_{CCB}=3.3V\pm0.3V$	0.3		5.6	ns

■ SWITCHING CHARACTERISTICS (Cont.)

(Over recommended operating free-air temperature range, $V_{CCA}=1.8V\pm0.15V$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output (B)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		2.0		ns
		$V_{CCB}=1.5V\pm0.1V$	0.1		5.5	ns
		$V_{CCB}=1.8V\pm0.15V$	0.1		4.6	ns
		$V_{CCB}=2.5V\pm0.2V$	0.1		3.6	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		3.4	ns
Propagation delay from input (B) to output (A)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		1.9		ns
		$V_{CCB}=1.5V\pm0.1V$	0.4		4.9	ns
		$V_{CCB}=1.8V\pm0.15V$	0.1		4.6	ns
		$V_{CCB}=2.5V\pm0.2V$	0.1		4.3	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		4.1	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.2		ns
		$V_{CCB}=1.5V\pm0.1V$	0.8		6.7	ns
		$V_{CCB}=1.8V\pm0.15V$	0.4		6.2	ns
		$V_{CCB}=2.5V\pm0.2V$	0.4		6.2	ns
		$V_{CCB}=3.3V\pm0.3V$	0.3		6.2	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		1.9		ns
		$V_{CCB}=1.5V\pm0.1V$	0.2		6.8	ns
		$V_{CCB}=1.8V\pm0.15V$	0.2		6.6	ns
		$V_{CCB}=2.5V\pm0.2V$	0.2		6.7	ns
		$V_{CCB}=3.3V\pm0.3V$	0.2		6.7	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		3.8		ns
		$V_{CCB}=1.5V\pm0.1V$	0.7		6.2	ns
		$V_{CCB}=1.8V\pm0.15V$	0.3		6.5	ns
		$V_{CCB}=2.5V\pm0.2V$	0.1		5.7	ns
		$V_{CCB}=3.3V\pm0.3V$	0.8		6.5	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		3.4		ns
		$V_{CCB}=1.5V\pm0.1V$	0.1		6.8	ns
		$V_{CCB}=1.8V\pm0.15V$	0.1		6.8	ns
		$V_{CCB}=2.5V\pm0.2V$	0.1		6.7	ns
		$V_{CCB}=3.3V\pm0.3V$	0.1		6.7	ns

■ SWITCHING CHARACTERISTICS (Cont.)

(Over recommended operating free-air temperature range, $V_{CCA}=2.5V\pm 0.2V$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output (B)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		1.9		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.1		5.2	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		4.3	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		3.3	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		2.9	ns
Propagation delay from input (B) to output (A)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		1.8		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.5		4.0	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.2		3.6	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		3.3	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		3.2	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.1		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.7		6.2	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.5		5.2	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.3		4.6	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.3		4.6	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		1.4		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.4		6.4	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.4		6.1	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.4		4.9	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.4		5.4	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		3.6		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.2		5.2	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		5.4	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		4.5	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.7		6.0	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		2.1		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.1		6.2	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		5.1	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		4.7	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		4.7	ns

■ SWITCHING CHARACTERISTICS (Cont.)

(Over recommended operating free-air temperature range, $V_{CCA}=3.3V\pm 0.3V$, unless otherwise specified)

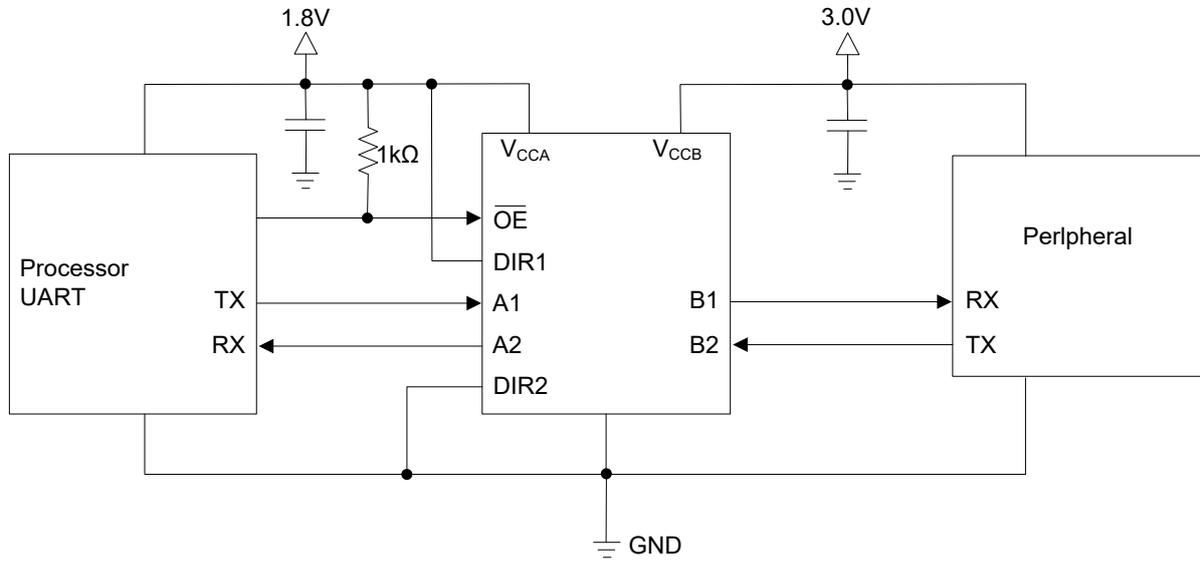
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output (B)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		1.8		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.1		5.1	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		4.1	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		3.2	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		2.7	ns
Propagation delay from input (B) to output (A)	t_{PLH} t_{PHL}	$V_{CCB}=1.2V$		1.9		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.5		3.7	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.2		3.4	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		2.9	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		2.7	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		3.1		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.9		5.9	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.5		5.0	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.3		5.0	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.3		5.0	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PZH} t_{PZL}	$V_{CCB}=1.2V$		1.2		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.4		6.2	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.4		5.9	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.4		4.7	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.4		5.2	ns
Propagation delay from input (\overline{OE}) to output (A)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		3.4		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.1		4.6	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		4.7	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.3		4.8	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.7		4.5	ns
Propagation delay from input (\overline{OE}) to output (B)	t_{PHZ} t_{PLZ}	$V_{CCB}=1.2V$		2.9		ns
		$V_{CCB}=1.5V\pm 0.1V$	0.1		5.9	ns
		$V_{CCB}=1.8V\pm 0.15V$	0.1		5.3	ns
		$V_{CCB}=2.5V\pm 0.2V$	0.1		5.3	ns
		$V_{CCB}=3.3V\pm 0.3V$	0.1		5.3	ns

■ OPERATING CHARACTERISTIC (C_L=0, f=10MHz, t_r=t_f=1ns, T_A=25°C, unless otherwise specified)

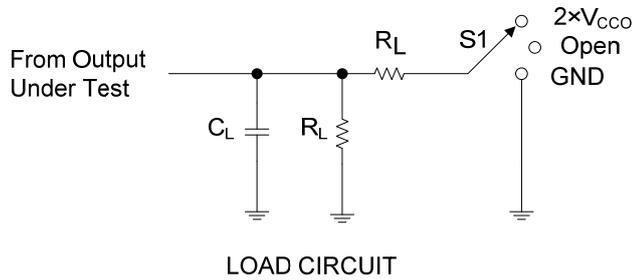
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance (A to B)	Outputs enabled	C _{PDA}	V _{CCA} =V _{CCB} =1.2V		3		pF
			V _{CCA} =V _{CCB} =1.5V		3		pF
			V _{CCA} =V _{CCB} =1.8V		3		pF
			V _{CCA} =V _{CCB} =2.5V		3		pF
			V _{CCA} =V _{CCB} =3.3V		4		pF
	Outputs disabled		V _{CCA} =V _{CCB} =1.2V		1		pF
			V _{CCA} =V _{CCB} =1.5V		1		pF
			V _{CCA} =V _{CCB} =1.8V		1		pF
			V _{CCA} =V _{CCB} =2.5V		2		pF
			V _{CCA} =V _{CCB} =3.3V		2		pF
Power Dissipation Capacitance (B to A)	Outputs enabled	V _{CCA} =V _{CCB} =1.2V		12		pF	
		V _{CCA} =V _{CCB} =1.5V		13		pF	
		V _{CCA} =V _{CCB} =1.8V		13		pF	
		V _{CCA} =V _{CCB} =2.5V		15		pF	
		V _{CCA} =V _{CCB} =3.3V		15		pF	
	Outputs disabled	V _{CCA} =V _{CCB} =1.2V		1		pF	
		V _{CCA} =V _{CCB} =1.5V		2		pF	
		V _{CCA} =V _{CCB} =1.8V		2		pF	
		V _{CCA} =V _{CCB} =2.5V		2		pF	
		V _{CCA} =V _{CCB} =3.3V		2		pF	
Power Dissipation Capacitance (A to B)	Outputs enabled	C _{PDB}	V _{CCA} =V _{CCB} =1.2V		12		pF
			V _{CCA} =V _{CCB} =1.5V		13		pF
			V _{CCA} =V _{CCB} =1.8V		13		pF
			V _{CCA} =V _{CCB} =2.5V		14		pF
			V _{CCA} =V _{CCB} =3.3V		16		pF
	Outputs disabled		V _{CCA} =V _{CCB} =1.2V		1		pF
			V _{CCA} =V _{CCB} =1.5V		2		pF
			V _{CCA} =V _{CCB} =1.8V		2		pF
			V _{CCA} =V _{CCB} =2.5V		2		pF
			V _{CCA} =V _{CCB} =3.3V		2		pF
Power Dissipation Capacitance (B to A)	Outputs enabled	V _{CCA} =V _{CCB} =1.2V		3		pF	
		V _{CCA} =V _{CCB} =1.5V		3		pF	
		V _{CCA} =V _{CCB} =1.8V		3		pF	
		V _{CCA} =V _{CCB} =2.5V		4		pF	
		V _{CCA} =V _{CCB} =3.3V		4		pF	
	Outputs disabled	V _{CCA} =V _{CCB} =1.2V		1		pF	
		V _{CCA} =V _{CCB} =1.5V		1		pF	
		V _{CCA} =V _{CCB} =1.8V		1		pF	
		V _{CCA} =V _{CCB} =2.5V		2		pF	
		V _{CCA} =V _{CCB} =3.3V		2		pF	

Note: Power dissipation capacitance per transceiver.

■ TYPICAL APPLICATION CIRCUIT

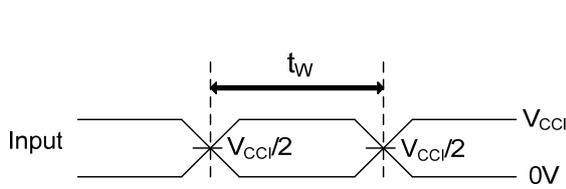


■ TEST CIRCUIT AND WAVEFORMS

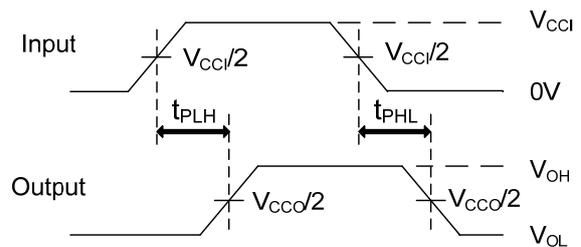


TEST	S1
t_{PD}	Open
t_{PLZ}/t_{PZL}	$2 \times V_{CCO}$
t_{PHZ}/t_{PZH}	GND

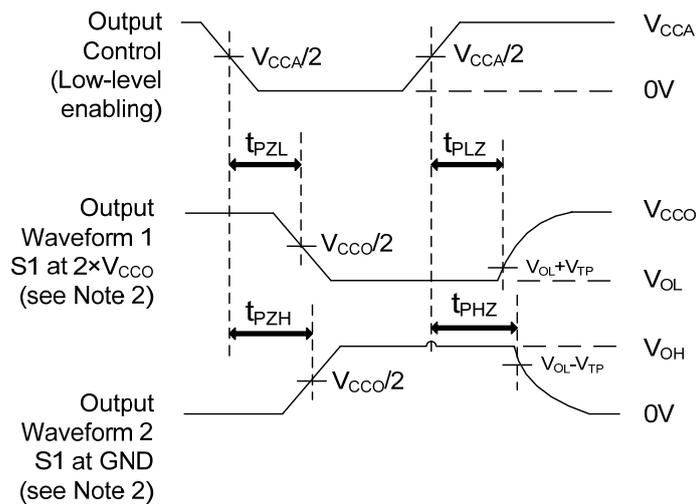
V_{CCO}	C_L	R_L	V_{TP}
1.2V	15pF	2k Ω	0.1V
1.5V \pm 0.1V	15pF	2k Ω	0.1V
1.8V \pm 0.15V	15pF	2k Ω	0.15V
2.5V \pm 0.2V	15pF	2k Ω	0.15V
3.3V \pm 0.3V	15pF	2k Ω	0.3V



PULSE DURATION



PROPAGATION DELAY TIMES



ENABLE AND DISABLE TIMES

Note: C_L includes probe and jig capacitance.

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