USS305NX

**Advance** 

## NPN EPITAXIAL SILICON TRANSISTOR

# 60V NPN LOW SATURATION MEDIUM POWER TRANSISTOR

#### DESCRIPTION

The **USS305NX** is an new low saturation 60V NPN transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

### **■ FEATURES**

- \* 5 amps continuous current
- \* Up to 20 amps peak current
- \* Very low saturation voltages
- \* Excellent hFE characteristics up to 10 amps

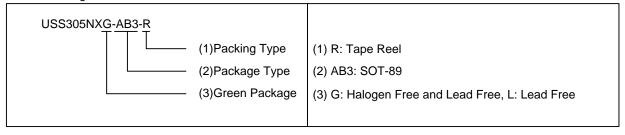


**SOT-89** 

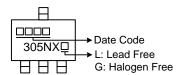
#### **■ ORDERING INFORMATION**

Ordering Number		Doolsons	Pin Assignment			Dealing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
USS305NXL-AB3-R	USS305NXG-AB3-R	SOT-89	В	С	Е	Tape Reel	

Note: Pin Assignment: B: Base C: Collector E: Emitter



#### MARKING



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## ■ ABSOLUATE MAXIUM RATINGS (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	$V_{CBO}$	150	V
Collector to Emitter Voltage	$V_{CEO}$	60	V
Emitter to Base Voltage	$V_{EBO}$	7	V
Bese Current	I <sub>B</sub>	2	Α
Collector Current	Ic	5	Α
Peak Collector Current	I <sub>CM</sub>	20	Α
Collector Dissipation	Pc	1.5	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Single pulse, P<sub>W</sub>=10ms.
- 3. Device mounted on FR-4 PCB with minimum recommended pad layout. (25x25x1.6mm)

#### **■ THERMAL DATA**

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	83	°C/W
Junction to Case	$\theta_{JC}$	60	°C/W

## ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	$BV_CBO$	I <sub>C</sub> =100μA	150			V
Collector-Emitter Breakdown Voltage	$BV_CEO$	I <sub>C</sub> =1μA	60			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =100μA	7.0			V
Collector Cutoff Current	$I_{CBO}$	V <sub>CB</sub> =120V			20	nA
Emitter Cutoff Current	$I_{EBO}$	V <sub>EB</sub> =6V			10	nA
Base Emitter On Voltage (Note)	V <sub>BE (ON)</sub>	$V_{CE}=1V$ , $I_{C}=6A$			1050	mV
Base-Emitter Saturation Voltage (Note)	V <sub>BE (SAT)</sub>	I <sub>C</sub> =6A, I <sub>B</sub> =300mA(Note)			1100	mV
Collector-Emitter Saturation Voltage (Note)	V <sub>CE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =5mA			30	mV
		I <sub>C</sub> =1A, I <sub>B</sub> =100mA			55	mV
		I <sub>C</sub> =1A, I <sub>B</sub> =50mA			65	mV
		$I_C=2A$ , $I_B=50mA$			125	mV
		I <sub>C</sub> =6A, I <sub>B</sub> =300mA			230	mV
DC Current Transfer Ratio (Note)	h <sub>FE</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =1V	100			
		I <sub>C</sub> =2A, V <sub>CE</sub> =1V	100		300	
		I <sub>C</sub> =5A, V <sub>CE</sub> =1V	55			
		I <sub>C</sub> =10A, V <sub>CE</sub> =1V	20			
Turn-ON Delay Time (Note 1)	t <sub>D(ON)</sub>			42		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	I <sub>C</sub> =1A, V <sub>CC</sub> =10V, I <sub>B1</sub> =I <sub>B2</sub> =100mA		760		ns
Transition Frequency (Note)	$f_T$	I <sub>C</sub> =100mA, V <sub>CE</sub> =10V, f=1MHz		130		MHz
Collector Capacitance	$C_OB$	V <sub>CB</sub> =10V, f=1MHz		31		pF

Note: Measured under pulsed conditions. Pulse Test: Pulse width ≤ 300µs, Duty cycle≤2%.

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