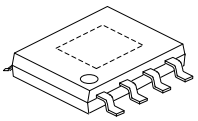
LOW DROP FIXED AND ADJUSTABLE POSITIVE VOLTAGE REGULATORS



HSOP-8

■ DESCRIPTION

The UTC **LD1119A** is a low dropout, 3-terminal positive voltage regulator designed to provide output current up to 1A, There are adjustable version (V_{REF}=1.25V) and various fixed versions.

■ FEATURES

* Low dropout voltage
* Output current up to 1.0A
* Built-in current limit and over temperature protection
* Low current consumption
* Support MLCC

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
LD1119AL-xx-SH2-R	LD1119AG-xx-SH2-R	HSOP-8	A	I	G	I	O	O	O	O	Tape Reel

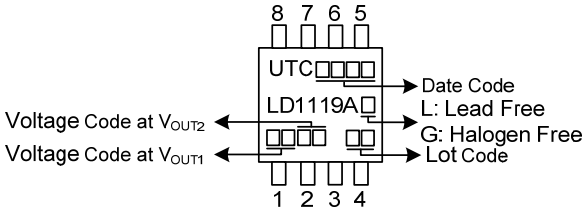
Notes: 1. Pin Assignment: A: ADJ G: GND I: V_{IN} O: V_{OUT}

2. xx: Output Voltage, Refer to Marking Information.

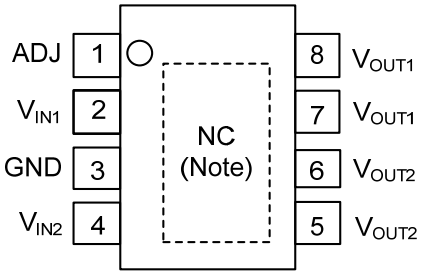
LD1119AG-xx-SH2-R

(1)Packing Type	(1) R: Tape Reel
(2)Package Type	(2) SH2: HSOP-8
(3)Output Voltage Code	(3) xx: refer to Marking Information
(4)Green Package	(4) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING INFORMATIONS

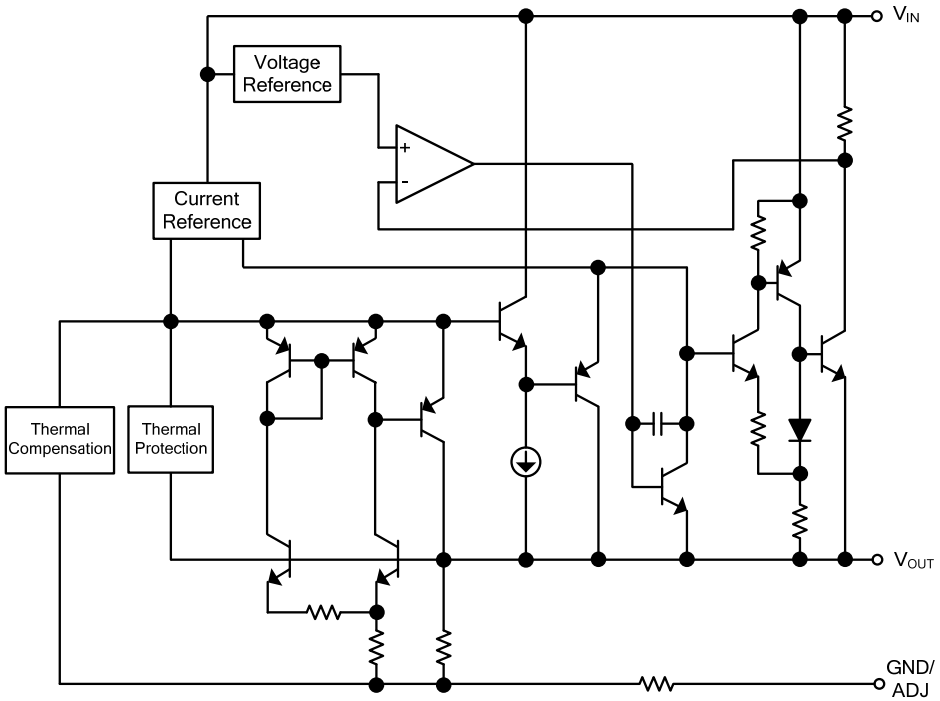
PACKAGE	VOLTAGE CODE		MARKING
	V_{OUT1}	V_{OUT2}	
HSOP-8	AD: ADJ	33: 3.3V	

■ PIN CONFIGURATION



Note: No connect.

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
DC Input Voltage	V_{IN}	18	V
Power Dissipation	P_D	Internally limited	
Junction Temperature	T_J	+150	°C
Operating Temperature (Note 2)	T_{OPR}	-40 ~ +125	°C
Storage temperature	T_{STG}	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. This condition is only determined from design. It can't be 100% tested in mass production.

■ RECOMMENDED OPERATING RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	12	V

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	150	°C/W

■ ELECTRICAL CHARACTERISTICS

(T_A=25°C, refer to the test circuits, T_J=0 ~ 125°C, C_O=10μF unless otherwise specified)

For LD1119A-3.3

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_{OUT}	V_{IN}=5.3V, I_{OUT}=10mA, T_J=25°C	3.234	3.300	3.366	V
Output Voltage	V_{OUT}	V_{IN}=4.75 to 10V, I_{OUT}=0~1000mA	3.234	3.300	3.366	V
Line Regulation	ΔV_{OUT}	V_{IN}=4.75 to 12V, I_{OUT}=0mA		1	8	mV
Load Regulation	ΔV_{OUT}	V_{IN}=4.75V, I_{OUT}=0~800mA		1	10	mV
Operating Input Voltage	V_{IN}	I_{OUT}=100mA			12	V
Quiescent Current	I_Q	V_{IN}≤12V		5	10	mA
Current Limit	I_{LIMIT}	V_{IN}=8.3V, T_J=25°C	1000			mA
Output Noise Voltage	e_N	B=10Hz to 10KHz, T_J=25°C		100		μV
Supply Voltage Rejection	SVR	I_{OUT}=40mA, f=120Hz, T_J=25°C, V_{IN}=6.3V, V_{RIPPLE}=1V_{PP}		70		dB
Dropout Voltage	V_D	I_{OUT}=800mA		1.2	1.4	V

For LD1119A-ADJ

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reference Voltage	V_{REF}	V_{IN}-V_{OUT}=2V, I_{OUT}=10mA, T_J=25°C	1.225	1.25	1.275	V
Reference Voltage	V_{REF}	V_{IN}-V_{OUT}=1.4 to 10V, I_{OUT}=10~1000mA	1.225	1.25	1.275	V
Line Regulation	ΔV_{OUT}	V_{IN}-V_{OUT}=1.5 to 12V, I_{OUT}=10mA		0.1	0.5	%
Load Regulation	ΔV_{OUT}	V_{IN}-V_{OUT}=3V, I_{OUT}=10~800mA		0.1	0.4	%
Operating Input Voltage	V_{IN}				12	V
Adjustment Pin Current	I_{ADJ}	V_{IN}≤12V		100	150	μA
Adjustment Pin Current Change	ΔI_{ADJ}	V_{IN}-V_{OUT}=1.4 to 10V I_{OUT}=10 ~ 1000mA		1	5	μA
Minimum Load Current	$I_{O(MIN)}$	V_{IN}=12V		2	5	mA
Current Limit	I_{LIMIT}	V_{IN}-V_{OUT}=5V, T_J=25°C	1000			mA
Supply Voltage Rejection	SVR	I_{OUT}=40mA, f=120Hz, T_J=25°C, V_{IN}-V_{OUT}=3V, V_{RIPPLE}=1V_{PP}		70		dB
Dropout Voltage	V_D	I_{OUT}=800mA		1.2	1.4	V

■ APPLICATION NOTE of LD1119A ADJUSTABLE

The **LD1119A** adjustable has a reference voltage of between the OUT and ADJ/GND pins. I_{ADJ} is 60μA typ. (120μA max.) and ΔI_{ADJ} is 1μA typ. (5μA max.).
R_1 is normally fixed to 120Ω.
From figure 6 we obtain:
$V_{OUT}=V_{REF}+R_2(I_{ADJ}+I_{R1})=V_{REF} + R_2(I_{ADJ}+V_{REF}/R_1)=V_{REF}(1+R_2/R_1) + R_2 \times I_{ADJ}$.

Usually R_2 value is in the range of few KΩ, so the R_2 X I_{ADJ} product could be neglected; then the above expression becomes: $V_{OUT}=V_{REF}(1+R_2/R_1)$
For better load regulation, realize a good Kelvin connection of R_1 and R_2 is important. Particularly R_1 connection must be realized very close to OUT and ADJ/GND pin, while R_2 ground connection must be placed as near as possible to the negative Load pin. Ripple rejection can be improved by introducing a 10μF electrolytic capacitor placed in parallel to the R_2 resistor (See Fig. 8)

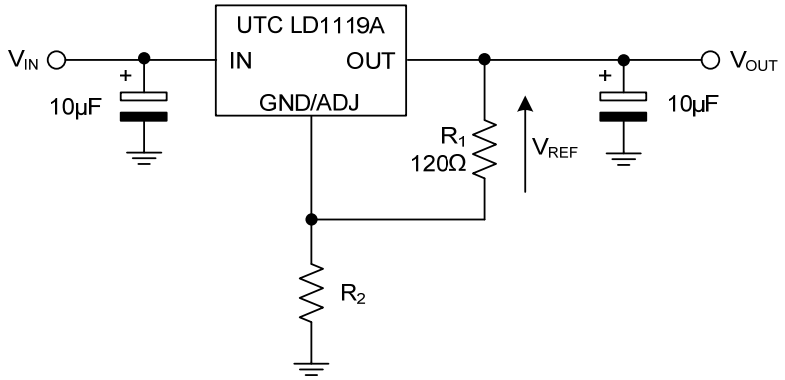


Fig.6 Adjustable Output Voltage Application Circuit

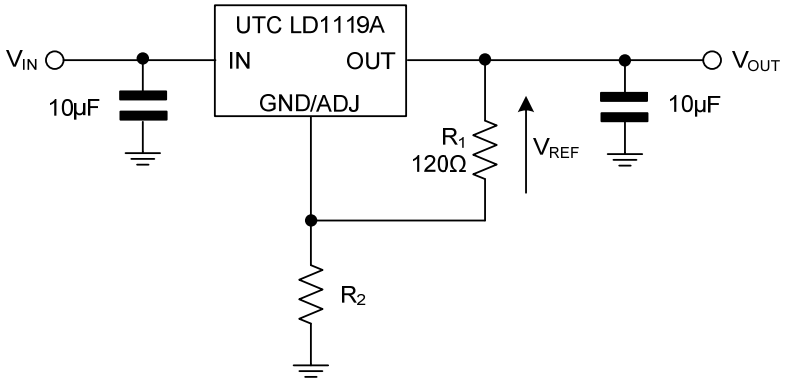


Fig.7 Adjustable Output Voltage Application Circuit (FOR MLCC)

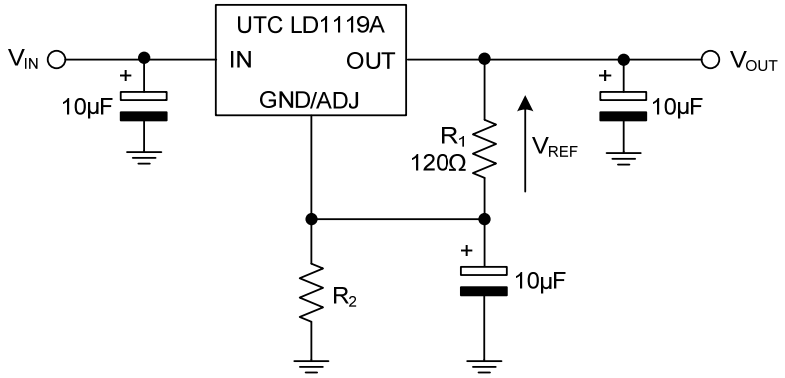


Fig.8 Adjustable Output Voltage Application with improved Ripple Rejection.