

# UNISONIC TECHNOLOGIES CO., LTD

**USS5360X** 

Preliminary

PNP EPITAXIAL SILICON TRANSISTOR

# 60V PNP LOW SATURATION MEDIUM POWER TRANSISTOR

# DESCRIPTION

The **USS5360X** is an PNP low  $V_{CE(SAT)}$  Breakthrough In Small Signal (BISS) transistor in a medium power.

NPN complement: USS4360X.

## FEATURES

- \* Very low collector-emitter saturation voltage VCE(SAT)
- \* High collector current capability IC and ICM
- \* High collector current gain (h<sub>FE</sub>) at high IC
- \* High energy efficiency due to less heat generation
- \* Smaller required Printed-Circuit Board (PCB) area than for conventional transistors

## ORDERING INFORMATION



SOT-89

Ordering Number		Deekere	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
USS5360XL-AB3-R	USS5360XG-AB3-R	SOT-89	В	С	Е	Tape Reel	
Note: Pin Assignment: B: Bas	e C: Collector E: Emitter						



#### MARKING



#### ■ ABSOLUATE MAXIUM RATINGS (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	V <sub>CBO</sub>	-80	V
Collector to Emitter Voltage	V <sub>CEO</sub>	-60	V
Emitter to Base Voltage	V <sub>EBO</sub>	-7	V
Bese Current	Ι <sub>Β</sub>	-500	mA
Collector Current	lc	-3	А
Peak Collector Current (t <sub>P</sub> ≤1ms)	I <sub>CM</sub>	-6	А
Collector Dissipation	Pc	0.95	W
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Single pulse, P<sub>W</sub>=10ms.

#### THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ <sub>JA</sub>	132	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

#### ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =-100μA	-80			V
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =-1mA	-60			V
Emitter-Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =-100μA	-7			V
Collector-Base Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-48V, I <sub>E</sub> =0A			-100	nA
Collector-Emitter Cut-off Current	I <sub>CES</sub>	$V_{CE}$ =-48V, $V_{BE}$ =0V			-100	nA
Emitter-Base Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> =-5V, I <sub>C</sub> =0A			-100	nA
Base-Emitter On Voltage (Note)	V <sub>BE(ON)</sub>	$V_{CE}$ =-5V, $I_{C}$ =-1V			-1.1	V
Base-Emitter Saturation Voltage (Note)	$V_{\text{BE(SAT)}}$	I <sub>C</sub> =-1A, I <sub>B</sub> =-100mA			-1.2	V
Collector-Emitter Saturation Voltage (Note)	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-500mA, I <sub>B</sub> =-50mA			-150	mV
		I <sub>C</sub> =-1A, I <sub>B</sub> =-100mA			-200	mV
		I <sub>C</sub> =-2A, I <sub>B</sub> =-200mA			-450	mV
		I <sub>C</sub> =-3A, I <sub>B</sub> =-300mA			-550	mV
DC Current Transfer Ratio (Note)	h <sub>FE</sub>	I <sub>C</sub> =-50mA, V <sub>CE</sub> =-5V	150			
		I <sub>C</sub> =-500mA, V <sub>CE</sub> =-5V	130			
		I <sub>C</sub> =-1A, V <sub>CE</sub> =-5V	120			
		I <sub>C</sub> =-2A, V <sub>CE</sub> =-5V	100			
		I <sub>C</sub> =-3A, V <sub>CE</sub> =-5V	80			
Transition Frequency (Note)	f⊤	V <sub>CE</sub> =-10V, I <sub>C</sub> =-50mA, f=100MHz	65	130		MHz
Collector Capacitance	COB	V <sub>CB</sub> =-10V, I <sub>E</sub> =i <sub>e</sub> =0A, f=1MHz		28	32	рF

Note : Measured under pulsed conditions. Pulse Test: Pulse width  $\leq$  300µs, Duty cycle $\leq$ 2%.



Preliminary

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