



UT3N10

Power MOSFET

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

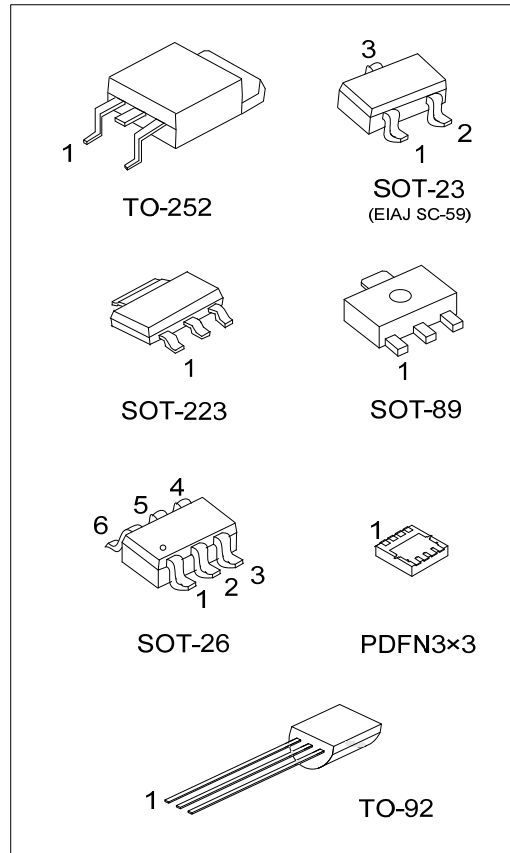
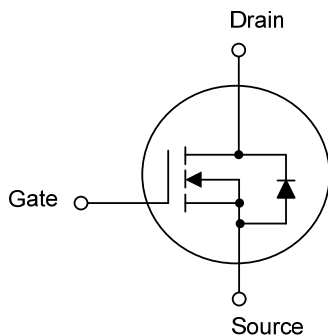
DESCRIPTION

The UTC **UT3N10** is an N-channel power MOSFET providing very low on-resistance. It has high efficiency and perfect cost-effectiveness. It can be generally applied in the commercial and industrial fields.

FEATURES

- * $R_{DS(ON)} \leq 165 \text{ m}\Omega @ V_{GS} = 10\text{V}, I_D = 3.0\text{A}$
- $R_{DS(ON)} \leq 180 \text{ m}\Omega @ V_{GS} = 4.5\text{V}, I_D = 2.0\text{A}$
- * Simple drive requirement

SYMBOL



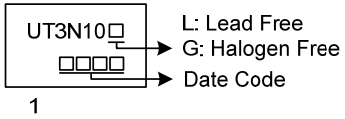
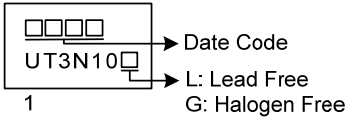
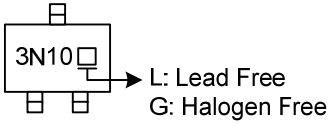
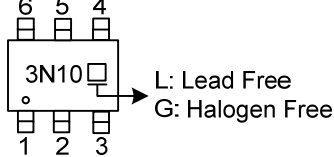
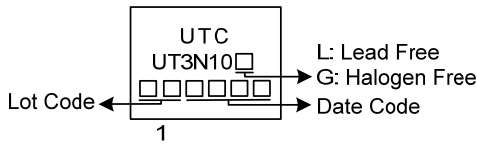
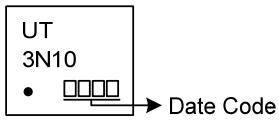
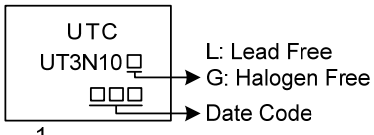
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT3N10L-AA3-R	UT3N10G-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-AB3-R	UT3N10G-AB3-R	SOT-89	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-AE3-R	UT3N10G-AE3-R	SOT-23	G	S	D	-	-	-	-	-	Tape Reel
UT3N10L-AG6-R	UT3N10G-AG6-R	SOT-26	D	D	G	S	D	D	-	-	Tape Reel
UT3N10L-TN3-R	UT3N10G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT3N10L-T92-B	UT3N10G-T92-B	TO-92	G	D	S	-	-	-	-	-	Tape Box
UT3N10L-T92-K	UT3N10G-T92-K	TO-92	G	D	S	-	-	-	-	-	Bulk
UT3N10L-P3030-R	UT3N10G-P3030-R	PDFN3x3	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT3N10G-AA3-R</p>	<p>(1) R: Tape Reel</p> <p>(2) AA3: SOT-223, AB3: SOT-89, AE3: SOT-23 AG6: SOT-26, TN3: TO-252, T92: TO-92 P3030: PDFN3x3</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING

<p style="text-align: center;">SOT-223</p>  <p style="text-align: center;">1</p>	<p style="text-align: center;">SOT-89</p>  <p style="text-align: center;">1</p>
<p style="text-align: center;">SOT-23</p> 	<p style="text-align: center;">SOT-26</p> 
<p style="text-align: center;">TO-252</p>  <p style="text-align: center;">1</p>	<p style="text-align: center;">PDFN3x3</p> 
<p style="text-align: center;">TO-92</p>  <p style="text-align: center;">1</p>	<p style="text-align: center;">-</p> <p style="text-align: center;">-</p>

■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ($V_{GS}=4.5\text{V}$, $T_A=25^\circ\text{C}$) (Note 2)		I_D	3.0	A
Pulsed Drain Current (Note 3, 4)		I_{DM}	10	A
Power Dissipation ($T_A=25^\circ\text{C}$)	SOT-223	P_D	0.89	W
	SOT-89		0.55	W
	SOT-23		0.35	W
	SOT-26			
	TO-252		2	W
	TO-92		0.4	W
	PDFN3x3		0.96	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Surface mounted on 1 in² copper pad of FR4 board; 270 $^\circ\text{C}/\text{W}$ when mounted on min. copper pad.

3. Repetitive Rating: Pulse width limited by maximum junction temperature.

4. Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient (Note)	SOT-223	θ_{JA}	140	$^\circ\text{C}/\text{W}$
	SOT-89		180	$^\circ\text{C}/\text{W}$
	SOT-23		350	$^\circ\text{C}/\text{W}$
	SOT-26			
	TO-252		62.5	$^\circ\text{C}/\text{W}$
	TO-92		312	$^\circ\text{C}/\text{W}$
	PDFN3x3		130	$^\circ\text{C}/\text{W}$

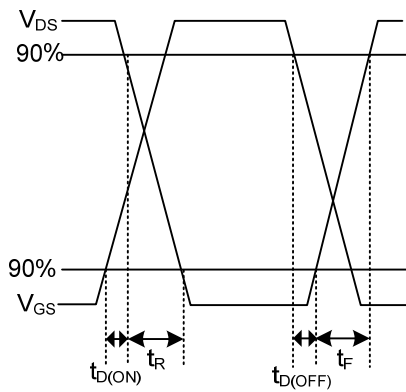
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

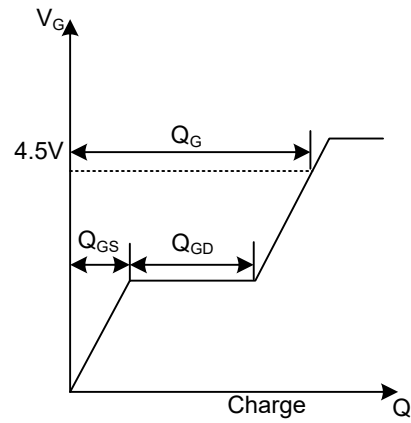
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$			10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 20V$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0		3.0	V
Drain to Source On-state Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 3.0A$			165	$m\Omega$
		$V_{GS} = 4.5V, I_D = 2.0A$			180	$m\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$		510		pF
Output Capacitance	C_{OSS}			36		pF
Reverse Transfer Capacitance	C_{RSS}			26		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note)	Q_G	$V_{GS} = 10V, V_{DS} = 80V, I_D = 3A$		18.2		nC
Gate Source Charge	Q_{GS}			3		nC
Gate Drain Charge	Q_{GD}			4.1		nC
Turn-ON Delay Time (Note)	$t_{D(ON)}$	$V_{GS} = 10V, V_{DS} = 50V, I_D = 3A, R_G = 25\Omega$		6		ns
Turn-ON Rise Time	t_R			15		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			44		ns
Turn-OFF Fall-Time	t_F			25		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage (Note)	V_{SD}	$I_S = 1.2A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_S = 3A, V_{GS} = 0V, di/dt = 100A/\mu s$		39		ns
Reverse Recovery Charge	Q_{rr}			40		nC

Note: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

■ TEST WAVEFORMS

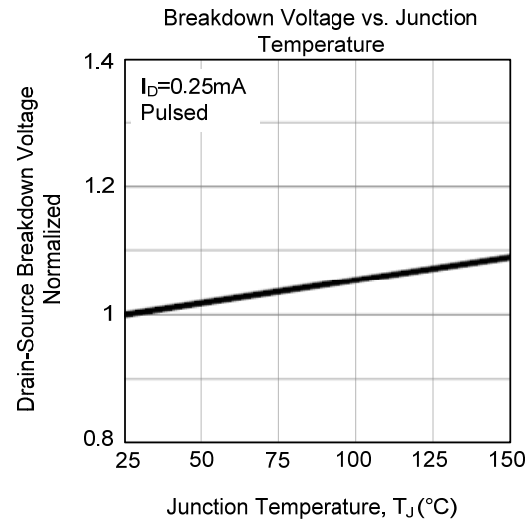
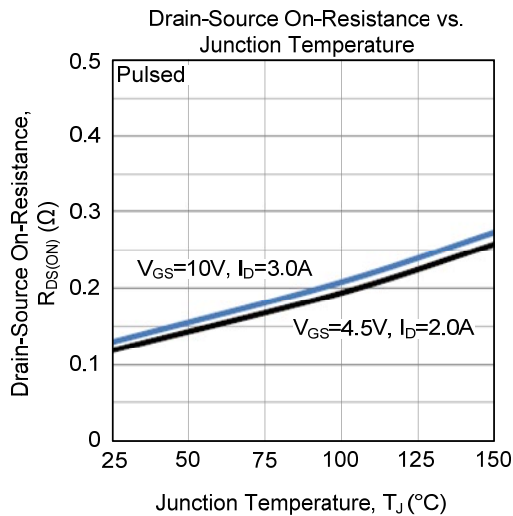
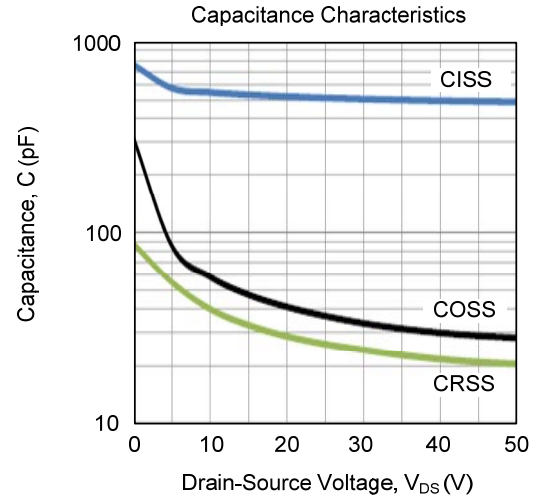
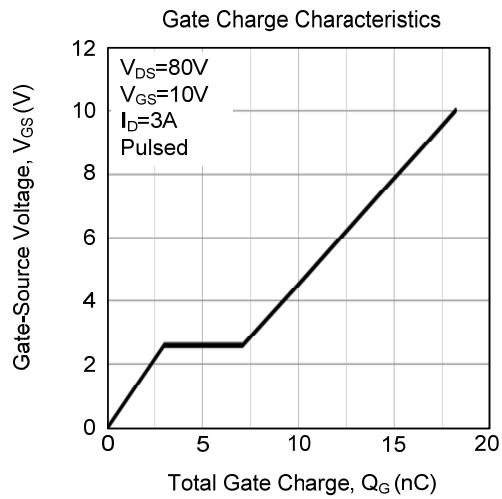
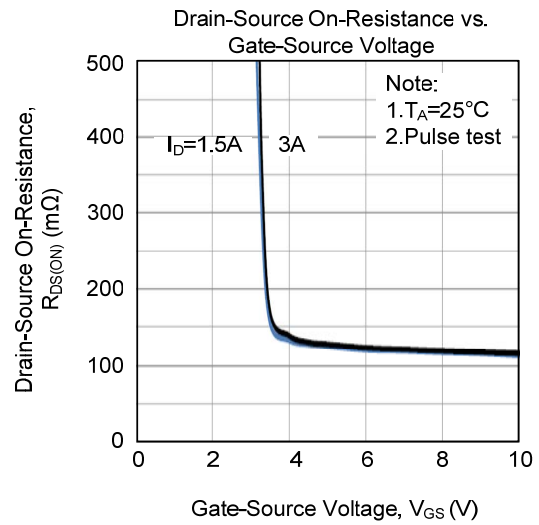
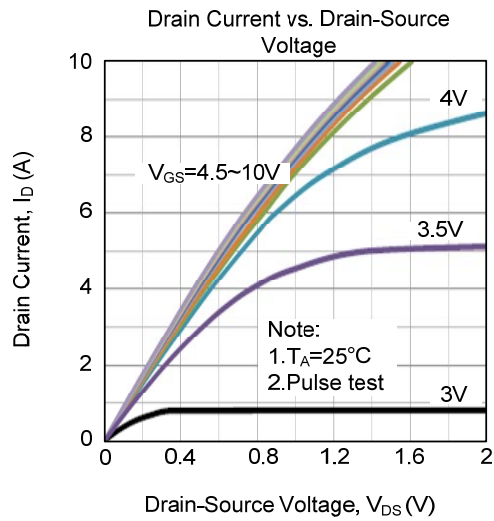


Switching Time Waveform

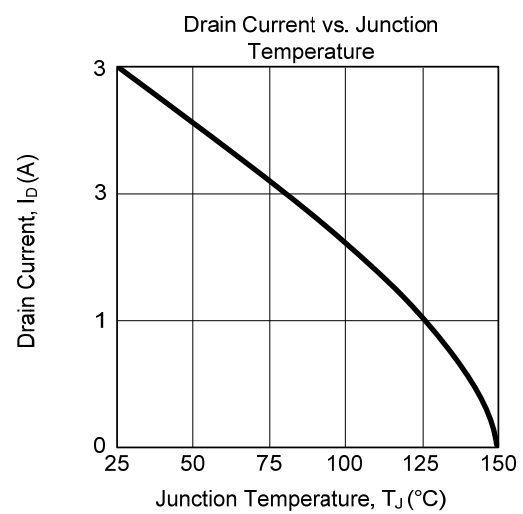
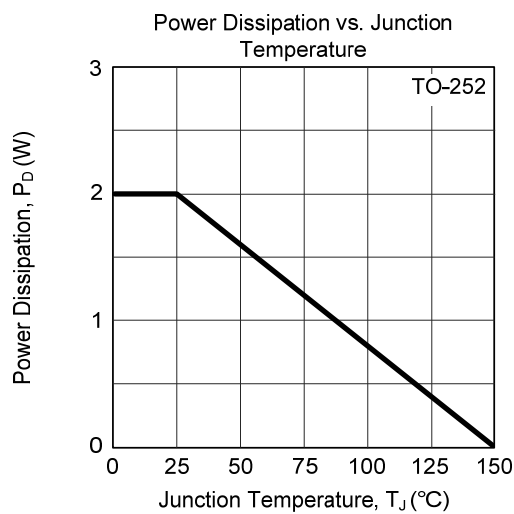
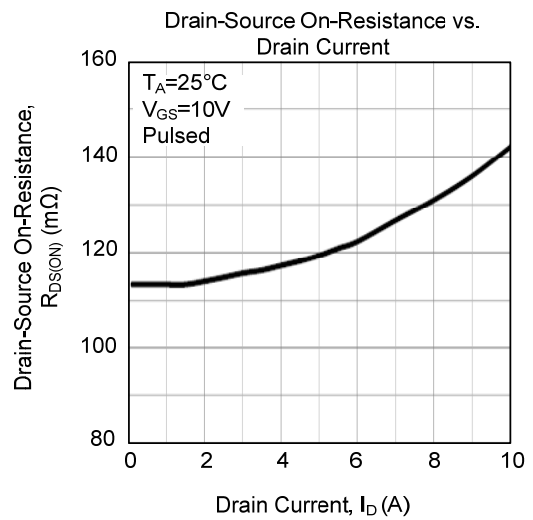
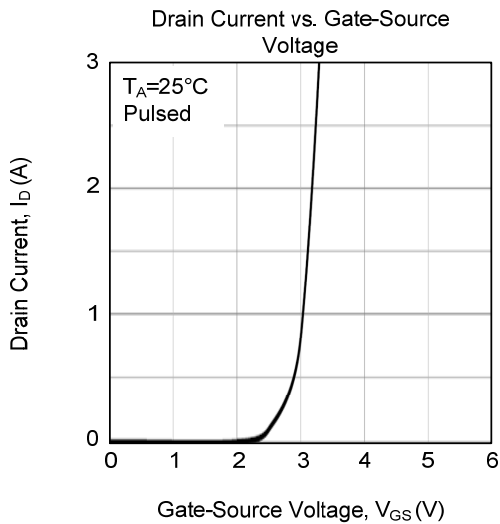
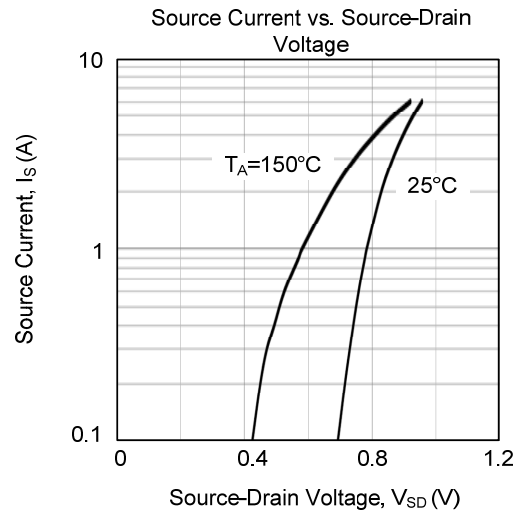
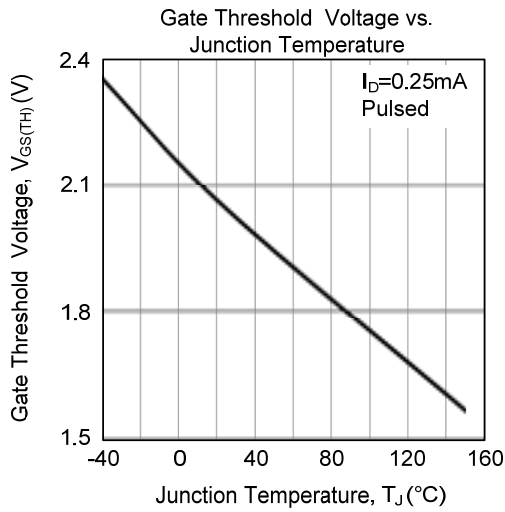


Gate Charge Waveform

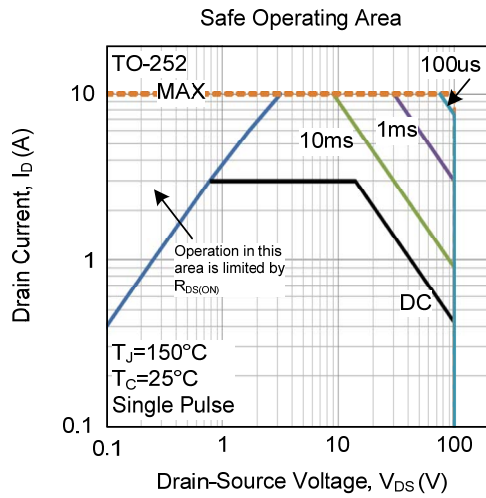
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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