



5N60-TC2

Power MOSFET

5A, 600V N-CHANNEL POWER MOSFET

■ DESCRIPTION

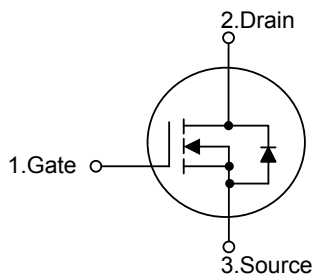
The UTC **5N60-TC2** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **5N60-TC2** is generally applied in high efficiency switch mode power supplies.

■ FEATURES

- * $R_{DS(ON)} < 1.8\Omega @ V_{GS} = 10V, I_D = 2.5A$
- * High Switching Speed

■ SYMBOL

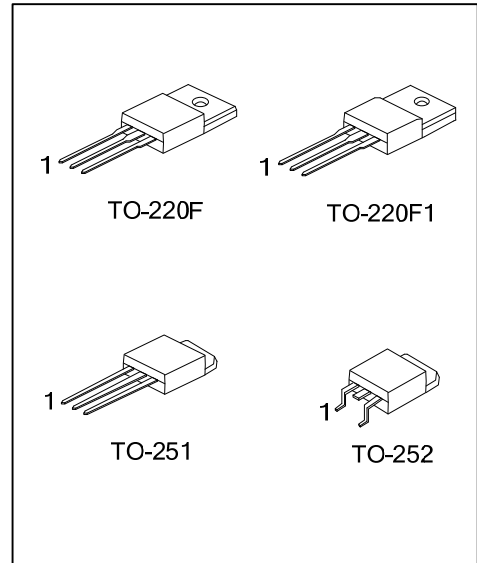


■ ORDERING INFORMATION

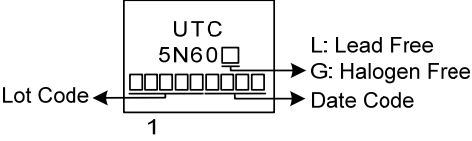
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5N60L-TF1-T	5N60G-TF1-T	TO-220F1	G	D	S	Tube
5N60L-TF3-T	5N60G-TF3-T	TO-220F	G	D	S	Tube
5N60L-TM3-T	5N60G-TM3-T	TO-251	G	D	S	Tube
5N60L-TN3-R	5N60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>5N60G-TF1-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TF1: TO-220F1, TF3: TO-220F, TM3: TO-251, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	5	A
	Pulsed (Note 2)	I_{DM}	10	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	125	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.4	V/ns
Power Dissipation	TO-220F/TO-220F1	P_D	36	W
	TO-251/TO-252		54	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 5.0\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$ Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 5.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F/TO-220F1	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		110 (Note)	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220F/TO-220F1	θ_{JC}	3.47	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		2.31 (Note)	$^\circ\text{C}/\text{W}$

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

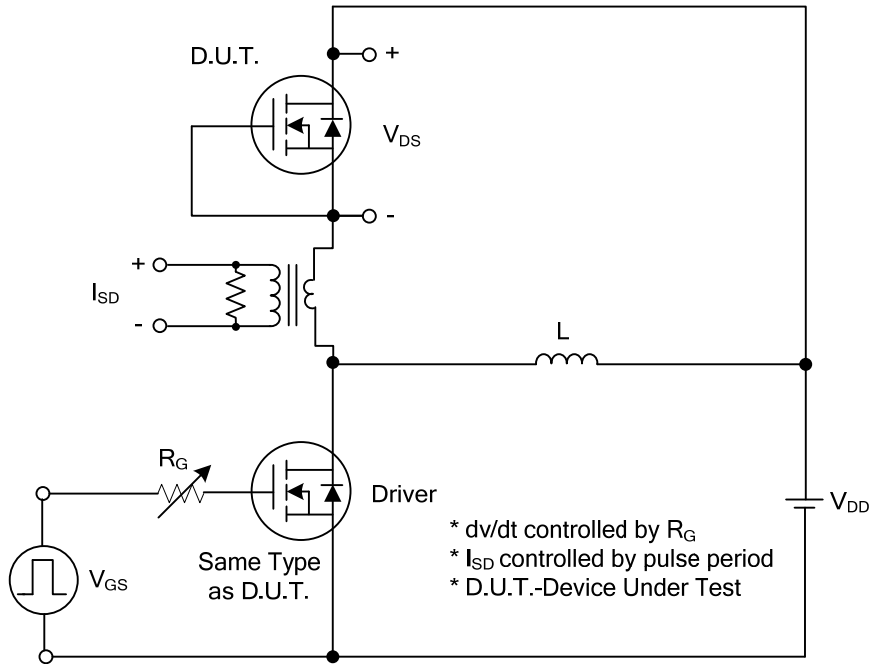
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}			100	nA
	Reverse				-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.5A$			1.8	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=25V, f=1.0\text{ MHz}$		605		pF
Output Capacitance	C_{OSS}			67		pF
Reverse Transfer Capacitance	C_{RSS}			4		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=100V, V_{GS}=10V, I_D=5.0A$ $I_G=1mA$ (Note 1, 2)		15		nC
Gate-source Charge	Q_{GS}			6.2		nC
Gate-drain Charge	Q_{GD}			2.6		nC
Turn-on Delay Time (Note 1)	$t_{D(ON)}$	$V_{DS}=30V, V_{GS}=10V, I_D=0.5A,$ $R_G=25\Omega$ (Note 1, 2)		44		ns
Rise Time	t_R			35		ns
Turn-off Delay Time	$t_{D(OFF)}$			128		ns
Fall-Time	t_F			30		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				5	A
Maximum Body-Diode Pulsed Current	I_{SM}				10	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$V_{GS}=0V, I_S=5.0A$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$V_{GS}=0V, I_S=5.0A,$		294		ns
Reverse Recovery Charge	Q_{rr}	$di_F/dt=100A/\mu s$ (Note1)		2.34		μC

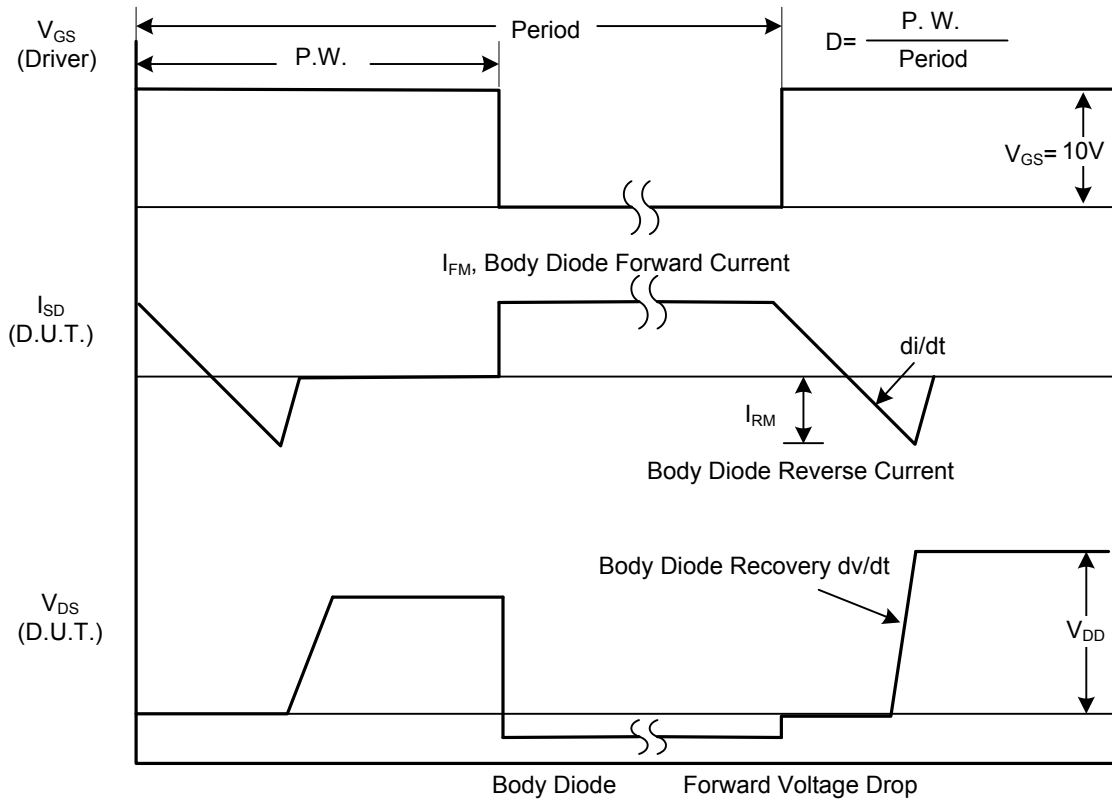
Notes: 1. Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

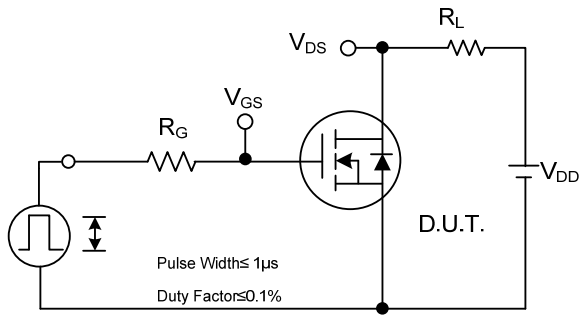


Peak Diode Recovery dv/dt Test Circuit

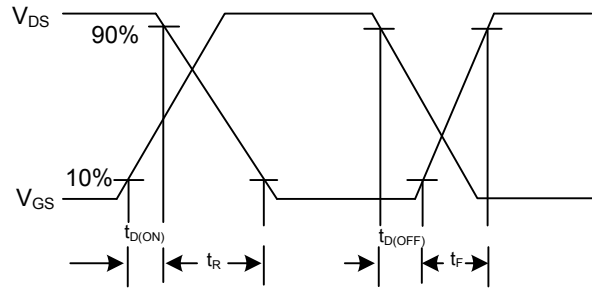


Peak Diode Recovery dv/dt Waveforms

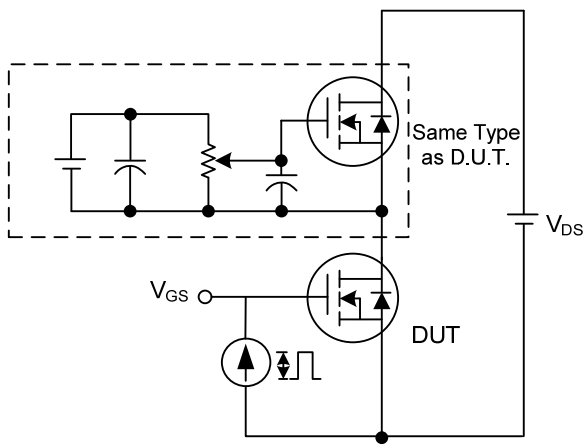
■ TEST CIRCUITS AND WAVEFORMS



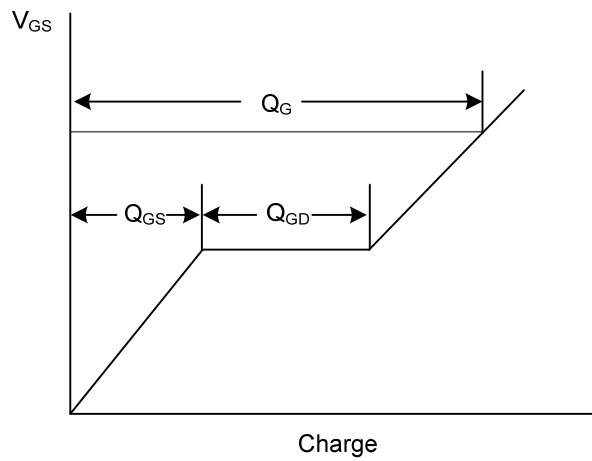
Switching Test Circuit



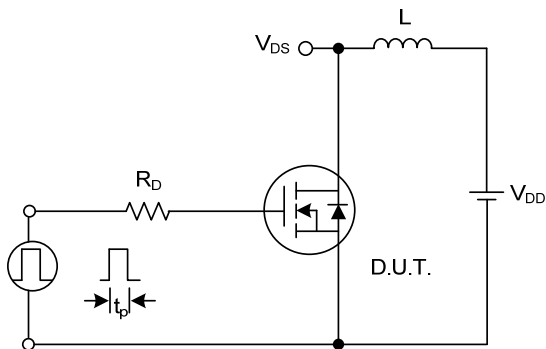
Switching Waveforms



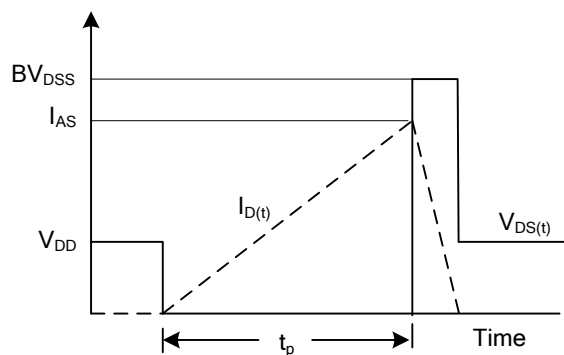
Gate Charge Test Circuit



Gate Charge Waveform

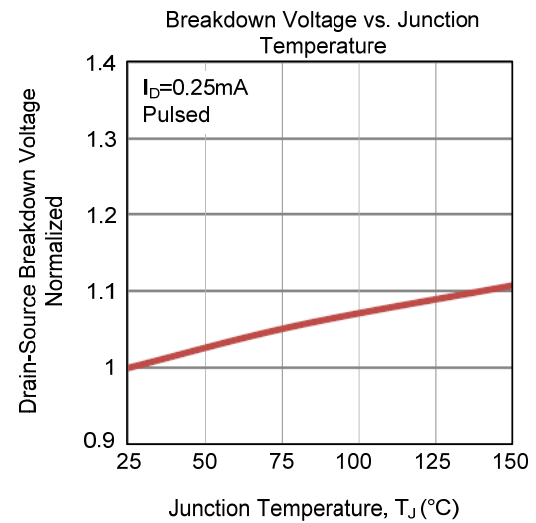
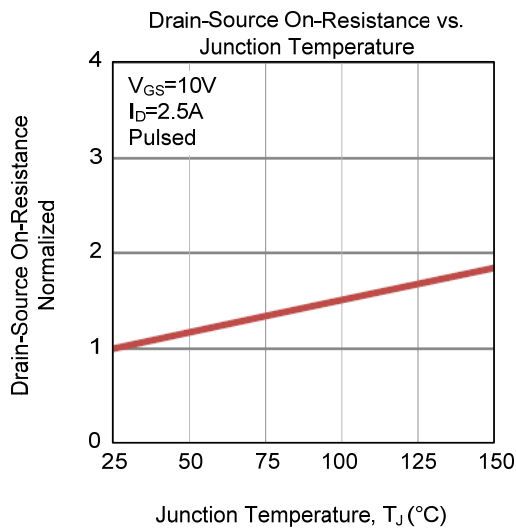
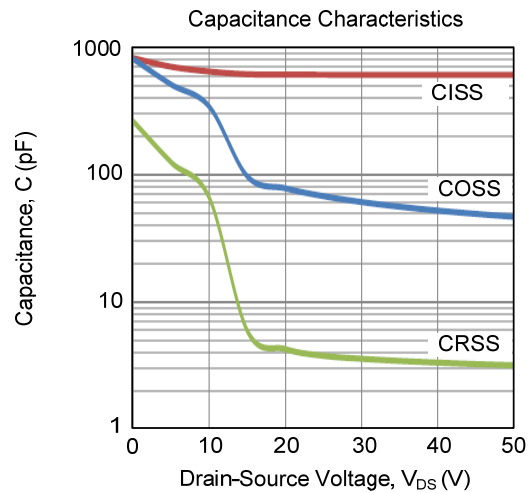
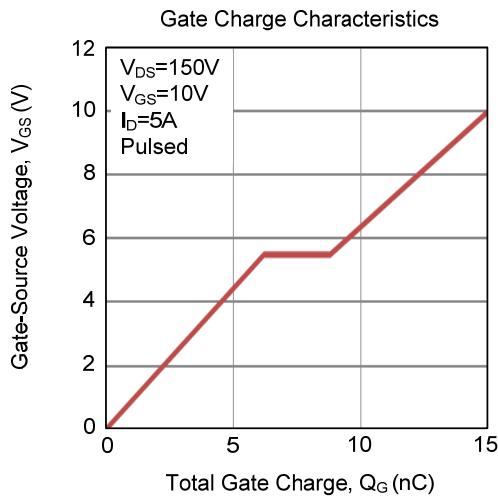
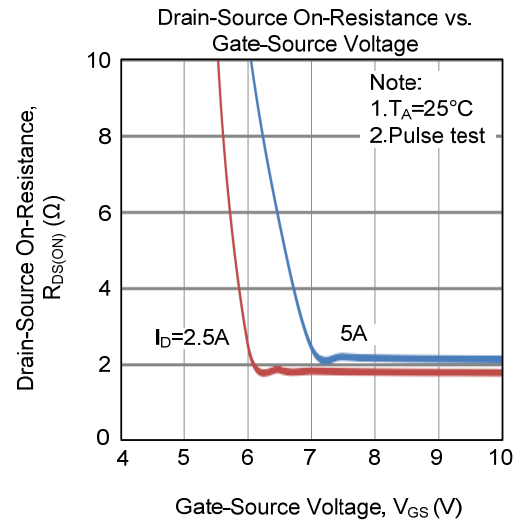
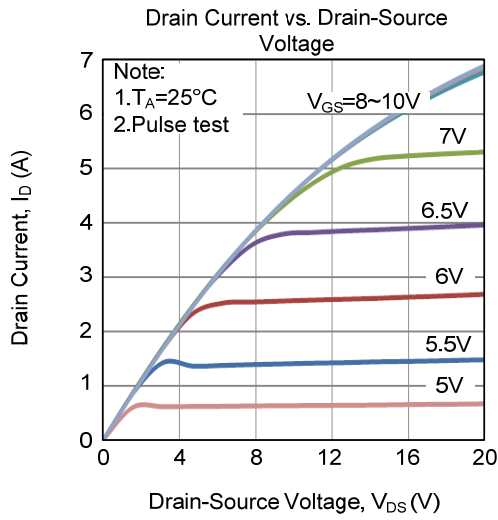


Unclamped Inductive Switching Test Circuit

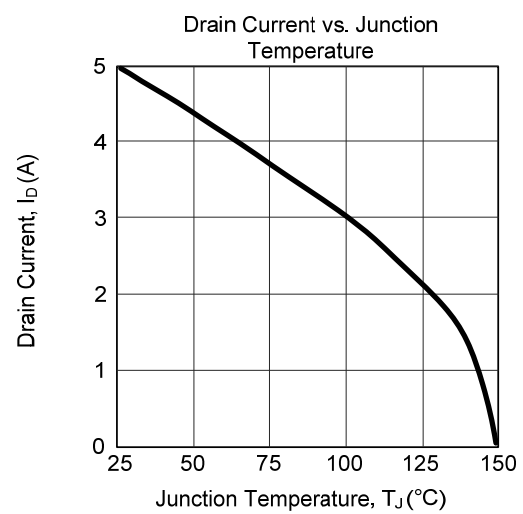
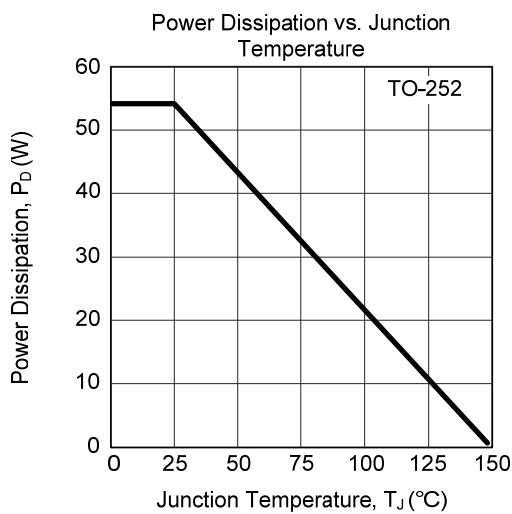
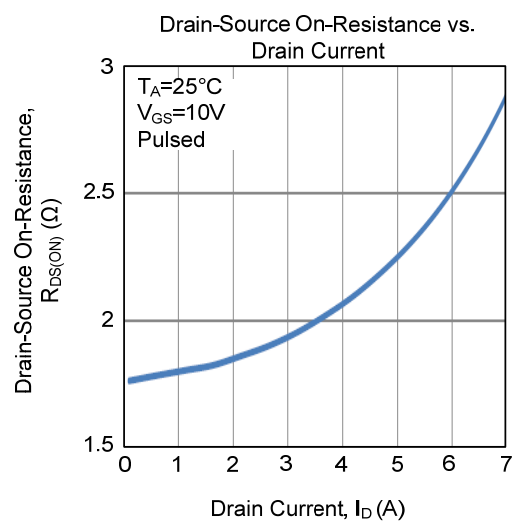
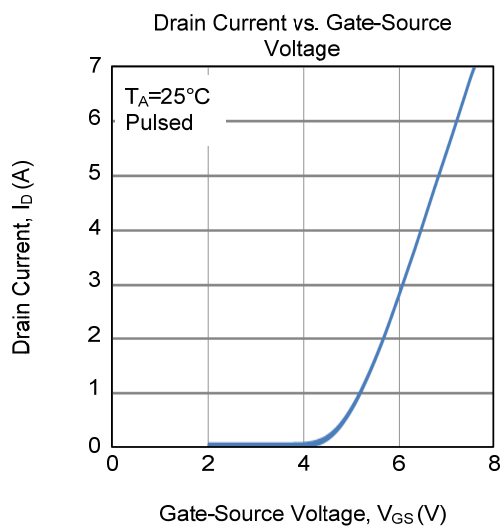
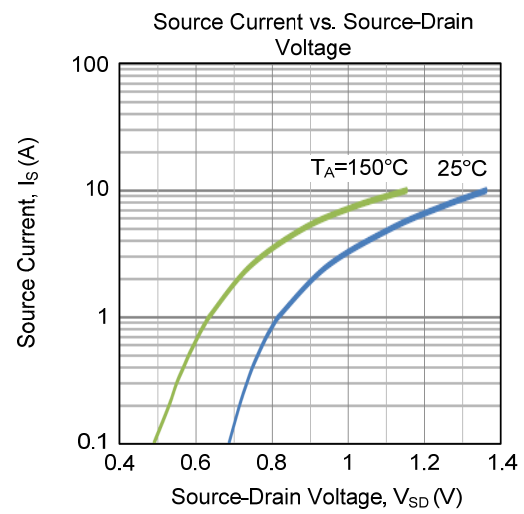
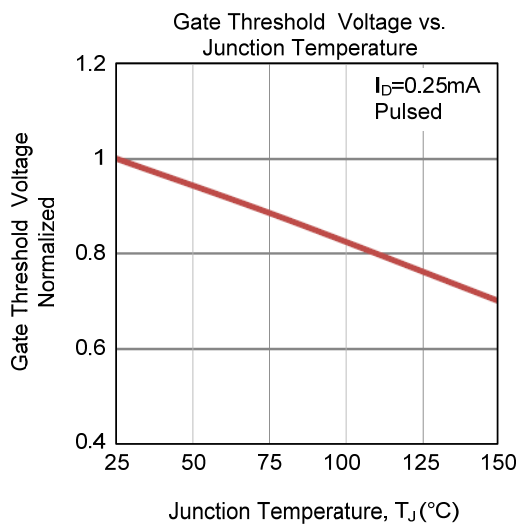


Unclamped Inductive Switching Waveforms

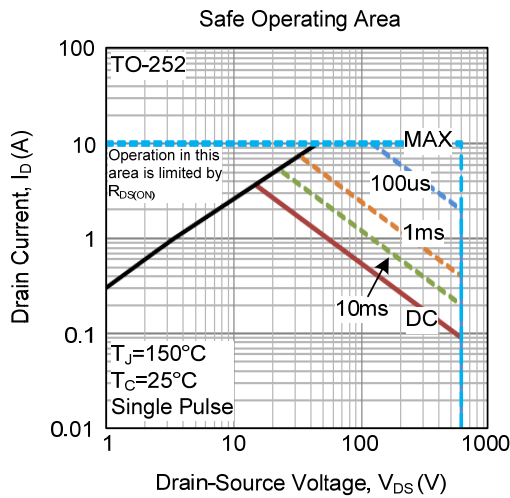
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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