



12N60-TC

Power MOSFET

12A, 600V N-CHANNEL POWER MOSFET

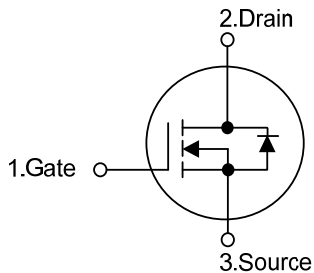
DESCRIPTION

The **UTC 12N60-TC** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} < 0.7\Omega @ V_{GS} = 10V, I_D = 6.0A$
- * Fast switching
- * Improved dv/dt capability

SYMBOL



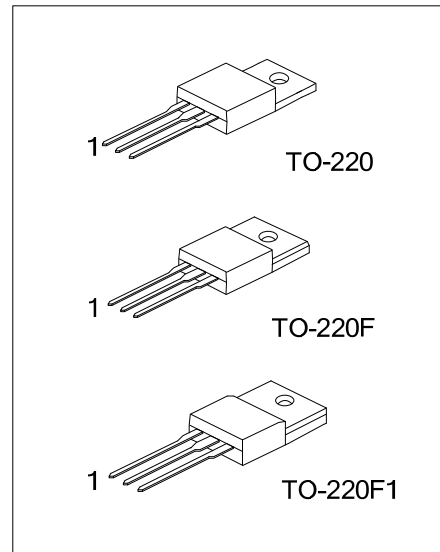
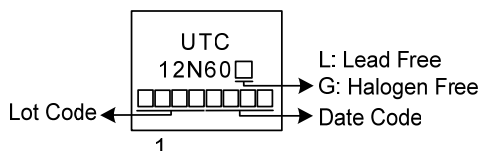
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N60L-TA3-T	12N60G-TA3-T	TO-220	G	D	S	Tube
12N60L-TF1-T	12N60G-TF1-T	TO-220F1	G	D	S	Tube
12N60L-TF3-T	12N60G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

<p>12N60G-TA3-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube</p> <p>(2) TA3: TO-220, TF1: TO-220F1, TF3: TO-220F</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	12	A
	Pulsed (Note 2)	I_{DM}	24	A
Avalanche Energy		E_{AS}	304	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.1	V/ns
Power Dissipation	TO-220	P_D	150	W
	TO-220F/TO-220F1		36	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. $L = 10\text{mH}$, $I_{AS} = 7.8\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 12\text{A}$, $di/dt \leq 200\text{A/s}$, $V_{DD} \leq BV_{DSS}$ Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ_{JA}	62.5	$^\circ\text{C/W}$
Junction to Case	TO-220	θ_{JC}	0.83	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.47	$^\circ\text{C/W}$

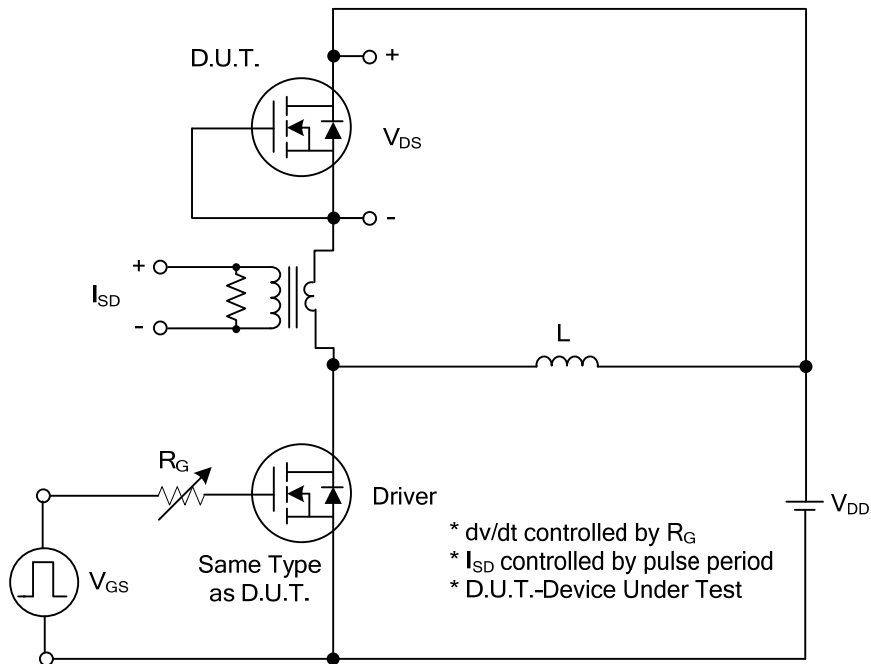
■ ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=600\text{V}$, $V_{GS}=0\text{V}$			10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}$, $V_{DS}=0\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=6.0\text{A}$			0.7	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$		1820		pF
Output Capacitance	C_{OSS}			166		pF
Reverse Transfer Capacitance	C_{RSS}			5		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	$V_{DS}=100\text{V}$, $I_D=12\text{A}$, $V_{GS}=10\text{V}$ $I_G=1\text{mA}$ (Note1,2)		33		nC
Gate-Source Charge	Q_{GS}			13		nC
Gate-Drain Charge	Q_{GD}			7		nC
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=100\text{V}$, $I_D=12\text{A}$, $R_G=25\Omega$ (Note1,2)		26		ns
Turn-On Rise Time	t_R			21		ns
Turn-Off Delay Time	$t_{D(OFF)}$			83		ns
Turn-Off Fall Time	t_F			29		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				12	A
Maximum Body-Diode Pulsed Current	I_{SM}				24	A
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}$, $I_S=12\text{A}$			1.4	V
Reverse Recovery Time	t_{rr}	$V_{GS}=0\text{V}$, $I_S=12\text{A}$, $di/dt=100\text{A}/\mu\text{s}$		380		ns
Reverse Recovery Charge	Q_{rr}			5.3		μC

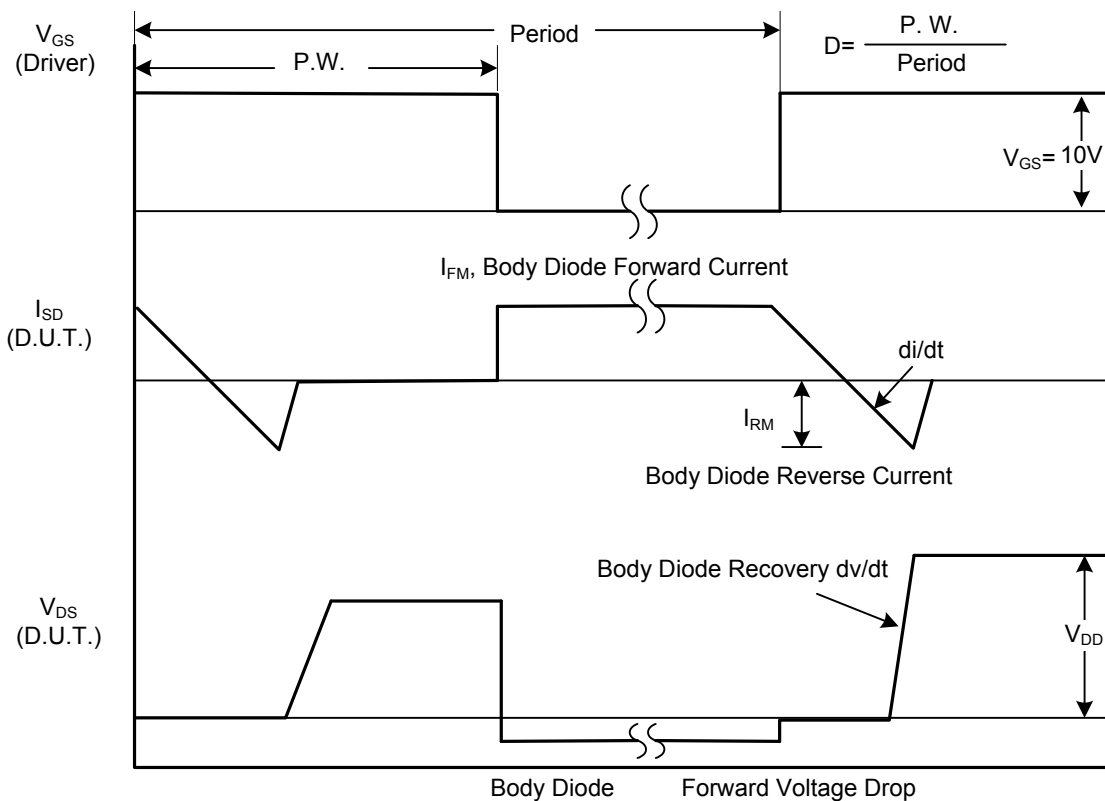
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

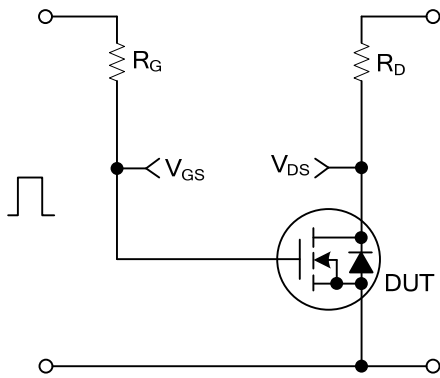


Peak Diode Recovery dv/dt Test Circuit

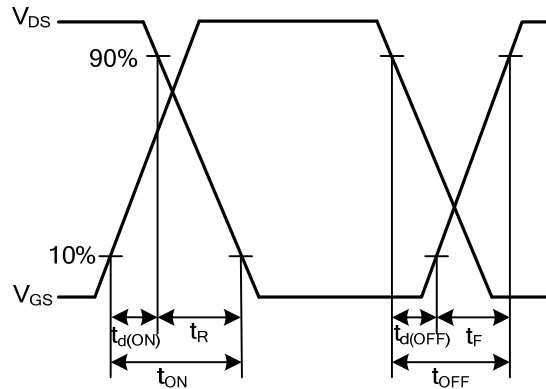


Peak Diode Recovery dv/dt Waveforms

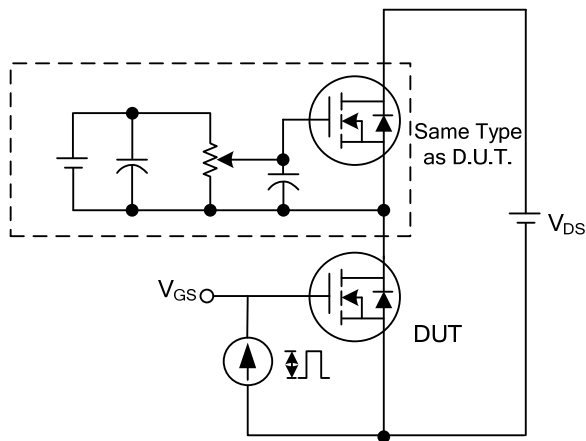
TEST CIRCUITS AND WAVEFORMS



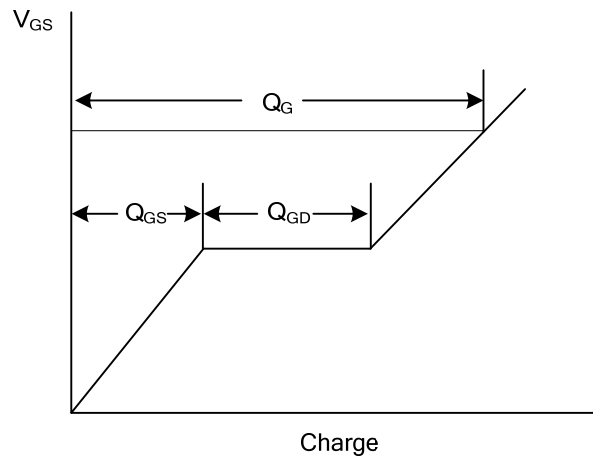
Switching Test Circuit



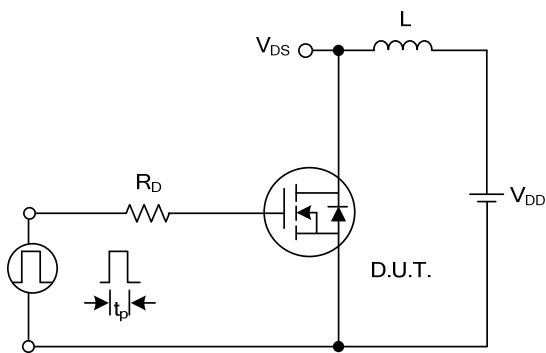
Switching Waveforms



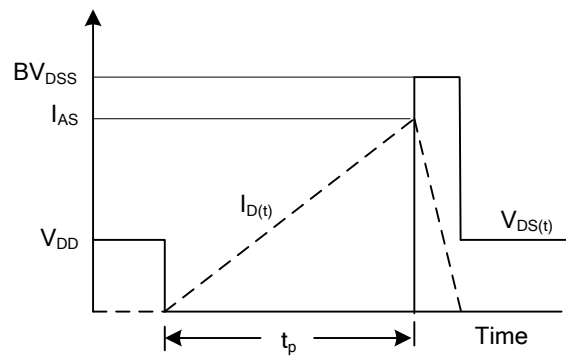
Gate Charge Test Circuit



Gate Charge Waveform

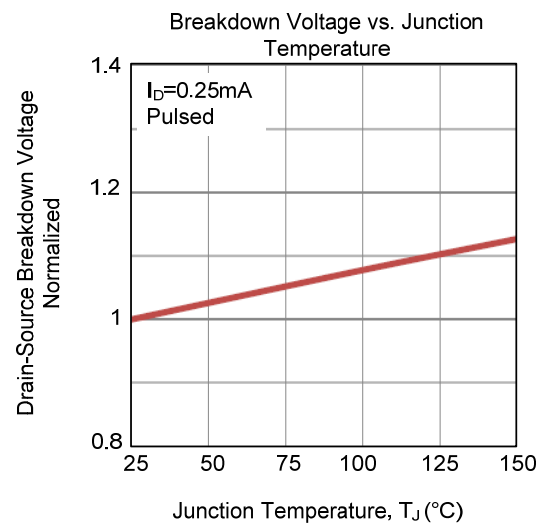
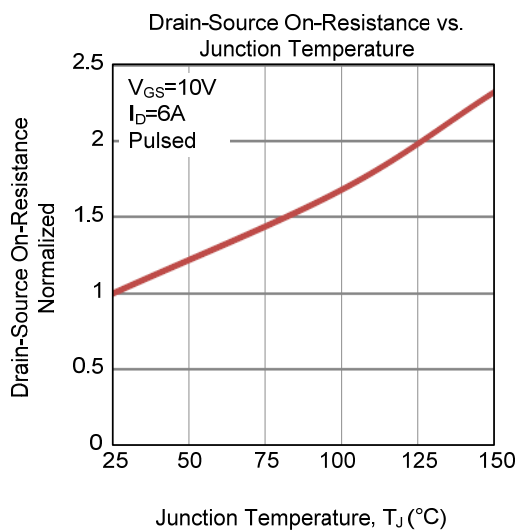
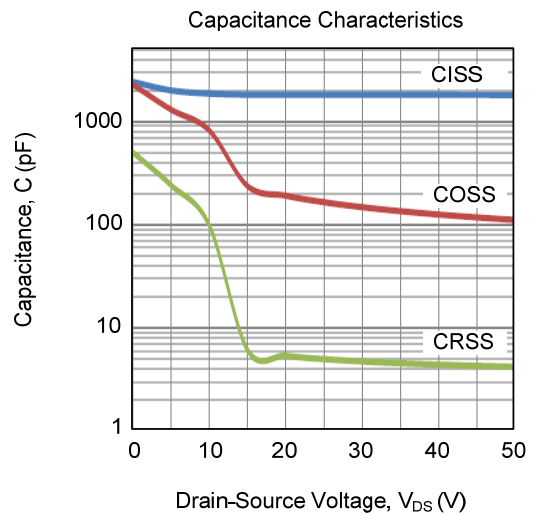
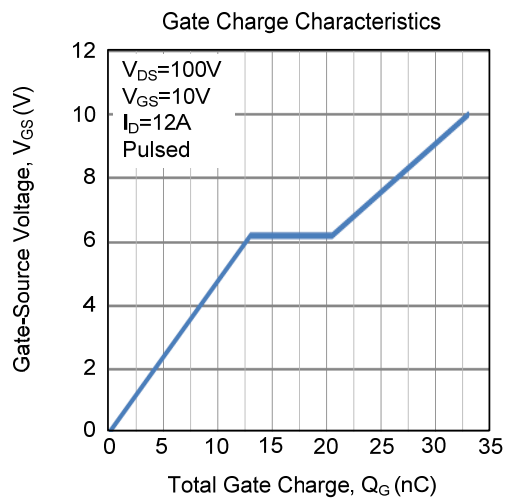
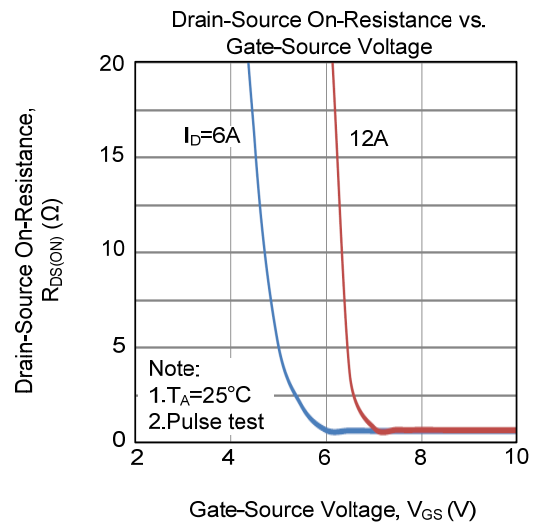
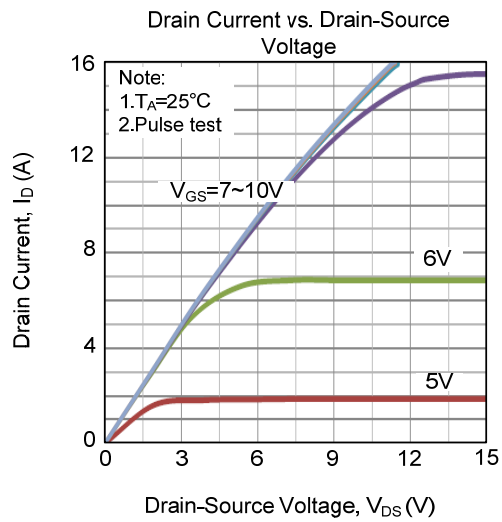


Unclamped Inductive Switching Test Circuit

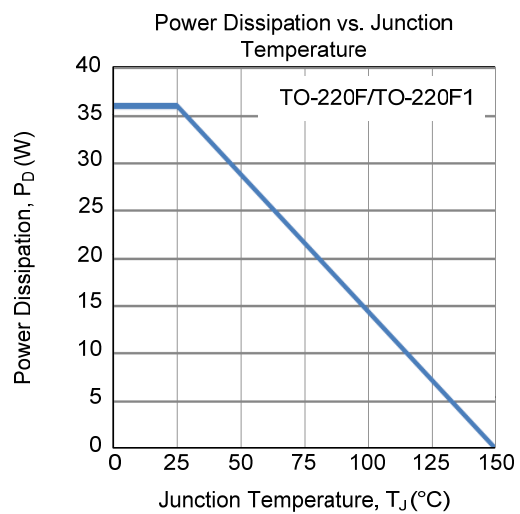
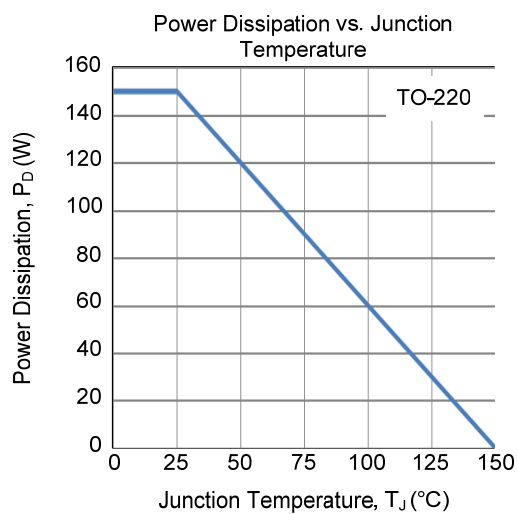
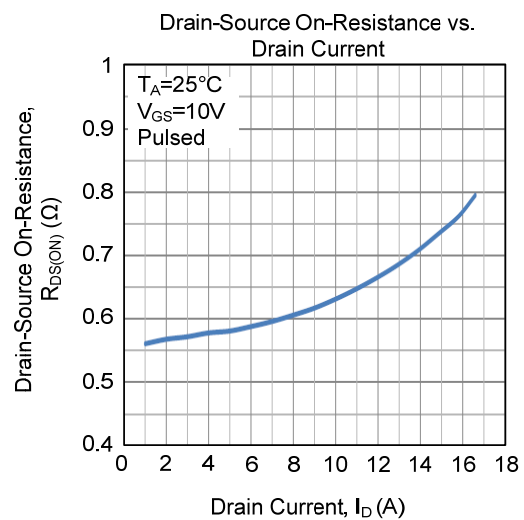
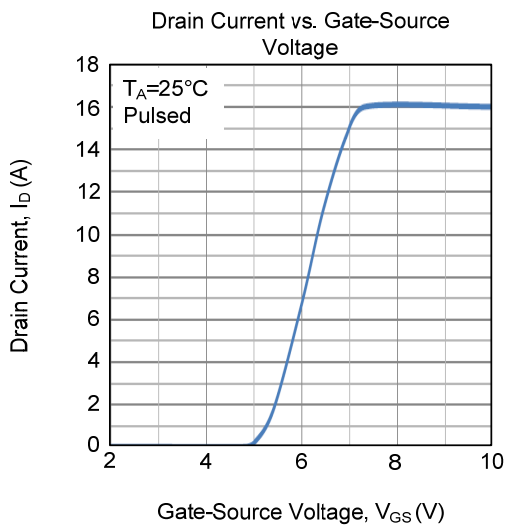
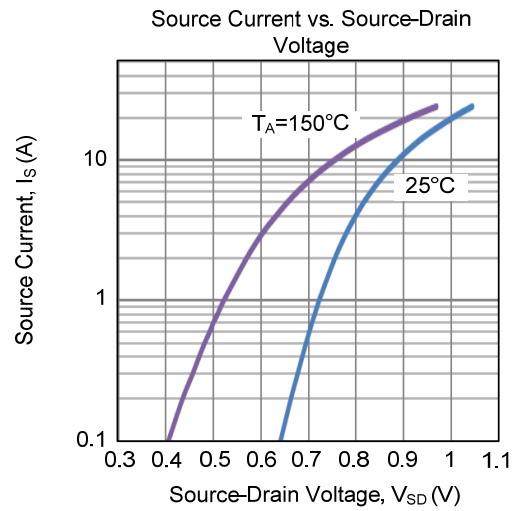
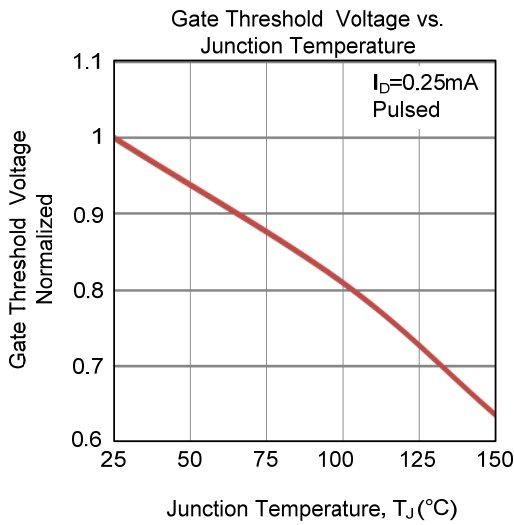


Unclamped Inductive Switching Waveforms

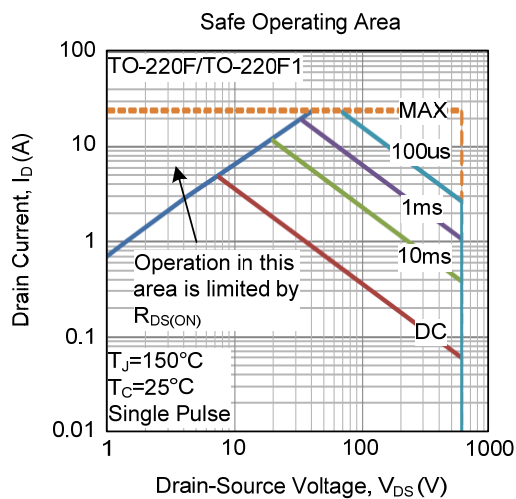
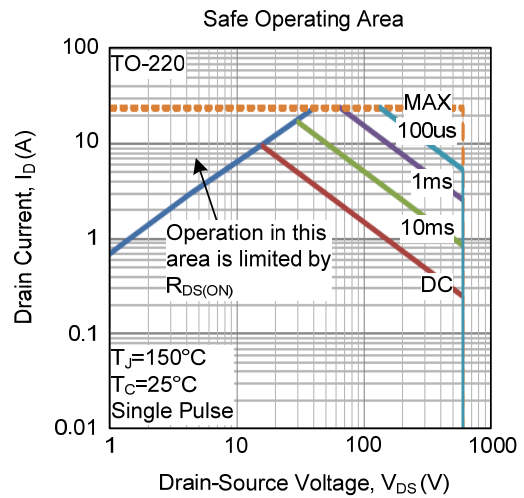
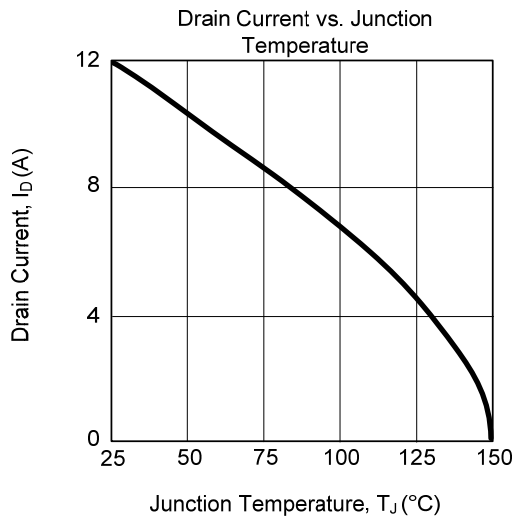
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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