

# 5N80-CQ

**Power MOSFET**

## 5A, 800V N-CHANNEL POWER MOSFET

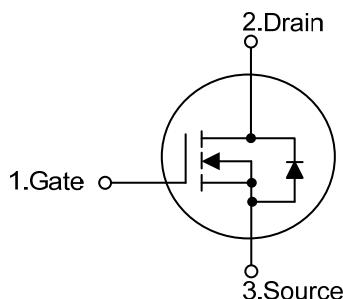
### ■ DESCRIPTION

The UTC 5N80-CQ is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

### ■ FEATURES

- \*  $R_{DS(ON)} \leq 3.8 \Omega$  @  $V_{GS}=10V$ ,  $I_D=2.0A$
- \* Low Reverse Transfer Capacitance
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

### ■ SYMBOL



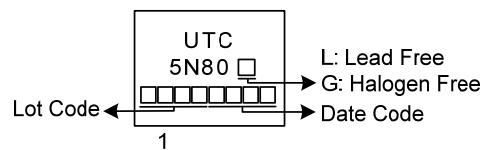
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5N80L-TA3-T	5N80G-TA3-T	TO-220	G	D	S	Tube
5N80L-TF3-T	5N80G-TF3-T	TO-220F	G	D	S	Tube
5N80L-TF1-T	5N80G-TF1-T	TO-220F1	G	D	S	Tube
5N80L-TM3-T	5N80G-TM3-T	TO-251	G	D	S	Tube
5N80L-TN3-R	5N80G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

5N80G-TA3-T  (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TM3: TO-251, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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## ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	800	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	5	A
	Pulsed (Note 2)	$I_{DM}$	8	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	78.5	mJ
Peak Diode Recovery $dv/dt$ (Note 4)		$dv/dt$	2.6	V/ns
Power Dissipation	TO-220	$P_D$	125	W
	TO-220F/TO-220F1		40	W
	TO-251/TO-252		54	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L=10\text{mH}$ ,  $I_{AS}=3.96\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 5.0\text{A}$ ,  $dI/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-220F1		110	$^\circ\text{C/W}$
	TO-251/TO-252			
Junction to Case	TO-220	$\theta_{JC}$	1	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.12	$^\circ\text{C/W}$
	TO-251/TO-252		2.31 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

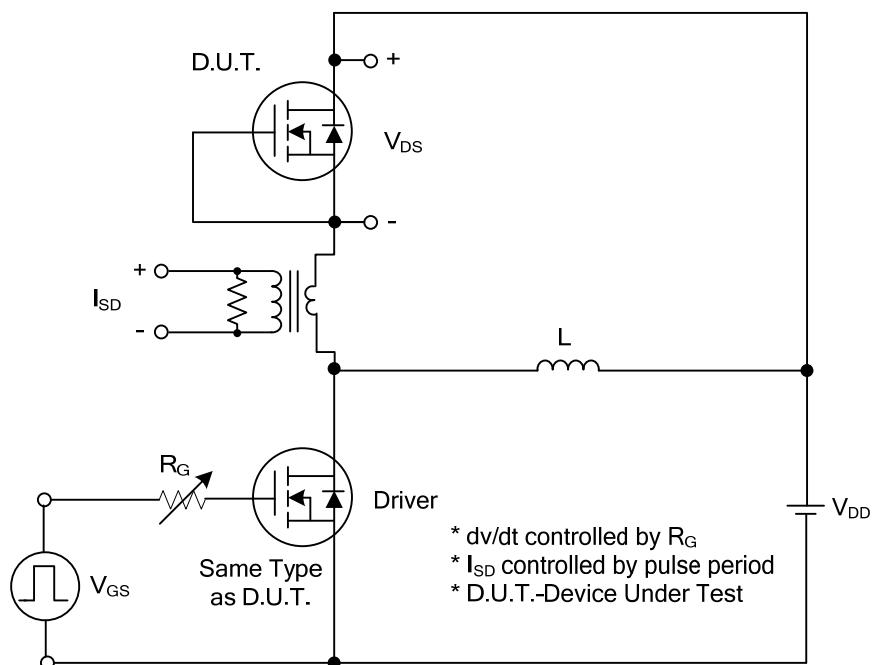
■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	800			V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=800\text{V}, \text{V}_{\text{GS}}=0\text{V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 30\text{V}, \text{V}_{\text{DS}}=0\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	3.0		5.0	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.0\text{A}$			3.8	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$		514		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			70		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			7		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$\text{Q}_G$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A}$ $\text{I}_G=1\text{mA}$ (Note 1, 2)		15		nC
Gate-Source Charge	$\text{Q}_{\text{GS}}$			4		nC
Gate-Drain Charge	$\text{Q}_{\text{GD}}$			3		nC
Turn-On Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$	$\text{V}_{\text{DD}}=100\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4\text{A},$ $\text{R}_G=25\Omega$ (Note 1, 2)		10		ns
Turn-On Rise Time	$t_R$			18		ns
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			38		ns
Turn-Off Fall Time	$t_F$			27		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_S$				4	A
Maximum Pulsed Drain-Source Diode Forward Current	$\text{I}_{\text{SM}}$				8	A
Drain-Source Diode Forward Voltage (Note 1)	$\text{V}_{\text{SD}}$	$\text{I}_S=4.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	$t_{\text{rr}}$	$\text{I}_S=4.0\text{A}, \text{V}_{\text{GS}}=0\text{V},$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		620		nS
Body Diode Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			4.2		$\mu\text{C}$

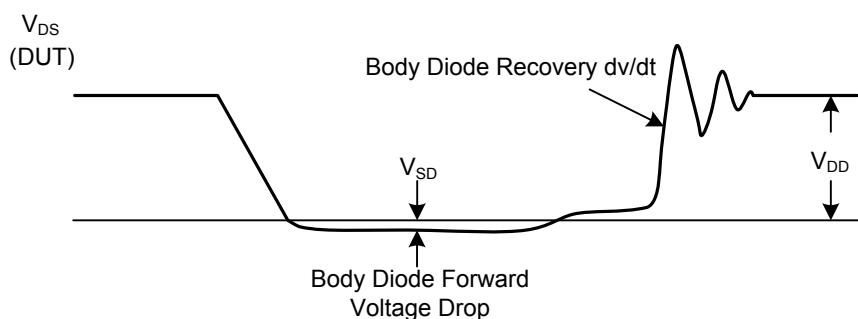
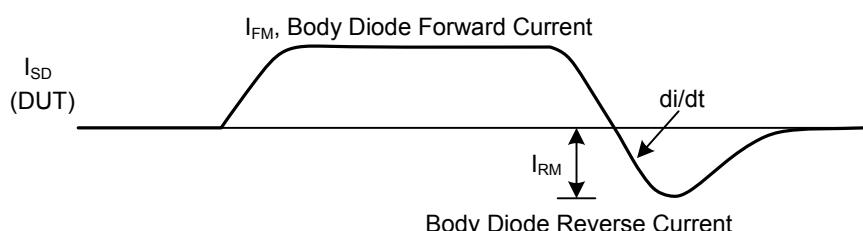
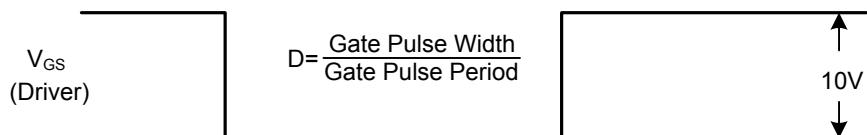
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

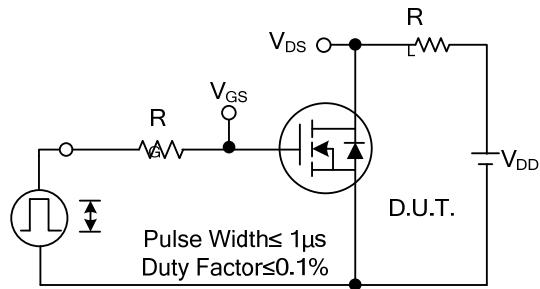


Peak Diode Recovery  $dv/dt$  Test Circuit

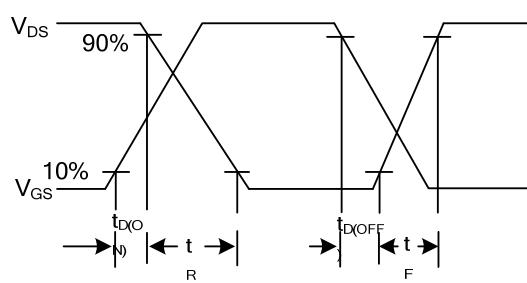


Peak Diode Recovery  $dv/dt$  Waveforms

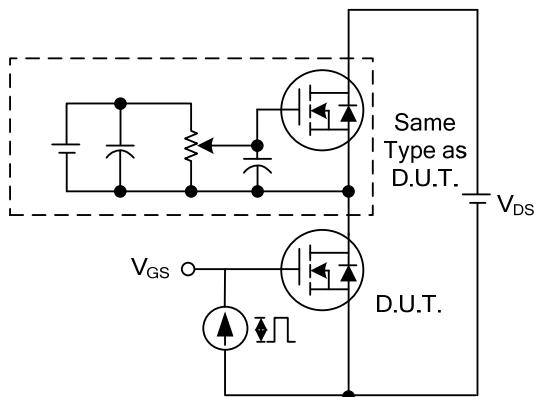
### ■ TEST CIRCUITS AND WAVEFORMS



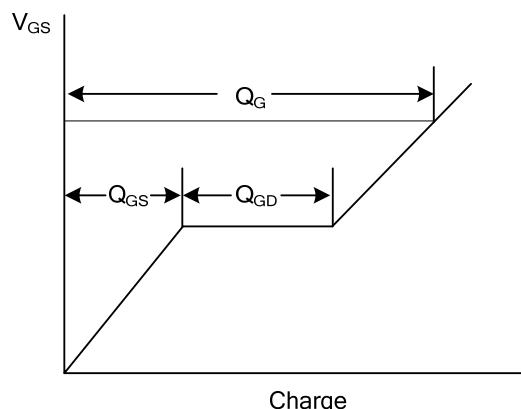
Switching Test Circuit



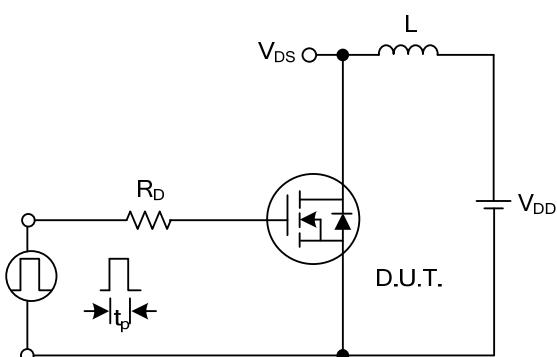
Switching Waveforms



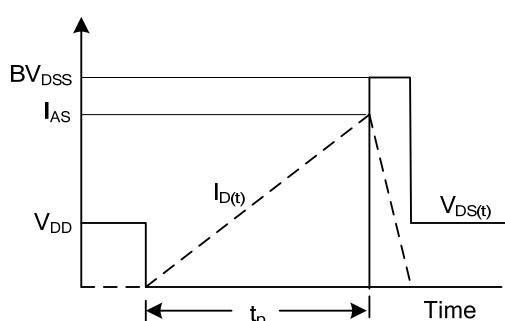
Gate Charge Test Circuit



Gate Charge Waveform

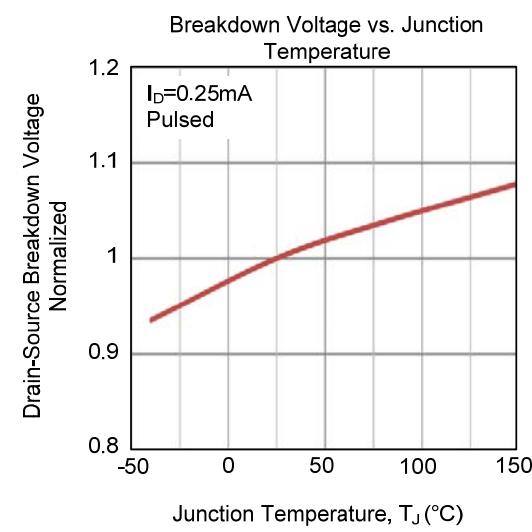
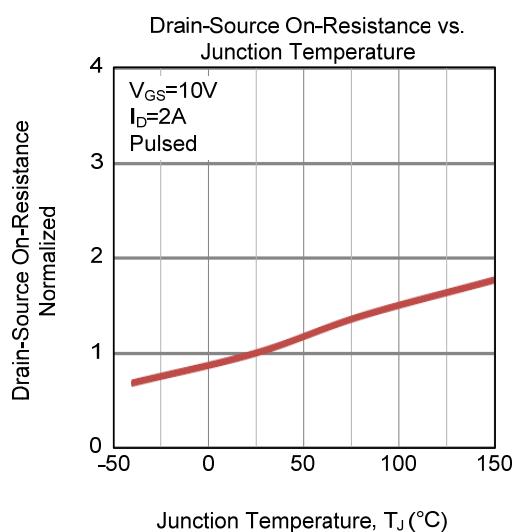
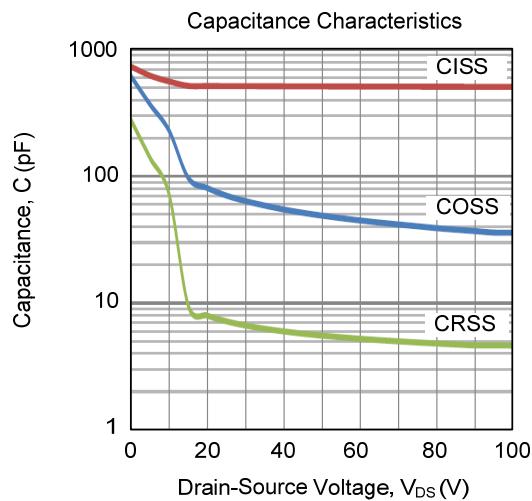
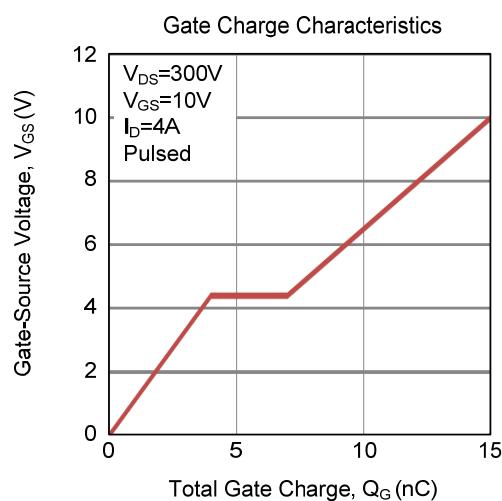
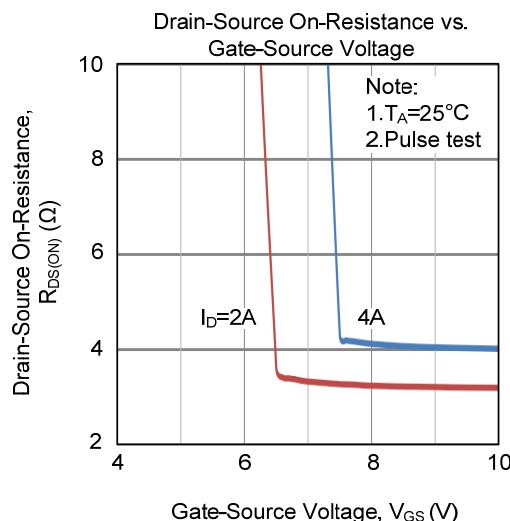
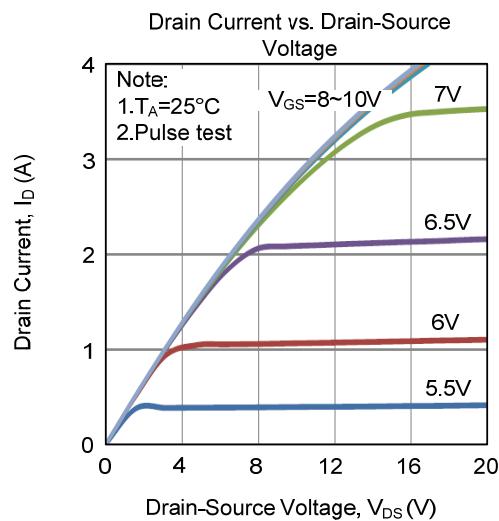


Unclamped Inductive Switching Test Circuit

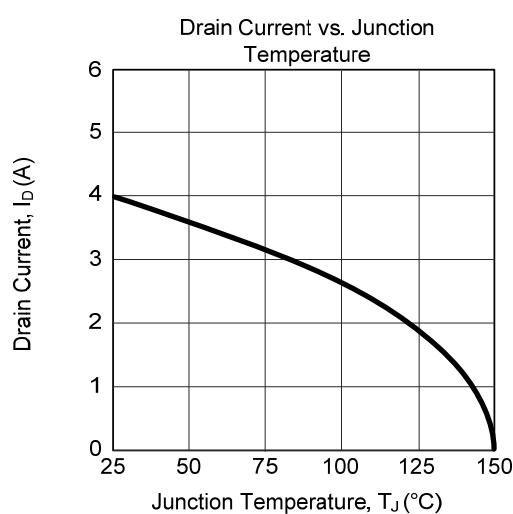
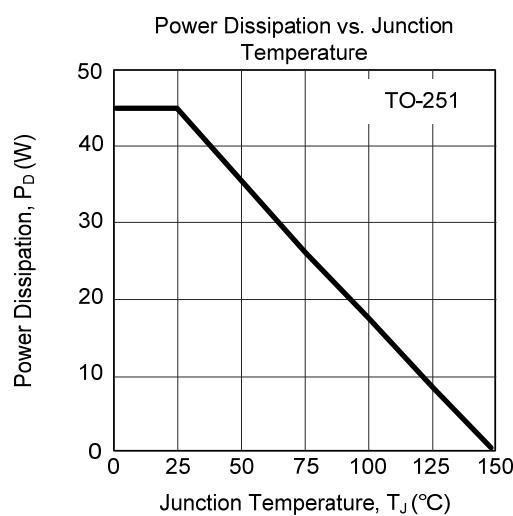
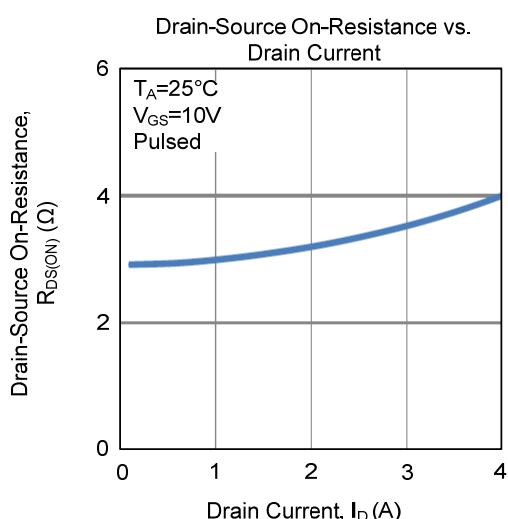
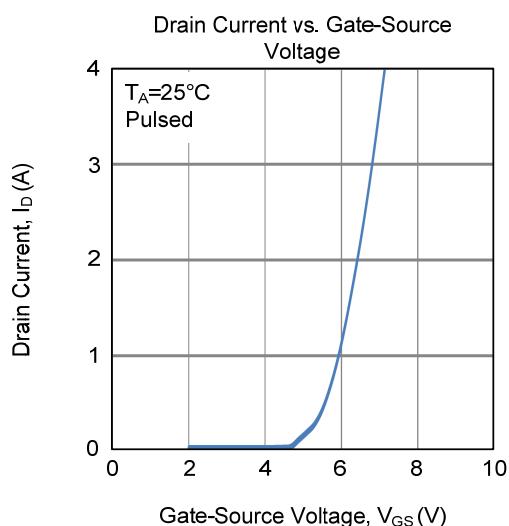
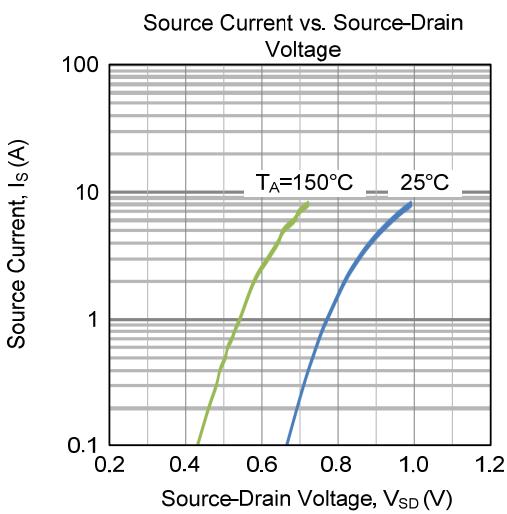
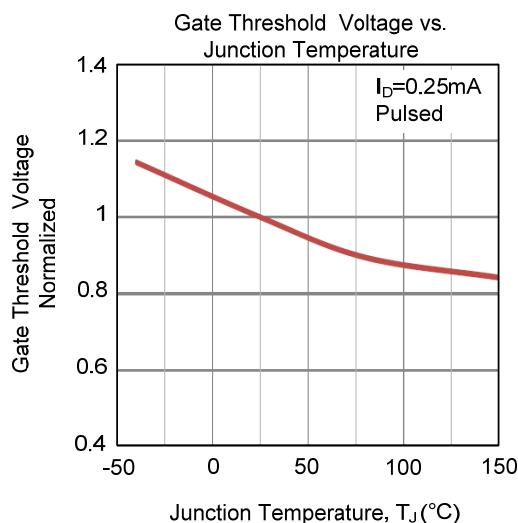


Unclamped Inductive Switching Waveforms

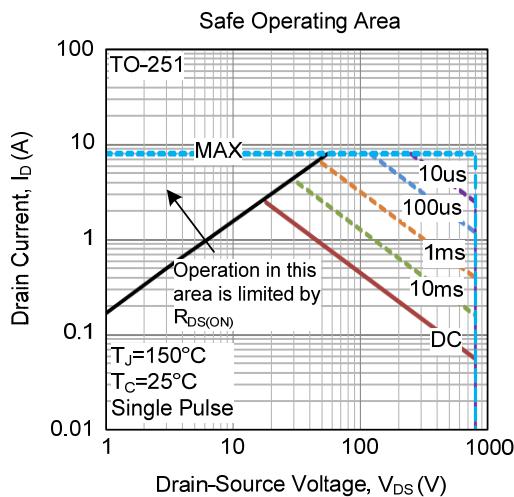
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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