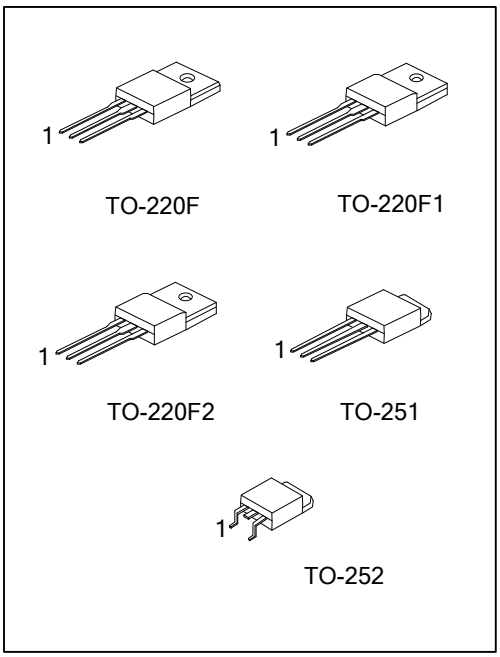
6N70-TC

Power MOSFET

6A, 700V N-CHANNEL POWER MOSFET



■ DESCRIPTION

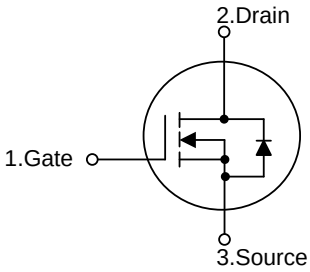
The UTC **6N70-TC** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **6N70-TC** is generally applied in high efficiency switch mode power supplies.

■ FEATURES

* $R_{DS(ON)} \leq 2.2\ \Omega$ @ V_{GS}=10V, I_D=3.0A
* High Switching Speed

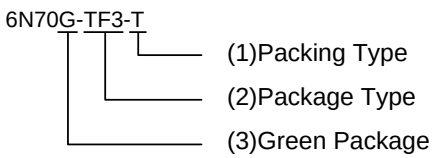
■ SYMBOL



■ ORDERING INFORMATION

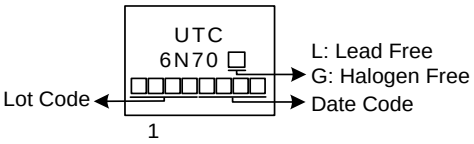
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
6N70L-TF3-T	6N70G-TF3-T	TO-220F	G	D	S	Tube
6N70L-TF1-T	6N70G-TF1-T	TO-220F1	G	D	S	Tube
6N70L-TF2-T	6N70G-TF2-T	TO-220F2	G	D	S	Tube
6N70L-TM3-T	6N70G-TM3-T	TO-251	G	D	S	Tube
6N70L-TN3-R	6N70G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



(1) T: Tube, R: Tape Reel
(2) TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2
TM3: TO-251, TN3: TO-252
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	6	A
	Pulsed (Note 2)	I_{DM}	12	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	168	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.97	V/ns
Power Dissipation	TO-220F/TO-220F1 TO-220F2	P_D	35	W
	TO-251/TO-252		52	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 10mH, I_{AS} = 5.8A, V_{DD} = 50V, R_G = 25 Ω Starting T_J = 25°C
4. I_{SD} ≤ 6.0A, di/dt ≤100A/μs, V_{DD} ≤BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220F/TO-220F1 TO-220F2	θ_{JA}	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220F/TO-220F1 TO-220F2	θ_{JC}	3.57	°C/W
	TO-251/TO-252		2.4 (Note)	°C/W

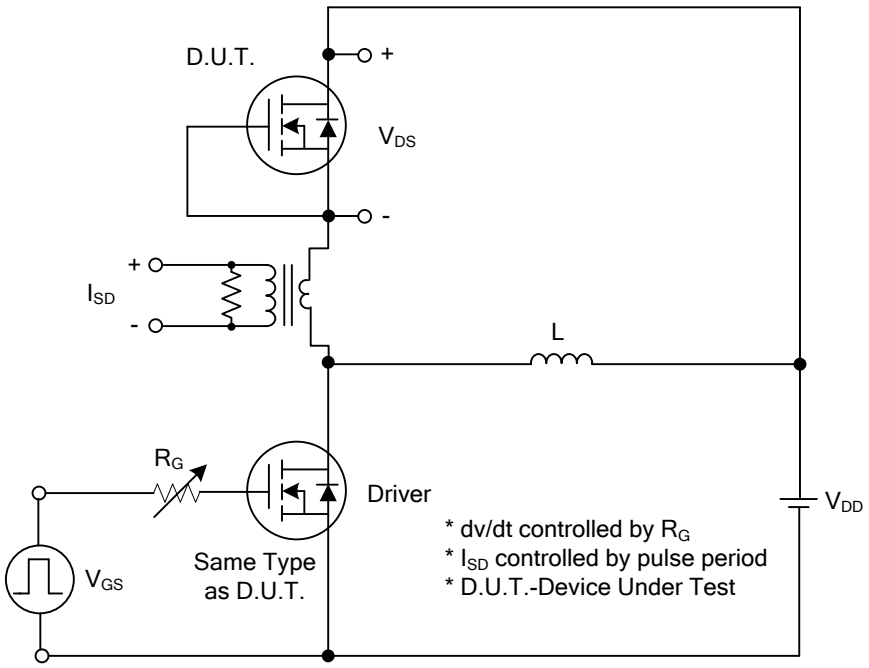
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

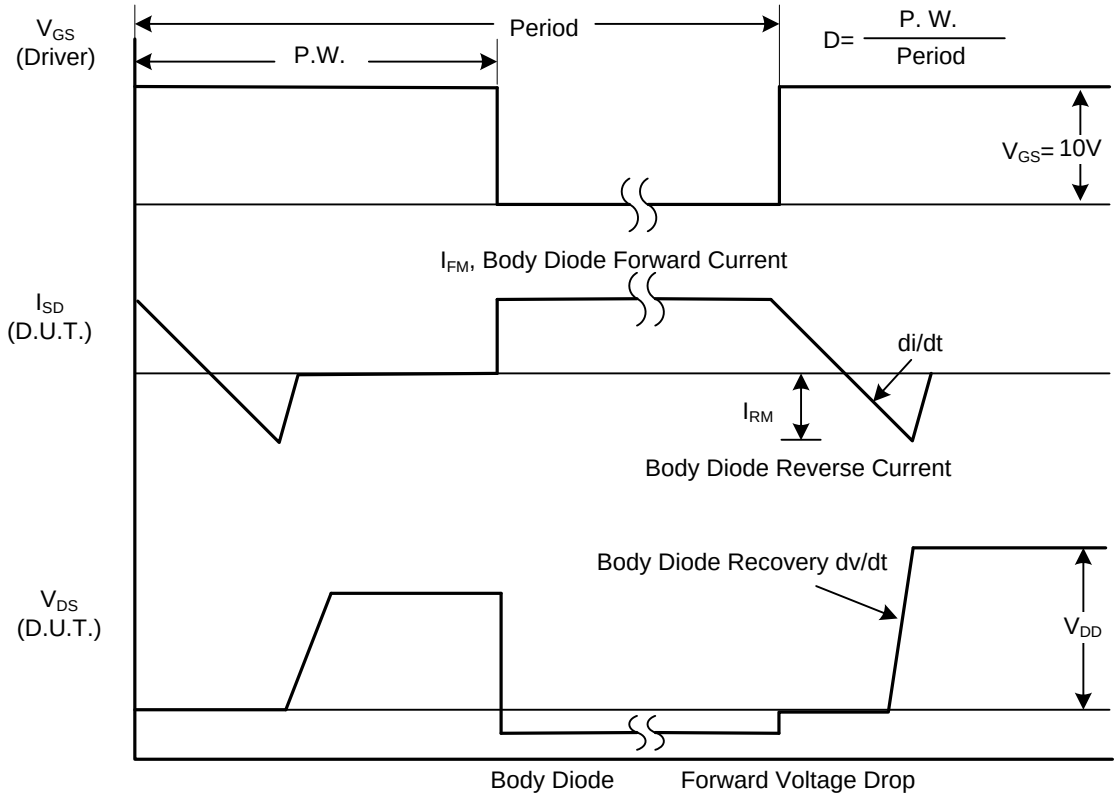
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D= 250μA	700			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=700V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=3.0A			2.2	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0 MHz		700		pF
Output Capacitance		C_{OSS}			68		pF
Reverse Transfer Capacitance		C_{RSS}			3.5		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=100V, V_{GS}=10V, I_D=6.0A I_G=1mA (Note 1, 2)		15		nC
Gateource Charge		Q_{GS}			6.5		nC
Gate-Drain Charge		Q_{GD}			3		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DS}=100V, V_{GS}=10V, I_D=6.0A, R_G=25Ω (Note 1, 2)		9.6		ns
Rise Time		t_R			16		ns
Turn-off Delay Time		$t_{D(OFF)}$			34		ns
Fall-Time		t_F			25		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				6	A
Maximum Body-Diode Pulsed Current		I_{SM}				12	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	V_{GS}=0V, I_S=6.0A			1.4	V
Reverse Recovery Time (Note 1)		t_{rr}	V_{GS}=0V, I_S=6.0A, dI_F/dt=100A/μs (Note1)		296		ns
Reverse Recovery Charge		Q_{rr}			2.78		μC

Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

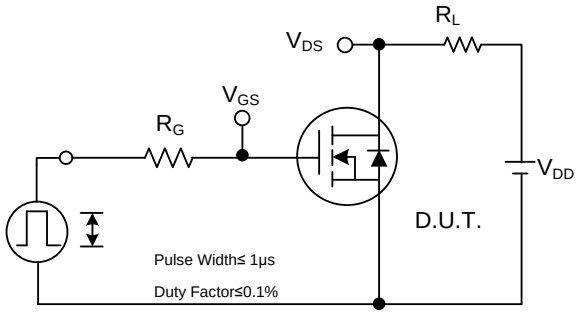


Peak Diode Recovery dv/dt Test Circuit

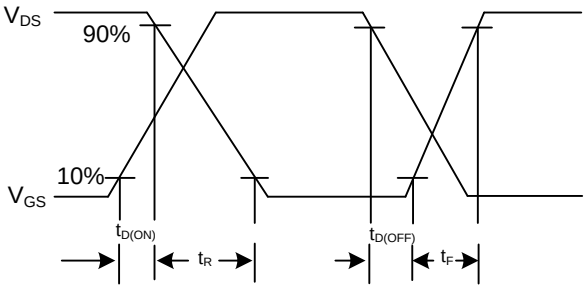


Peak Diode Recovery dv/dt Waveforms

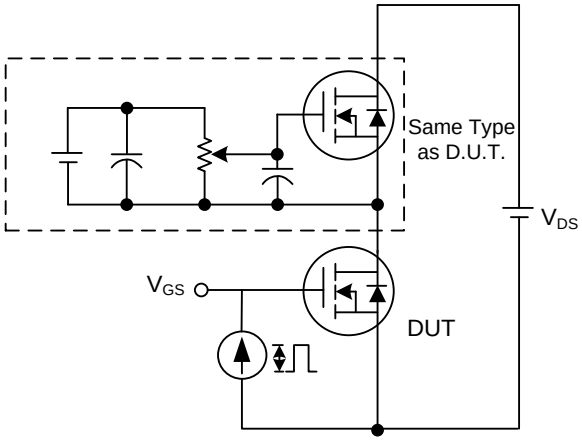
■ TEST CIRCUITS AND WAVEFORMS



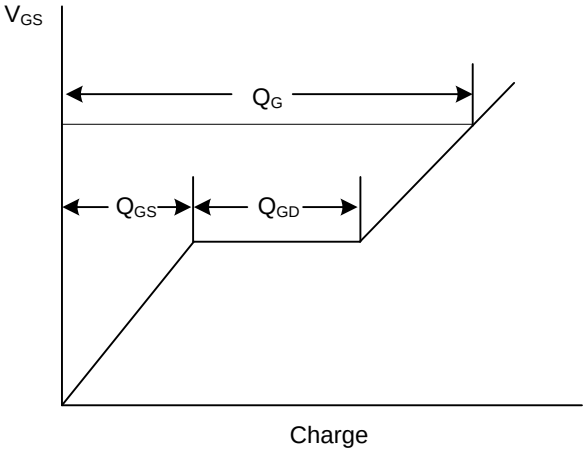
Switching Test Circuit



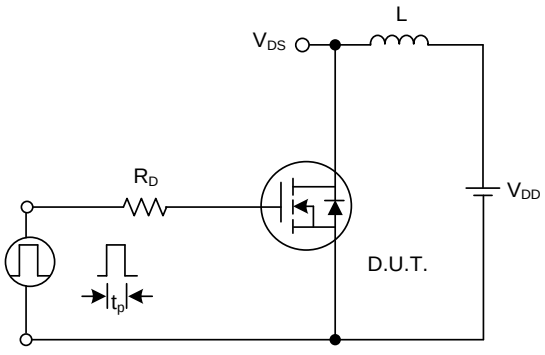
Switching Waveforms



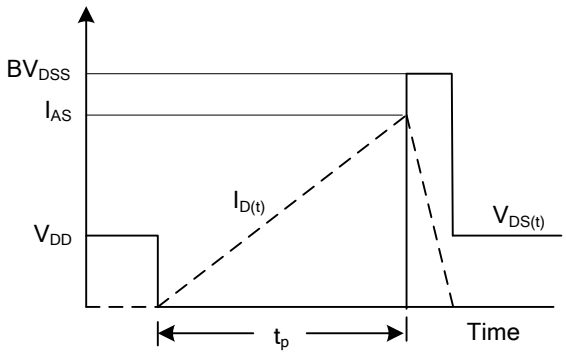
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms