# UNISONIC TECHNOLOGIES CO., LTD

UPA806

**Preliminary** 

### NPN SILICON TRANSISTOR

## NPN SILICON HIGH FREQUENCY TRANSISTOR

### DESCRIPTION

The UTC UPA806 is NPN silicon high frequency transistor, it uses UTC's advanced technology to provide customers with high gain, high gain bandwidth and low noise figure, etc.

The UTC UPA806 is suited for various hand-held wireless applications.

# SOT-363

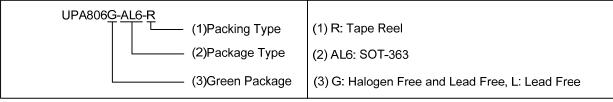
### **FEATURES**

- \* High Gain
- \* Low Noise Figure
- \* High Gain Bandwidth
- \* Excellent Low Voltage, Low Current Performance

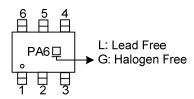
### **ORDERING INFORMATION**

Ordering Number		Dookono	Pin Assignment					Doolsing	
Lead Free	Halogen Free	Package	1	2	3	4	5	6	Packing
UPA806L-AL6-R	UPA806G-AL6-R	SOT-363	E2	B2	C1	E1	B1	C2	Tape Reel

Pin Assignment: G: Gate D: Drain S: Source



### **MARKING**



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### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	$V_{CBO}$	9	V
Collector to Emitter Voltage	$V_{CEO}$	6	V
Emitter to Base Voltage	$V_{EBO}$	2	V
Collector Current	Ic	30	mA
Total Power Dissipation	$P_{D}$	200	mW
Junction Temperature	$T_J$	+150	°C
Storage Temperature Range	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector Cutoff Current	I <sub>CBO</sub>	$V_{CB}$ =5 $V$ , $I_E$ =0			0.1	μΑ
Emitter Cutoff Current	I <sub>EBO</sub>	$V_{EB}$ =1V, $I_C$ =0			0.1	μΑ
Forward Current Gain (Note 1)	h <sub>FE</sub>	V <sub>CE</sub> =3V, I <sub>C</sub> =10mA	75	100	150	
Gain Bandwidth	f <sub>T</sub>	V <sub>CE</sub> =3V, I <sub>C</sub> =10mA, f=2GHz		12		GHz
Feedback Capacitance (Note 2)	$C_{re}$	V <sub>CB</sub> =3V, I <sub>E</sub> =0, f=1MHz		0.4	0.7	pF

Notes: 1. Pulsed measurement, pulse width ≤350µs, duty cycle ≤ 2%

<sup>2.</sup> The emitter terminal should be connected to the ground terminal of the 3 terminal capacitance bridge.

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