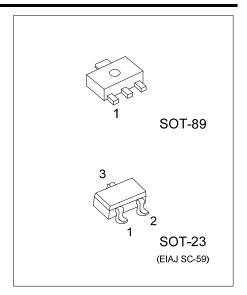
# ADAPTIVE 100/120Hz CURRENT RIPPLE REMOVING CIRCUIT CONTROLLER

#### ■ DESCRIPTION

UTC **ULD5133** is a controller, which drives external NMOSFET to remove the 100/120Hz LED current ripple on AC/DC power by a capacitor between VC and GND. The chip ensures minimum power dissipation on NMOSFET while removing LED current ripple relying on the adaptive technology.

UTC **ULD5133** allows user to setup the maximum cathode voltage of LED string by sensing the drain voltage of NMOSFET which could help limit the power dissipation on chip.

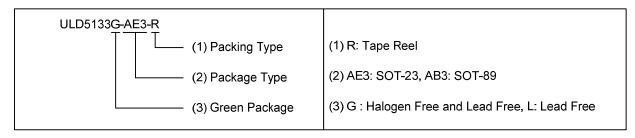


#### **■** FEATURES

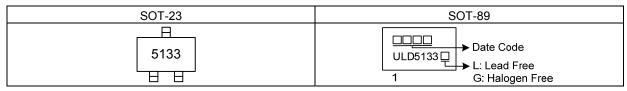
- \* Controller for adaptive 100/120Hz current ripple remover
- \* Amplitude of LED current ripple programming
- \* Maximum cathode voltage of LED programming
- \* Maximum LED current programming

#### ■ ORDERING INFORMATION

Ordering Number		Package	Packing	
Lead Free	nd Free Halogen Free			
ULD5133L-AE3-R	ULD5133G-AE3-R	SOT-23	Tape Reel	
ULD5133L-AB3-R	ULD5133G-AB3-R	SOT-89	Tape Reel	

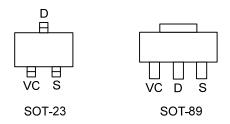


# MARKING



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# ■ PIN CONFIGURATION



# ■ PIN DESCRIPTION

PIN NO.		PIN NAME	DESCRIPTION			
SOT-23	SOT-89	PIN NAIVIE	DESCRIPTION			
1	1	VC	Programming LED Current Ripple Pin			
2	3	S	Connecting NMOSFET Source Pin			
3	2	D	Connecting NMOSFET Drain Pin			

# ■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Junction Temperature	$T_J$	+150	°C
Lead Temperature	T <sub>L</sub>	+260	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# ■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT
Maximum Junction Temperature	TJ	+150	°C

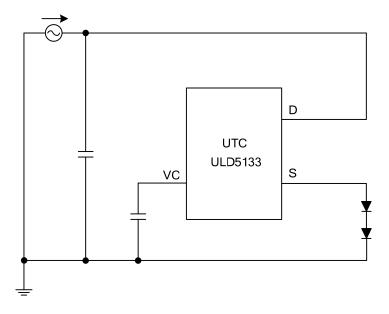
# ■ THERMAL RESISTANCE

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-23	$\theta_{JA}$	325	°C/W
	SOT-89		180	°C/W
Junction to Case	SOT-23	0	110	°C/W
	SOT-89	$\theta_{JC}$	38	°C/W

# ■ **ELECTRICAL CHARACTERISTICS** T<sub>A</sub>=25°C, unless otherwise stated.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Resistance Between D Pin and VC Pin	$R_{D\_VC}$		40	47	52	ΚΩ
Voltage (D Pin to S Pin)	$V_{D-S}$				30	V
Voltage (VC Pin to S Pin)	V <sub>VC-S</sub>				8	V

### TYPICAL APPLICATION CIRCUIT



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