

1P50**POWER MOSFET****-1.0A, -500V P-CHANNEL
POWER MOSFET****■ DESCRIPTION**

The UTC **1P50** is a P-channel MOS Field Effect Transistor. It uses UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance.

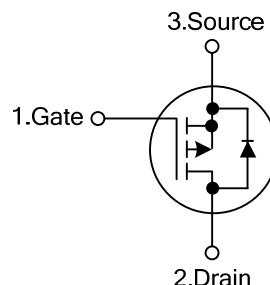
The UTC **1P50** is suitable for high voltage switching applications.

■ FEATURES

- * $R_{DS(ON)} \leq 14 \Omega$ @ $V_{GS}=-10V$, $I_D=-0.5A$

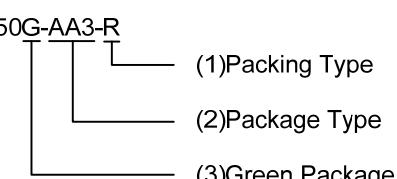
- * High switching speed

- * Low input capacitance

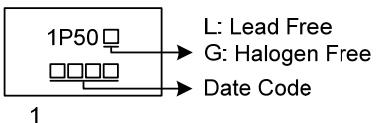
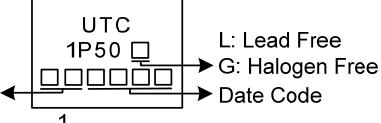
■ SYMBOL**■ ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
1P50L-AA3-R	1P50G-AA3-R	SOT-223	G	D	S	Tape Reel
1P50L-TN3-R	1P50G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

1P50G-AA3-R 	(1) R: Tape Reel (2) AA3: SOT-223, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

SOT-223	TO-252
 1	 1

■ ABSOLUTE MAXIMUM RATING ($T_A=25^\circ\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	DC	I_D	-1	A
	Pulsed (Note 2)	I_{DM}	-3	A
Single Avalanche Energy (Note 3)		E_{AS}	25	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.1	V/ns
Power Dissipation ($T_c=25^\circ\text{C}$)	SOT-223	P_D	10	W
	TO-252		28	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=30\text{mH}$, $I_{AS}=-1.3\text{A}$, $V_{DD}=-50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.

4. $I_{SD} \leq -1.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150	$^\circ\text{C/W}$
	TO-252		110	$^\circ\text{C/W}$
Junction to Case	SOT-223	θ_{JC}	12.5	$^\circ\text{C/W}$
	TO-252		4.46	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

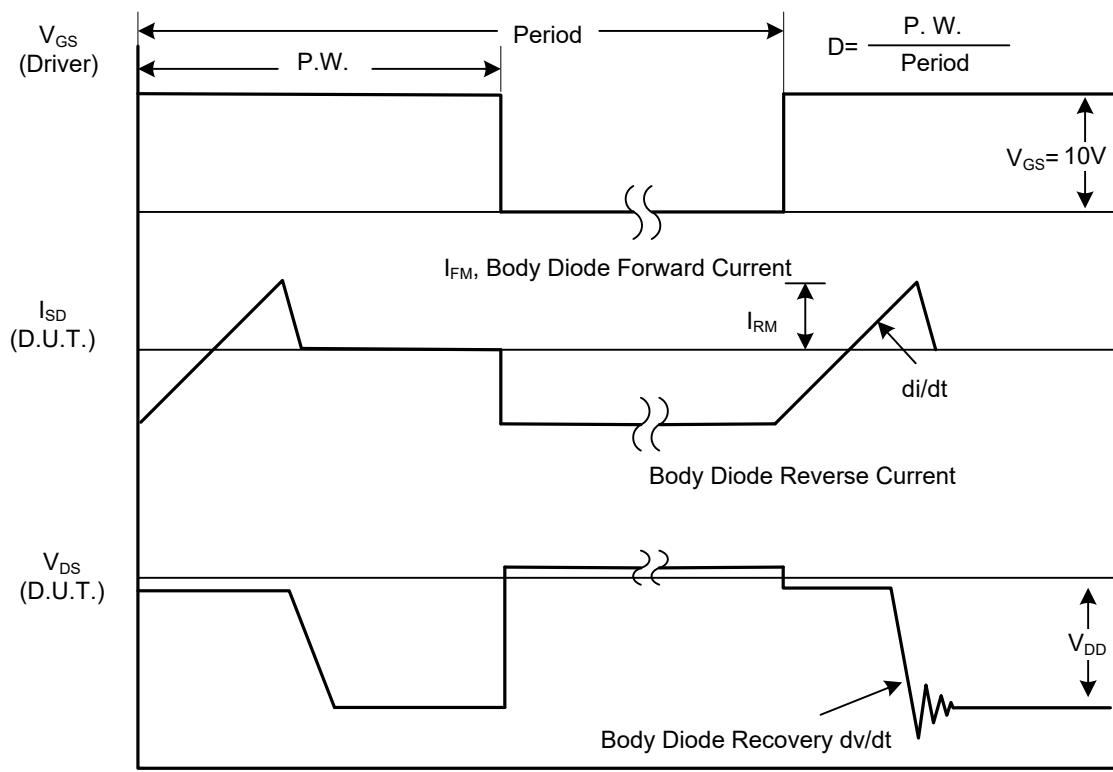
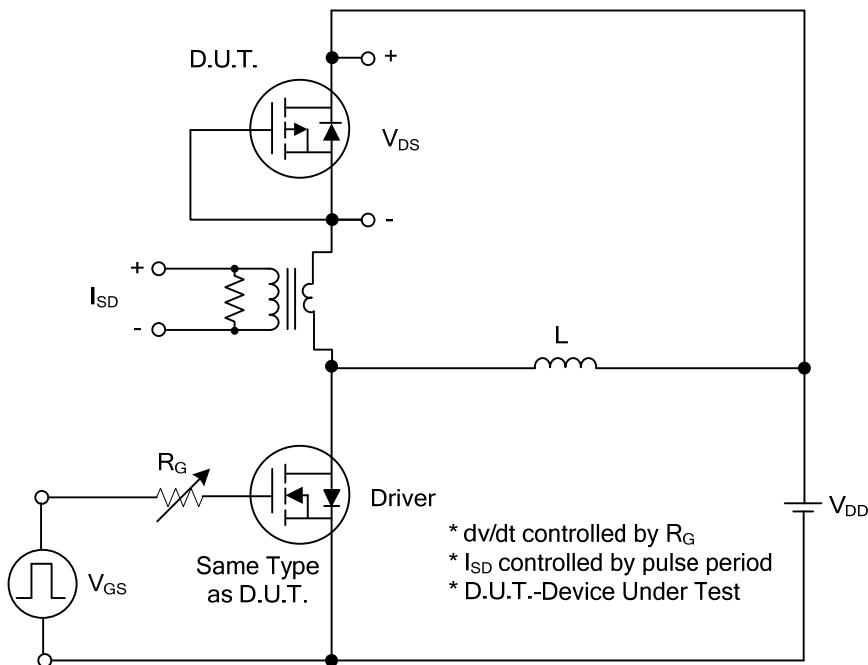
■ ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	500			V
Drain-Source Leakage Current	$I_{\text{DS}}^{\text{SS}}$	$V_{\text{DS}}=-500\text{V}, V_{\text{GS}}=0\text{V}$		-1.0		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}}=+30\text{V}, V_{\text{DS}}=0\text{V}$		+100		nA
	Reverse	$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-2.0		-4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=0.5\text{A}$			14	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-25\text{V}, f=1.0\text{MHz}$		165		pF
Output Capacitance	C_{OSS}			28		pF
Reverse Transfer Capacitance	C_{RSS}			5.6		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{\text{DS}}=-400\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-1.0\text{A}$ (Note 1, 2)		8.3		nC
Gate to Source Charge	Q_{GS}			4.6		nC
Gate to Drain Charge	Q_{GD}			1.9		nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=-100\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-1.0\text{A}, R_{\text{G}}=25\Omega$ (Note 1, 2)		8		ns
Rise Time	t_R			17		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			22		ns
Fall-Time	t_F			36		ns
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				-1	A
Maximum Body-Diode Pulsed Current	I_{SM}				-3	A
Diode Forward Voltage	V_{SD}	$I_F=-1.0\text{A}, V_{\text{GS}}=0\text{V}$			-1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F=-1.0\text{A}, V_{\text{GS}}=0\text{V}, \text{di}/\text{dt}=100\text{A}/\mu\text{s}$		145		ns
Body Diode Reverse Recovery Charge	Q_{rr}			830		nC

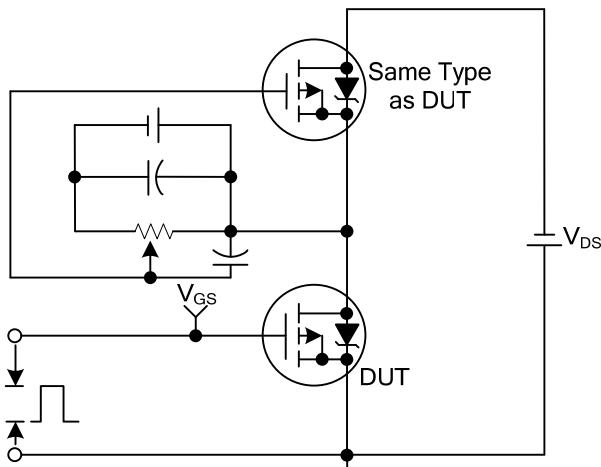
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

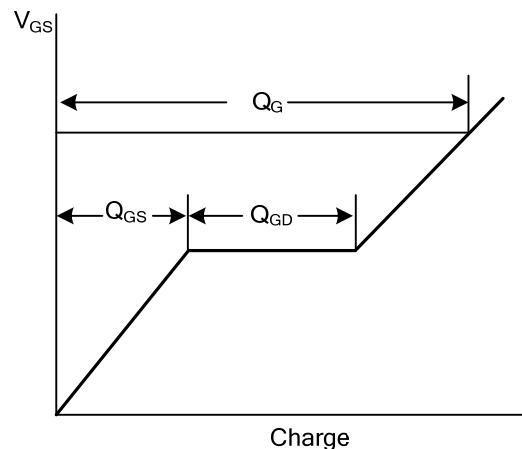
■ TEST CIRCUITS AND WAVEFORMS



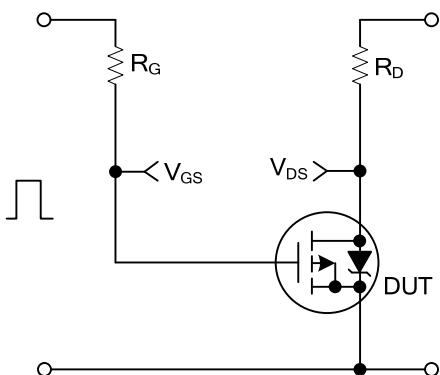
■ TEST CIRCUITS AND WAVEFORMS



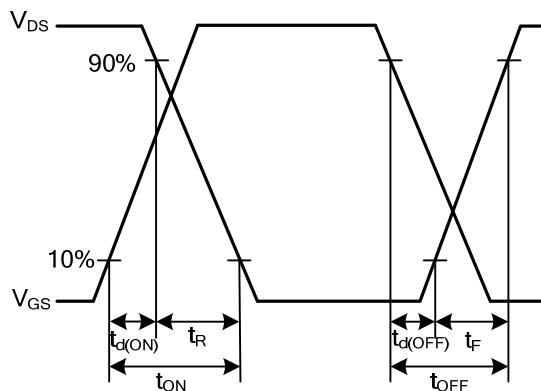
Gate Charge Test Circuit



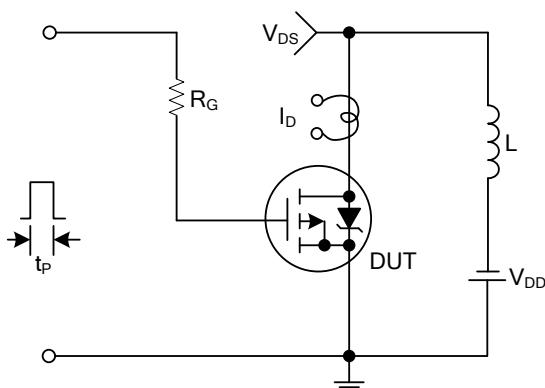
Gate Charge Waveforms



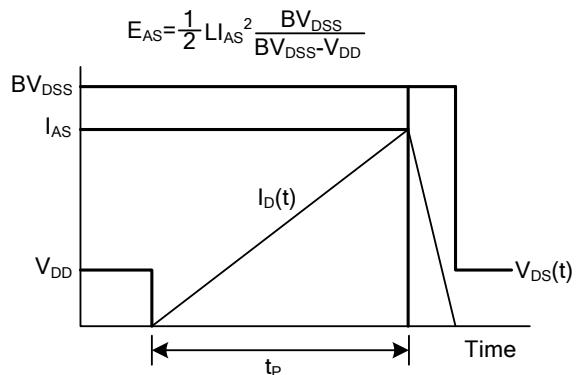
Resistive Switching Test Circuit



Resistive Switching Waveforms

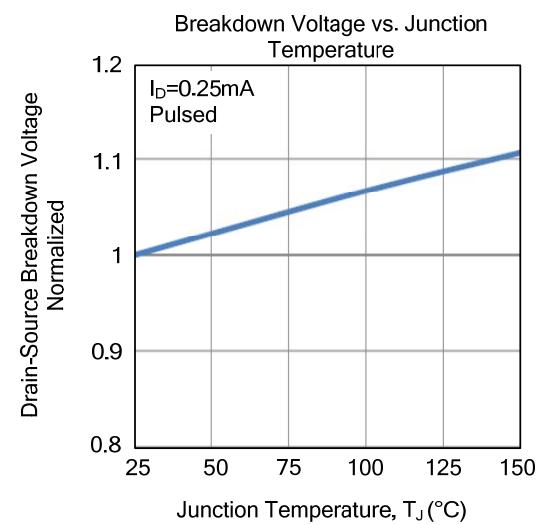
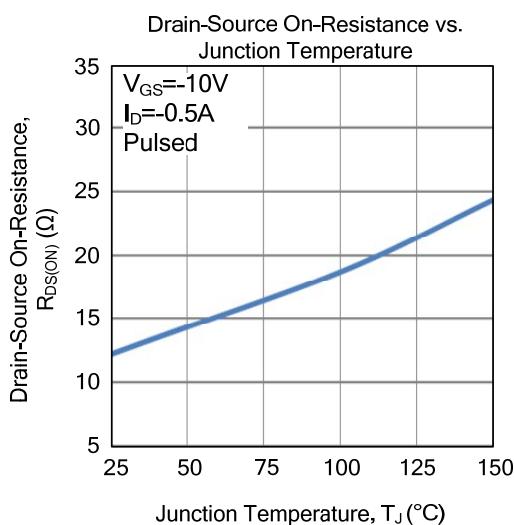
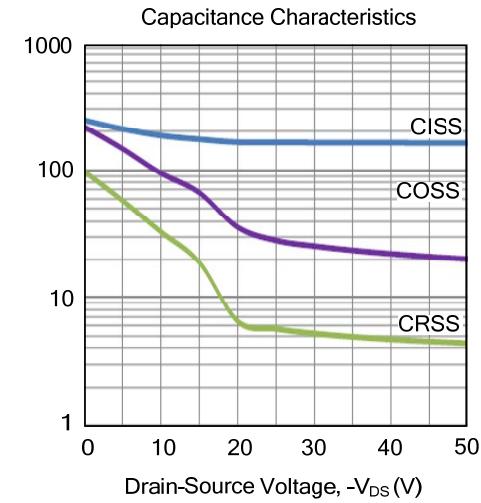
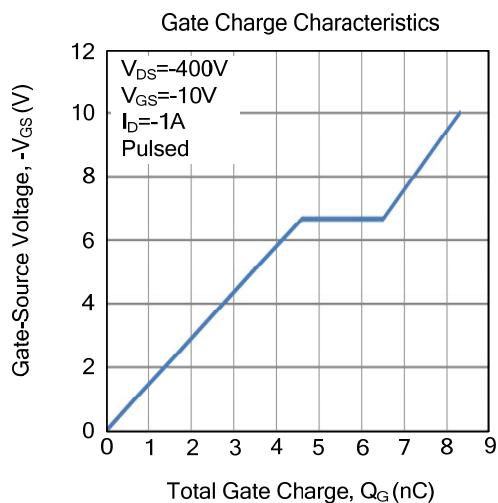
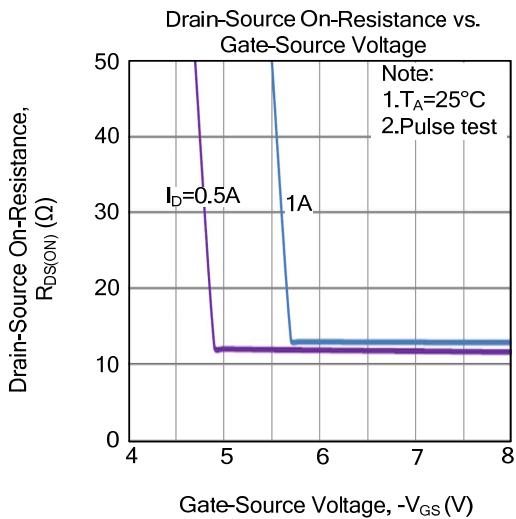
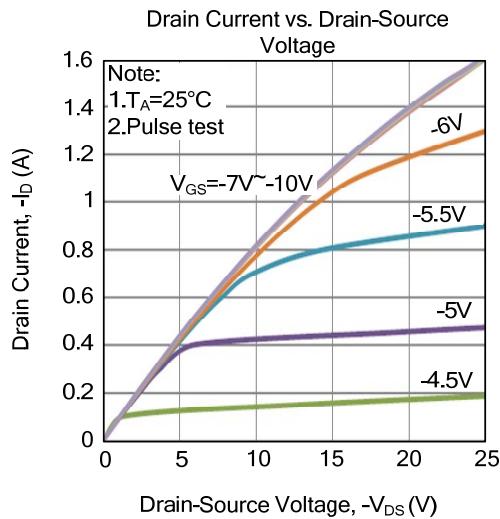


Unclamped Inductive Switching Test Circuit

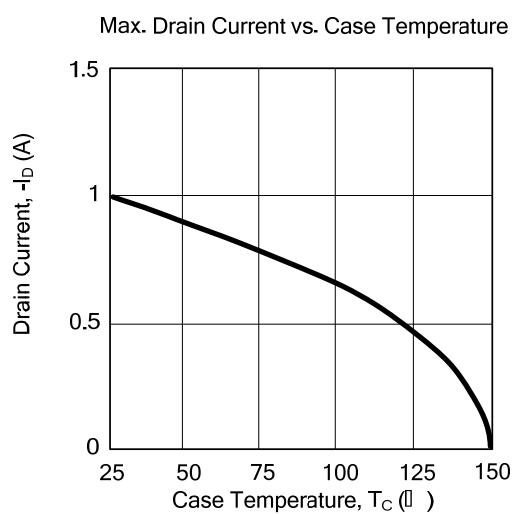
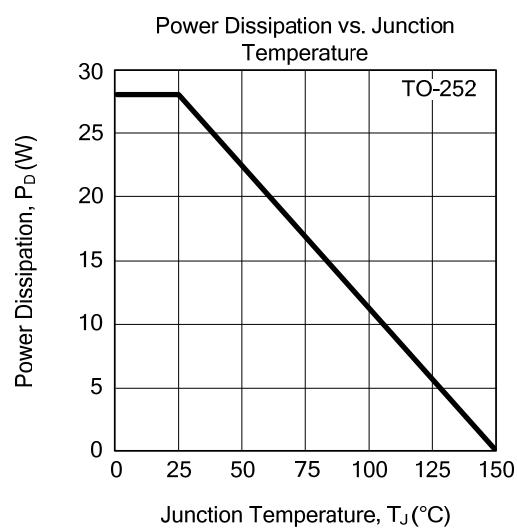
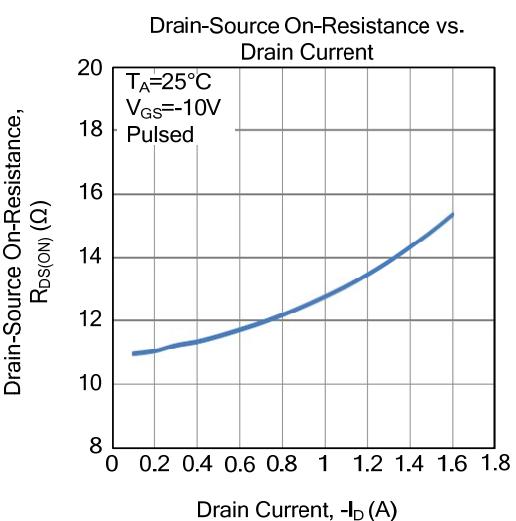
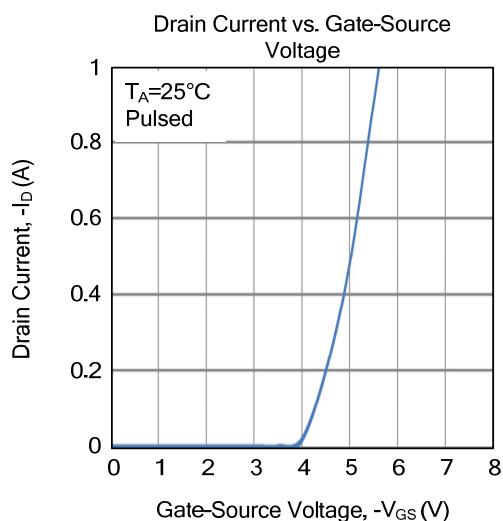
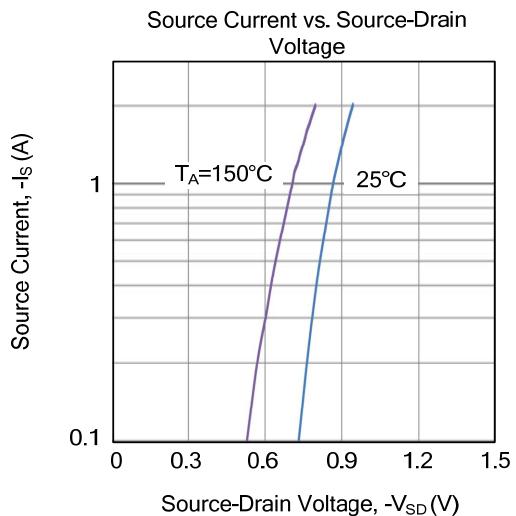
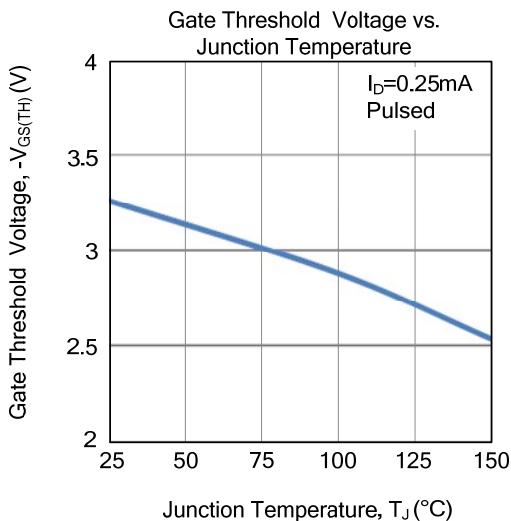


Unclamped Inductive Switching Waveforms

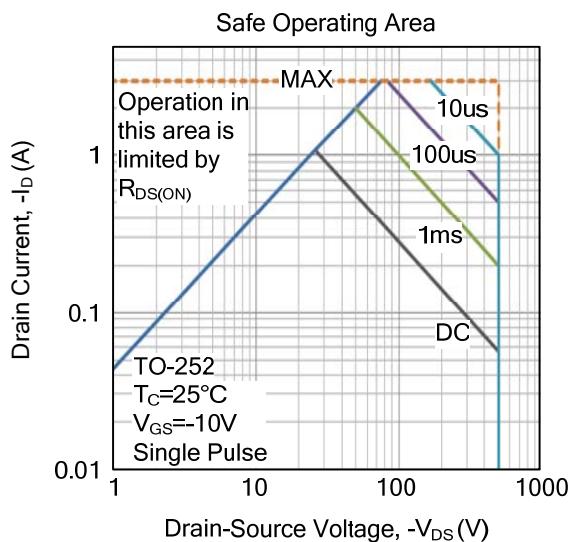
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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