



12N65-C

Power MOSFET

12A, 650V N-CHANNEL POWER MOSFET

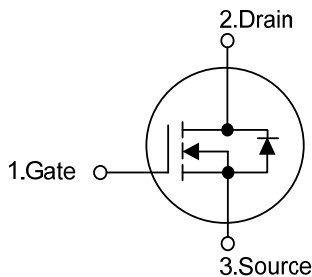
■ DESCRIPTION

The UTC **12N65-C** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

- * $R_{DS(ON)} < 0.75 \Omega @ V_{GS} = 10 V, I_D = 6.0 A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

■ SYMBOL

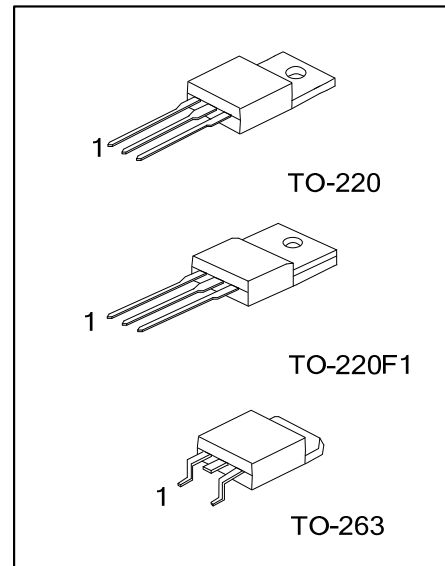


■ ORDERING INFORMATION

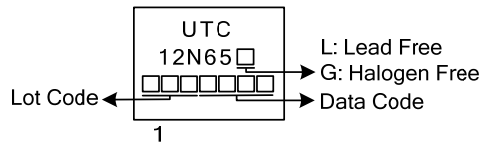
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N65L-TF1-T	12N65G-TF1-T	TO-220F1	G	D	S	Tube
12N65L-TF3-T	12N65G-TF3-T	TO-220F	G	D	S	Tube
12N65L-TQ2-T	12N65G-TQ2-T	TO-263	G	D	S	Tube
12N65L-TQ2-R	12N65G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>12N65G-TF1-T</p>	<p>(1) T: Tube, R: Tape Reel</p> <p>(2) TF1: TO-220F1, TF3: TO-220F, TQ2: TO-263</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current		I_D	12	A
Pulsed Drain Current (Note 2)		I_{DM}	48	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	123	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2	ns
Power Dissipation	TO-220/TO-263	P_D	225	W
	TO-220F1		51	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 4.95\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 12\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220/TO-263	θ_{JC}	0.56	$^\circ\text{C}/\text{W}$
	TO-220F1		2.45	$^\circ\text{C}/\text{W}$

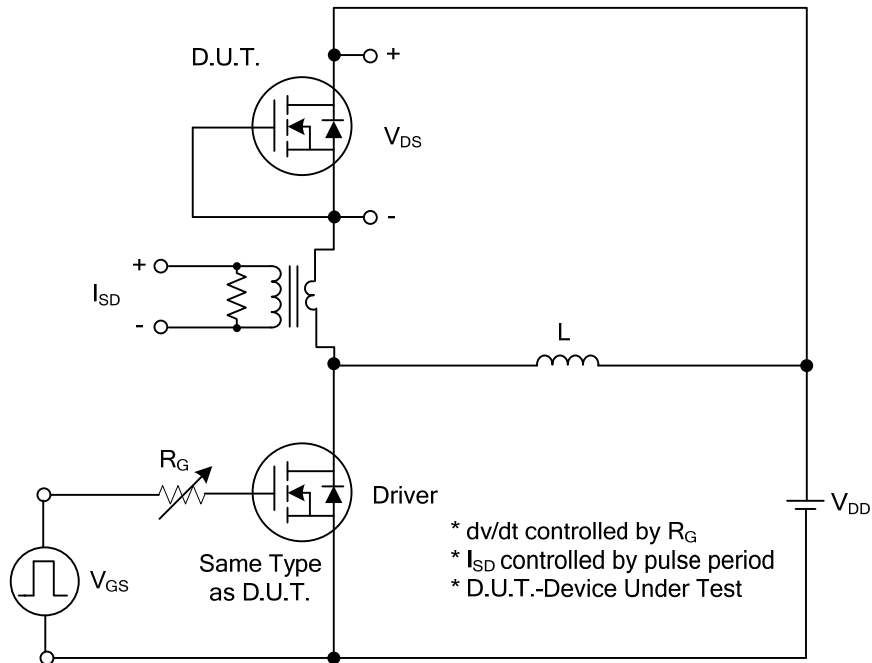
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	650			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward	I _{GSS}			100	nA
	Reverse					
		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =6.0A			0.75	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0 MHz		1440		pF
Output Capacitance	C _{OSS}			240		pF
Reverse Transfer Capacitance	C _{RSS}			45		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =50V, I _D =1.3A, I _G =100μA V _{GS} =10V (Note 1,2)		143.7		nC
Gate-Source Charge	Q _{GS}			10.7		nC
Gate-Drain Charge	Q _{GD}			26.1		nC
Turn-On Delay Time (Note 1)	t _{D(ON)}	V _{DD} =30V, I _D =0.5A, R _G =25Ω, V _{GS} =10V (Note 1,2)		80		ns
Turn-On Rise Time	t _R			150		ns
Turn-Off Delay Time	t _{D(OFF)}			442		ns
Turn-Off Fall Time	t _F			215		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				12	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				48	A
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0 V, I _S =6.0 A			1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} =0 V, I _S =6.0 A, dI _F /dt=100 A/μs (Note 1)		375		ns
Reverse Recovery Charge	Q _{rr}			2.46		μC

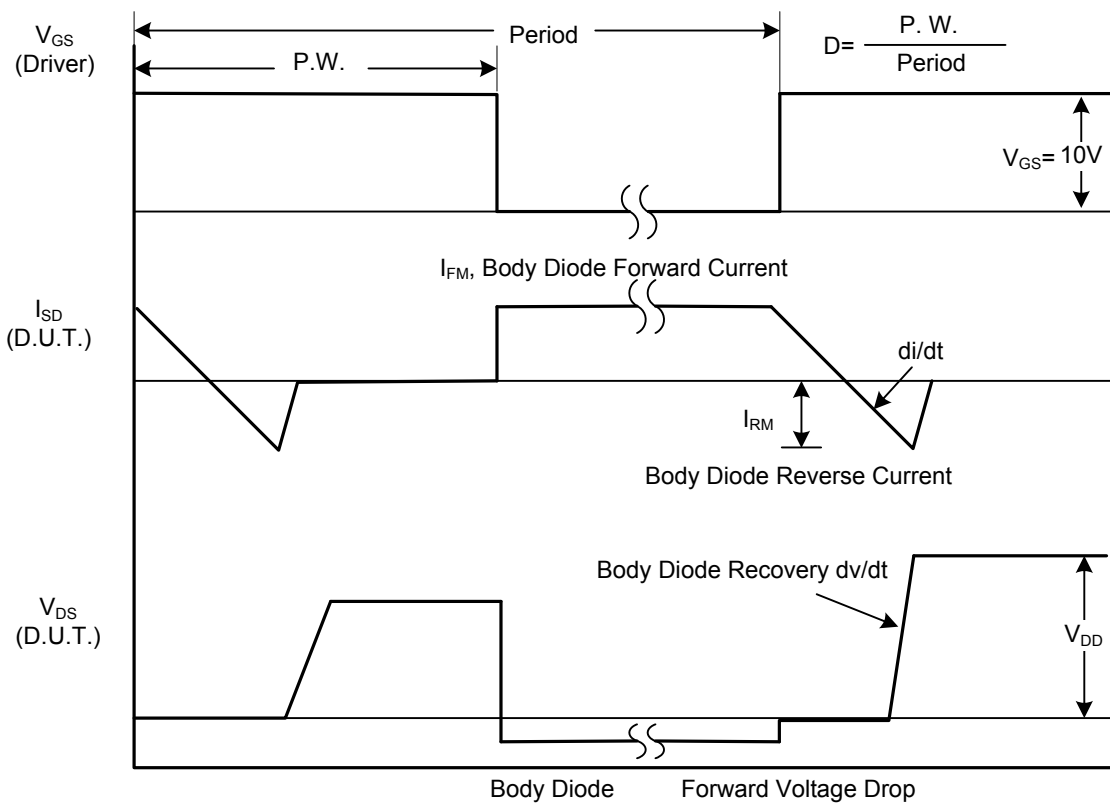
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

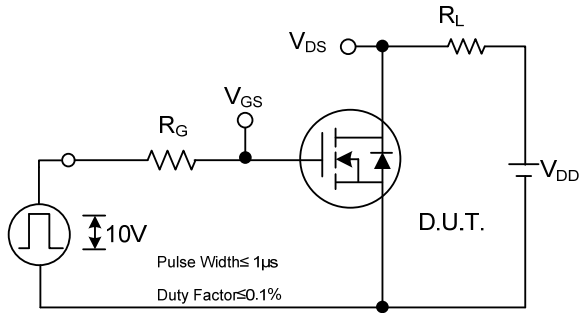


Peak Diode Recovery dv/dt Test Circuit

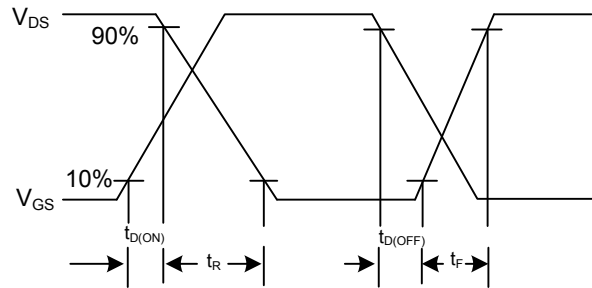


Peak Diode Recovery dv/dt Waveforms

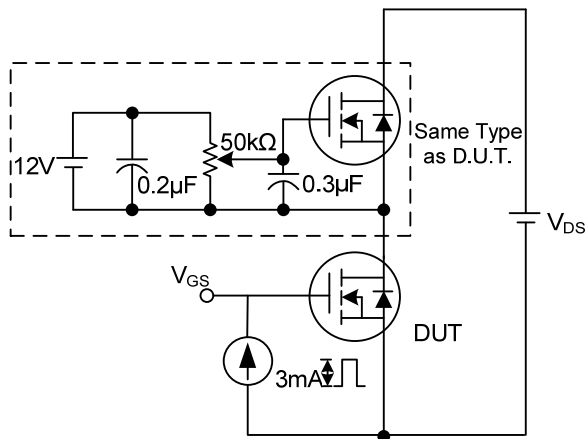
TEST CIRCUITS AND WAVEFORMS (Cont.)



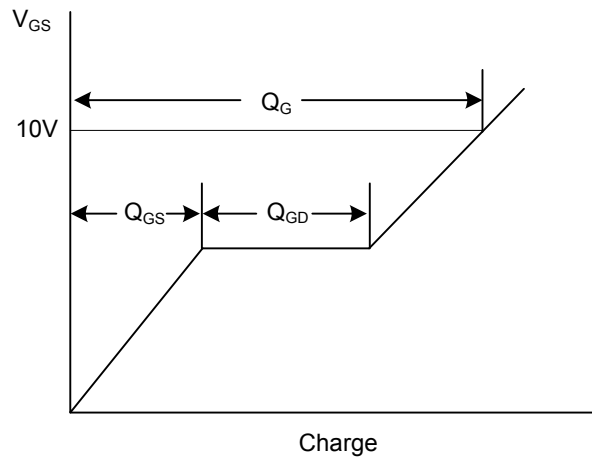
Switching Test Circuit



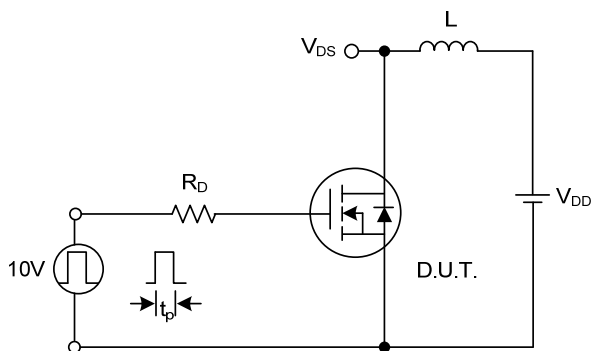
Switching Waveforms



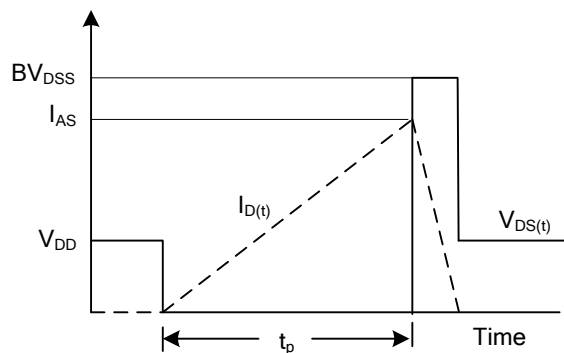
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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