

UNISONIC TECHNOLOGIES CO., LTD

6N65-HC Power MOSFET

6.0A, 650V N-CHANNEL **POWER MOSFET**

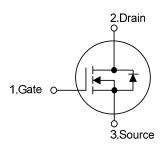
DESCRIPTION

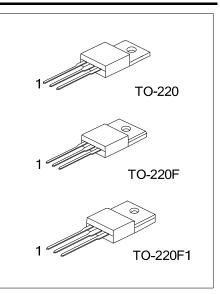
The UTC 6N65-HC is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} \le 1.3 \Omega @ V_{GS} = 10V, I_D = 3.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL

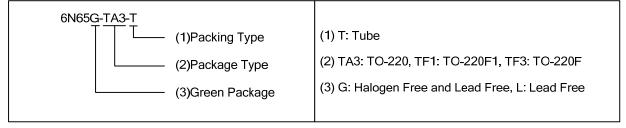




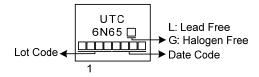
ORDERING INFORMATION

Ordering Number		Daakaaa	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
6N65L-TA3-T	6N65G-TA3-T	TO-220	G	D	S	Tube	
6N65L-TF1-T	6N65G-TF1-T	TO-220F1	G	D	S	Tube	
6N65L-TF3-T	6N65G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate S: Source D: Drain



MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current		I_{D}	6	Α
Pulsed Drain Current (Note 2)		I _{DM}	24	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	68	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.8	V/ns
Power Dissipation	TO-220	D	125	W
	TO-220F/TO-220F1	P _D	40	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 17mH, I_{AS} = 2.82A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 6.0$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
Junction to Case	TO-220	0	1	°C/W
	TO-220F/TO-220F1	θЈС	3.2	°C/W

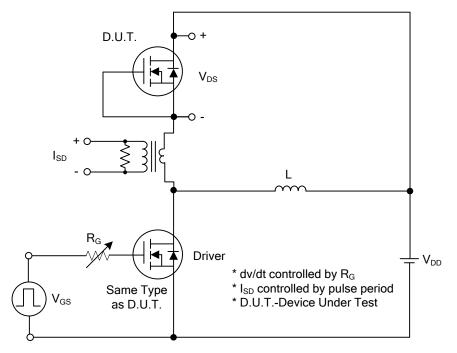
■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	650			٧
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V$			10	μΑ
Gate- Source Leakage Current	Forward		$V_{GS} = 30V, V_{DS} = 0V$			100	nA
	Reverse	I _{GSS}	$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_D = 3.0A$			1.3	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}			1050		pF
Output Capacitance		Coss	V _{DS} =25V, V _{GS} =0V, f=1.0 MHz		337		pF
Reverse Transfer Capacitance		C _{RSS}			94		pF
SWITCHING CHARACTERISTIC	S						
Total Gate Charge (Note 1)		Q_G	V _{DS} =250V, V _{GS} =10V, I _D =6.0A, I _D =100μA (Note 1, 2)		32		nC
Gate-Source Charge		Q_GS			11.2		nC
Gate-Drain Charge		Q_GD	10-100μA (Note 1, 2)		9.6		nC
Turn-On Delay Time (Note 1)		t _{D(ON)}			17		ns
Turn-On Rise Time		t _R	V_{DD} =30V, V_{GS} =10V, I_{D} =6.0A,		21		ns
Turn-Off Delay Time		t _{D(OFF)}	$R_G = 25\Omega$ (Note 1, 2)		113		ns
Turn-Off Fall Time		t _F			37		ns
DRAIN-SOURCE DIODE CHARA	CTERISTIC	S AND MAX	(IMUM RATINGS				
Maximum Continuous Drain-Source Diode		I _S				6	Α
Forward Current						U	^
Maximum Pulsed Drain-Source Diode		I _{SM}				24	Α
Forward Current						47	^
Drain-Source Diode Forward Voltage		V_{SD}	I _S =6.0A , V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	I _S =6.0A , V _{GS} =0V		312		ns
Body Diode Reverse Recovery Charge		Q_{rr}	di/dt=100A/μs		2.44		μC

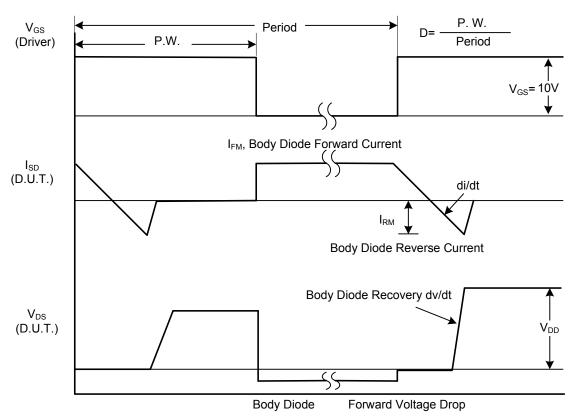
Notes: 1. Pulse Test: Pulse width \leq 300 μ s, Duty cycle \leq 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



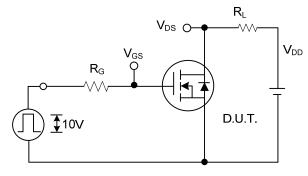
Peak Diode Recovery dv/dt Test Circuit



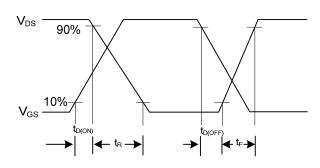
Peak Diode Recovery dv/dt Waveforms

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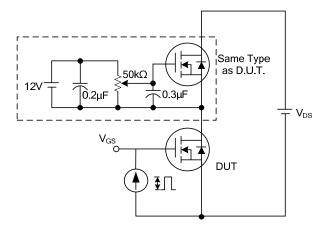
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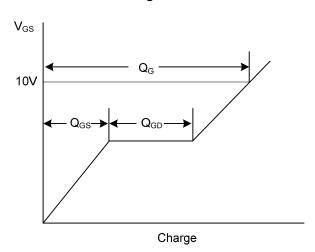
Switching Test Circuit



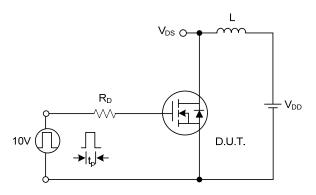
Switching Waveforms



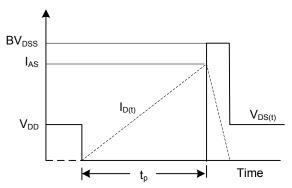
Gate Charge Test Circuit



Gate Charge Waveform

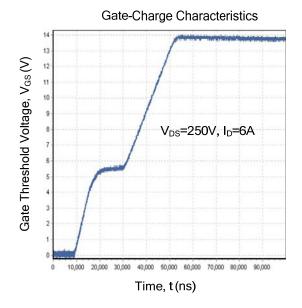


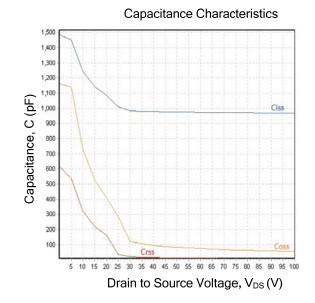
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS





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