UNISONIC TECHNOLOGIES CO., LTD

4N60-HC Power MOSFET

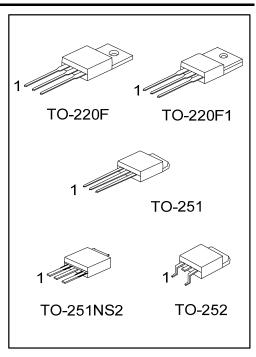
4.0A, 600V N-CHANNEL **POWER MOSFET**

DESCRIPTION

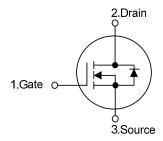
The UTC 4N60-HC is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

FEATURES

- * $R_{DS(ON)} \le 2.2 \Omega$ @ V_{GS} =10V, I_D =2.0A
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness



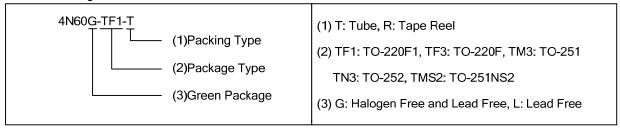
SYMBOL



ORDERING INFORMATION

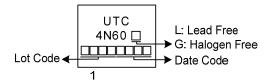
Ordering Number		Dealtage	Pin Assignment			Da alsia a	
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	Tube	
4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D	S	Tube	
4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	Tube	
4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	Tape Reel	
4N60L-TMN2-T	4N60G-TMN2-T	TO-251NS2	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current		I _D	4	Α
Pulsed Drain Current (Note 2)		I_{DM}	16	Α
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	132	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.4	V/ns
Power Dissipation	TO-220F/TO-220F1		34	W
	TO-251/TO-252 TO-251NS2	P_D	50	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 30mH, I_{AS} = 2.97A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 4. $I_{SD} \le 4.0 \text{A}$, di/dt $\le 200 \text{A}/\mu \text{s}$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25 ^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220F/TO-220F1		62.5	°C/W
	TO-251/TO-252 TO-251NS2	θ_{JA}	110	°C/W
Junction to Case	TO-220F/TO-220F1		3.68	°C/W
	TO-251/TO-252 TO-251NS2	θ_{JC}	2.5	°C/W

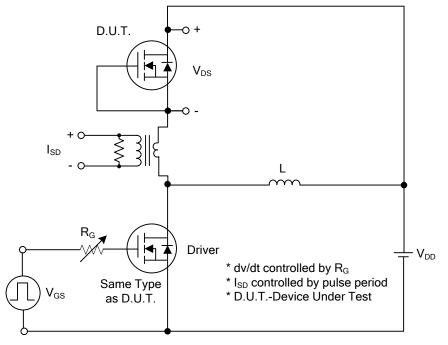
■ **ELECTRICAL CHARACTERISTICS** (T_J =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Drain-Source Leakage Current		I_{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ
Gate- Source Leakage Current	Forward	000	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
	Reverse		$V_{GS} = -30V$, $V_{DS} = 0V$			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D = 2.0A			2.2	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}			551		pF
Output Capacitance		C_{OSS}	V _{DS} =25V, V _{GS} =0V, f=1.0 MHz		186		pF
Reverse Transfer Capacitance		C_{RSS}			66		pF
SWITCHING CHARACTERISTICS	S						
Total Gate Charge (Note 1)		Q_G	V _{DS} =150V, V _{GS} =10V, I _D =5.0A, I _D =100μA (Note 1, 2)		18.3		nC
Gate-Source Charge		Q_GS			6.4		nC
Gate-Drain Charge		Q_{GD}	10-100μΑ (Note 1, 2)		5.6		nC
Turn-On Delay Time (Note 1)		$t_{D(ON)}$			11		ns
Turn-On Rise Time		t_R	V_{DD} =30V, V_{GS} =10V, I_{D} =5.0A,		15		ns
Turn-Off Delay Time		t _{D(OFF)}	R _G =25Ω (Note 1, 2)		60		ns
Turn-Off Fall Time		t_{F}			26		ns
DRAIN-SOURCE DIODE CHARA	CTERISTIC	CS AND MA	XIMUM RATINGS				
Maximum Continuous Drain-Source Diode		Is				4	Α
Forward Current						4	^
Maximum Pulsed Drain-Source Diode		I _{SM}				16	Α
Forward Current						10	^
Drain-Source Diode Forward Voltage		V_{SD}	I _S =4.0A , V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	I _S =4.0A , V _{GS} =0V di/dt=100A/μs		285		ns
Body Diode Reverse Recovery Charge		Q_{rr}	IS-4.0A, VGS-0V dirat-100A/µS		1.54		μC

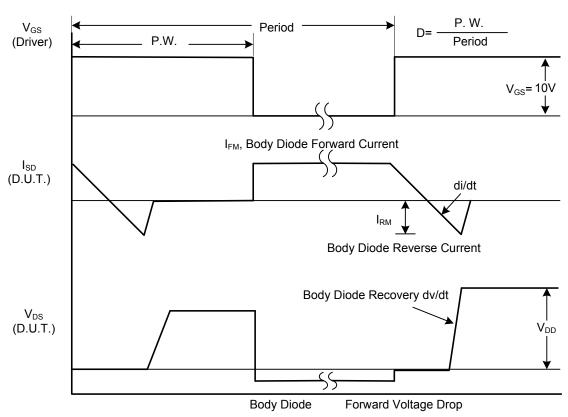
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

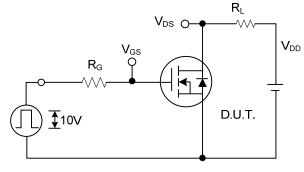


Peak Diode Recovery dv/dt Test Circuit

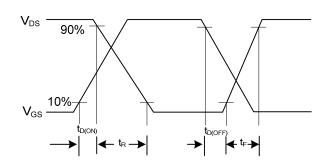


Peak Diode Recovery dv/dt Waveforms

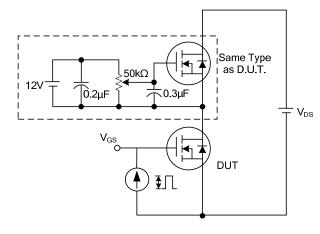
■ TEST CIRCUITS AND WAVEFORMS



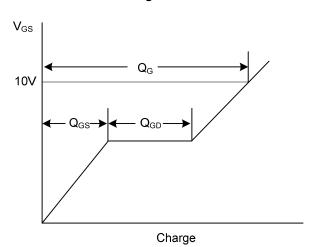
Switching Test Circuit



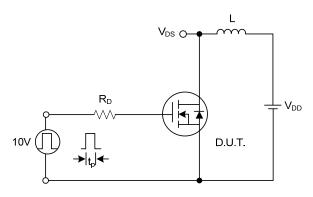
Switching Waveforms



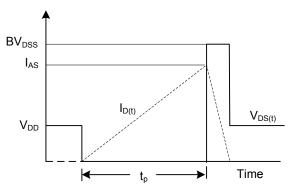
Gate Charge Test Circuit



Gate Charge Waveform

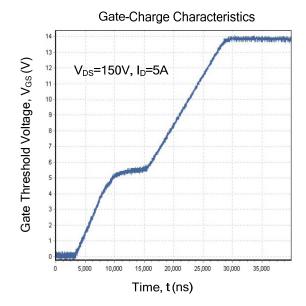


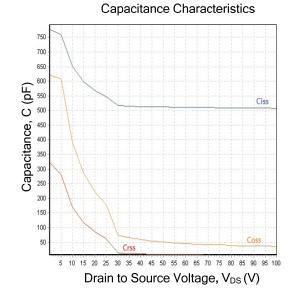
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS





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