

UNISONIC TECHNOLOGIES CO., LTD

5NM70-FD **Power MOSFET**

5A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

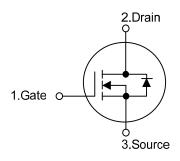
DESCRIPTION

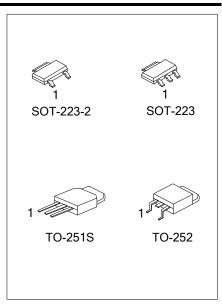
The UTC 5NM70-FD is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 1.6 Ω @ V_{GS} =10V, I_{D} = 2.5A
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness

SYMBOL

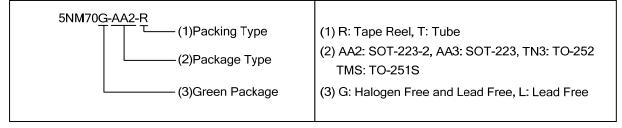




ORDERING INFORMATION

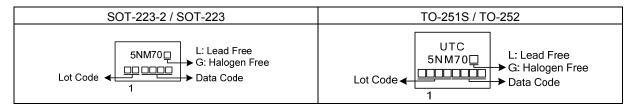
Ordering Number		Dackago	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
5NM70L-AA2-R	5NM70G-AA2-R	SOT-223-2	G	D	S	Tape Reel	
5NM70L-AA3-R	5NM70G-AA3-R	SOT-223	G	D	S	Tape Reel	
5NM70L-TMS-T	5NM70G-TMS-T	TO-251S	G	D	S	Tube	
5NM70L-TN3-R	5NM70G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate S: Source D: Drain



5NM70-FD Power MOSFET

■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	700	V	
Gate-Source Voltage		V_{GSS}	±30	V	
Drain Current	Continuous	I_{D}	5.0	Α	
	Pulsed (Note 2)	I_{DM}	20	Α	
Avalanche Energy	valanche Energy Single Pulsed (Note 3)		95	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	17	V/ns	
Power Dissipation	SOT-223-2/SOT-223	D	12	W	
	TO-251S/TO-252	P_D	54	W	
Junction Temperature		T_J	+150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by maximum junction temperature.
- 3. L=66mH, I_{AS} =1.7A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 5.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223-2/SOT-223	0	150	°C/W
	TO-251S/TO-252	θ_{JA}	110	°C/W
Junction to Case	SOT-223-2/SOT-223	0	10.4	°C/W
	TO-251S/TO-252	θ _{JC}	2.3	°C/W

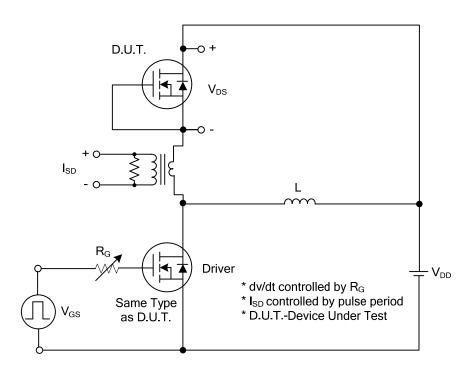
■ **ELECTRICAL CHARACTERISTICS** (T_J = 25°C unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	700			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =700V, V _{GS} =0V			10	μΑ
Gate-Source Leakage Current	Forward		V_{GS} =30V, V_{DS} =0V			100	- A
	Reverse	I_{GSS}	V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$			4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =2.5A			1.6	Ω
DYNAMIC CHARACTERISTICS		_				-	
Input Capacitance		C _{ISS}			240		pF
Output Capacitance		Coss	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		130		pF
Reverse Transfer Capacitance		C_{RSS}			16		pF
SWITCHING CHARACTERISTIC	S	_				-	
Total Gate Charge (Note 1)		Q_G	V _{DS} =50V, V _{GS} =10V, I _D =1.3A ,		33		nC
Gate to Source Charge		Q_GS	V _{DS} =50V, V _{GS} =10V, I _D =1.3A, I _G =100μA (Note 1, 2)		3		nC
Gate to Drain Charge		Q_GD	IG-100μA (Note 1, 2)		7		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			34		ns
Rise Time		t_R	V_{DD} =30V, V_{GS} =10V, I_{D} =0.5A,		50		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	R _G =25Ω (Note 1, 2)		136		ns
Fall-Time		t _F			36		ns
SOURCE- DRAIN DIODE RATIN	IGS AND CHA	RACTERIST	ICS				
Maximum Body-Diode Continuous Current		Is				5.0	Α
Maximum Body-Diode Pulsed Current		I_{SM}				20	Α
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =5.0A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =5.0A, V _{GS} =0V, dI _F /dt=100A/µs		125		ns
Body Diode Reverse Recovery Charge		Q_{rr}			0.51		μC

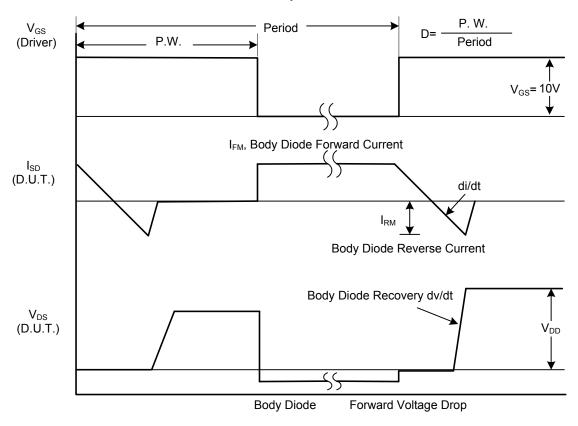
Notes: 1. Pulse Test: Pulse width \leq 300 μ s, Duty cycle \leq 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



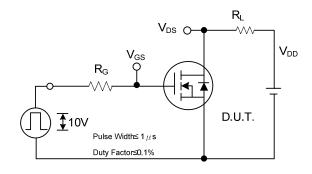
Peak Diode Recovery dv/dt Test Circuit

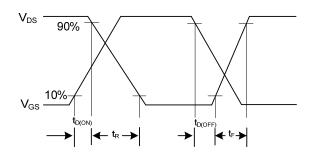


Peak Diode Recovery dv/dt Waveforms

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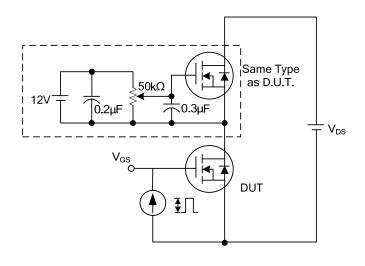
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

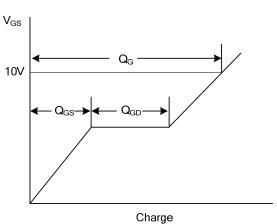




Switching Test Circuit

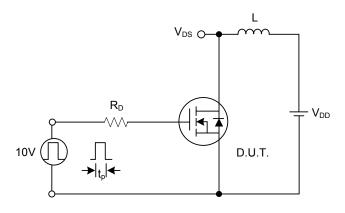
Switching Waveforms

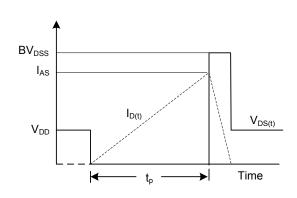




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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