

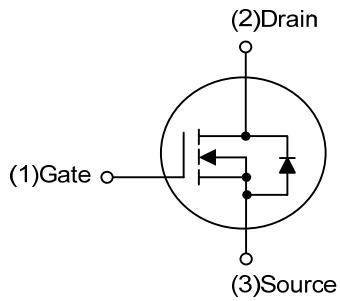
UTT100N75H**POWER MOSFET****100A, 75V N-CHANNEL
POWER MOSFET****■ DESCRIPTION**

The UTC **UTT100N75H** is a N-channel Power MOSFET, it uses UTC's advanced technology to provide excellent $R_{DS(ON)}$ with low gate charge, etc.

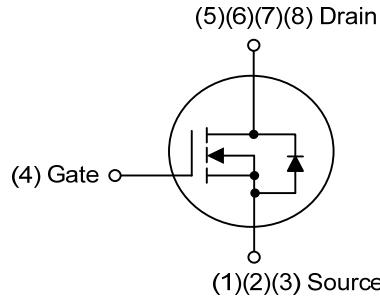
The UTC **UTT100N75H** is suitable for DC motor control, UPS and load switching, etc.

■ FEATURES

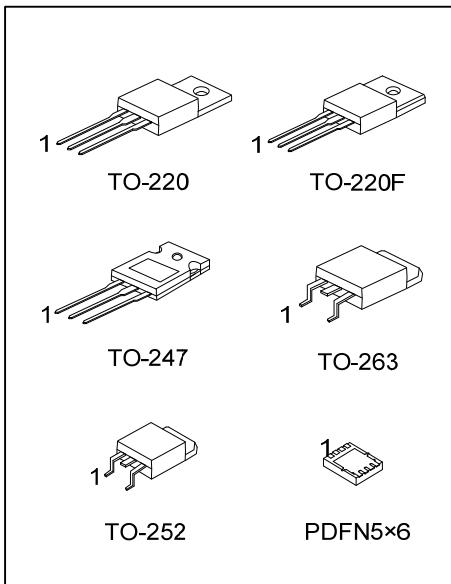
- * $R_{DS(ON)} \leq 9.0 \text{ m}\Omega @ V_{GS}=10V, I_D=50A$
- * High power and current handling capability
- * High speed switching
- * Low gate charge

■ SYMBOL

TO-220/TO-220F/TO-247
TO-252/TO-263



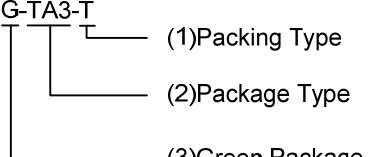
PDFN5x6



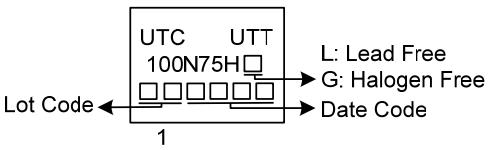
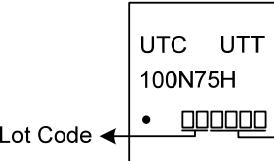
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTT100N75HL-TA3-T	UTT100N75HG-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UTT100N75HL-TF3-T	UTT100N75HG-TF3-T	TO-220F	G	D	S	-	-	-	-	-	Tube
UTT100N75HL-TN3-R	UTT100N75HG-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTT100N75HL-T47-T	UTT100N75HG-T47-T	TO-247	G	D	S	-	-	-	-	-	Tube
UTT100N75HL-TQ2-T	UTT100N75HG-TQ2-T	TO-263	G	D	S	-	-	-	-	-	Tube
UTT100N75HL-TQ2-R	UTT100N75HG-TQ2-R	TO-263	G	D	S	-	-	-	-	-	Tape Reel
UTT100N75HL-P5060-R	UTT100N75HG-P5060-R	PDFN5×6	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 UTT100N75HG-TA3-T	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TN3: TO-252 T47: TO-247, TQ2: TO-263, P5060: PDFN5×6 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

TO-220 / TO-220F / TO-247 / TO-252 / TO-263	PDFN5×6
 Lot Code ← → Date Code	 Lot Code ← → Date Code

■ ABSOLUTE MAXIMUM RATING ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	75	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	$T_C=25^\circ\text{C}$	I_D	100
		$T_C=100^\circ\text{C}$		48
	Pulsed (Note 2)		I_{DM}	200
Peak diode recovery voltage		dv/dt	3.77	V/ns
Avalanche Energy (Note 3)		E_{AS}	180	mJ
Power Dissipation ($T_C=25^\circ\text{C}$)		TO-220	P_D	180
		TO-263		W
		TO-220F		45
		TO-252		64
		TO-247		230
		PDFN5x6		60
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=0.1\text{mH}$, $I_{AS}=60\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD}\leq 30\text{A}$, $di/dt\leq 200\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient	steady state	TO-220	θ_{JA}	62.5	
		TO-220F			
		TO-263			
		TO-252			
		TO-247			
Junction to Case		PDFN5x6		35.7 (Note)	
		TO-220	θ_{JC}	0.69	
		TO-263		$^\circ\text{C/W}$	
		TO-220F		2.77	
		TO-252		$^\circ\text{C/W}$	
		TO-247		1.95 (Note)	
		PDFN5x6		0.54	
				$^\circ\text{C/W}$	
				2 (Note)	

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

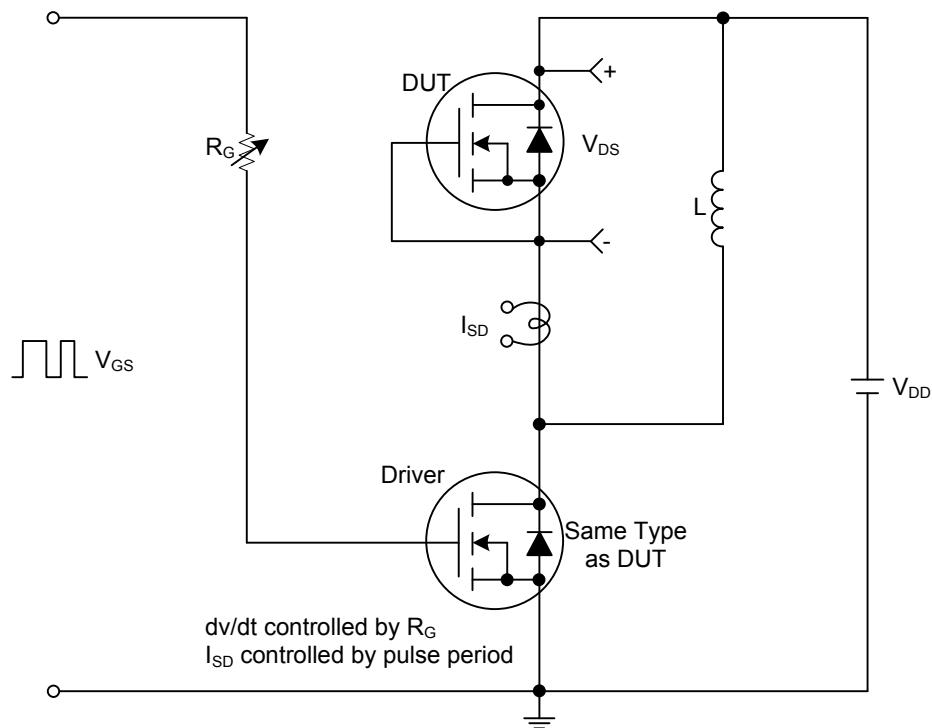
■ ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ C$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu A, V_{GS}=0V$	75			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=75V, V_{GS}=0V, T_C=25^\circ C$ $V_{DS}=75V, V_{GS}=0V, T_C=125^\circ C$		1		μA
Gate-Source Leakage Current	Forward	$V_{GS}=+20V, V_{DS}=0V$		+100		nA
	Reverse	$V_{GS}=-20V, V_{DS}=0V$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=50A$		9.0		$m\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=25V, f=1.0MHz$		4200		pF
Output Capacitance	C_{OSS}			365		pF
Reverse Transfer Capacitance	C_{RSS}			305		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DS}=60V, V_{GS}=10V, I_D=100A$ $I_D=1mA$		115		nC
Gate to Source Charge	Q_{GS}			19		nC
Gate to Drain Charge	Q_{GD}			49		nC
Turn-on Delay Time	$t_{D(ON)}$			18		ns
Rise Time	t_R	$V_{DD}=37.5V, V_{GS}=10V, I_D=100A$ $R_G=3\Omega$		22		ns
Turn-off Delay Time	$t_{D(OFF)}$			54		ns
Fall-Time	t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Drain-Source Diode Forward Current	I_S	$I_S=100A, V_{GS}=0V$		100		A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}			1.2		V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}			50		ns
Body Diode Reverse Recovery Charge (Note 1)	Q_{rr}			70		nC

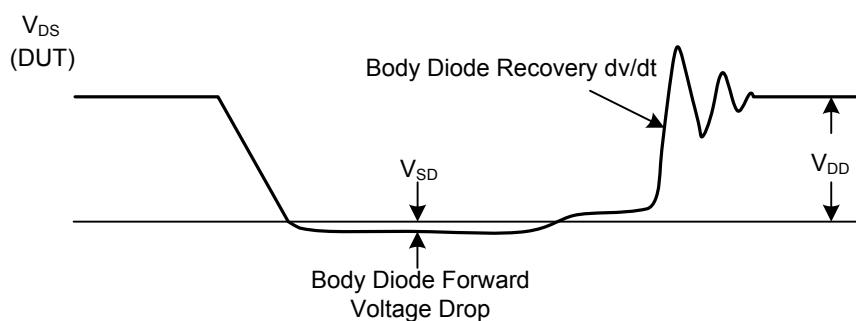
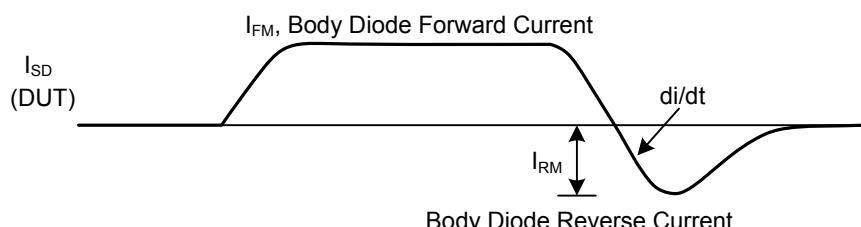
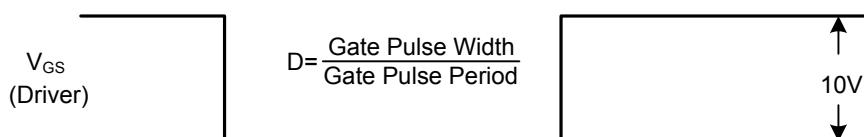
Notes: 1. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



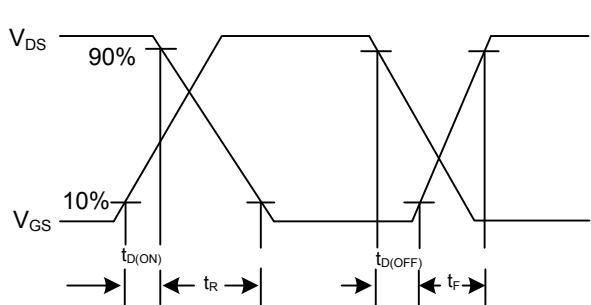
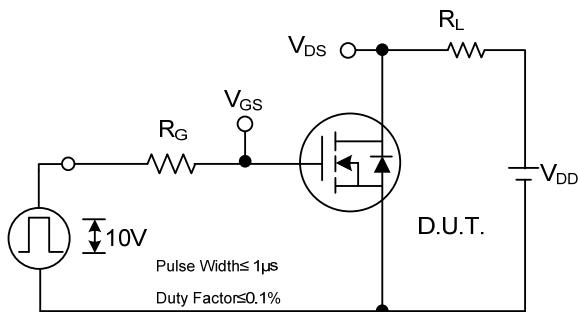
Peak Diode Recovery dV/dt Test Circuit



Peak Diode Recovery dV/dt Test Circuit and Waveforms

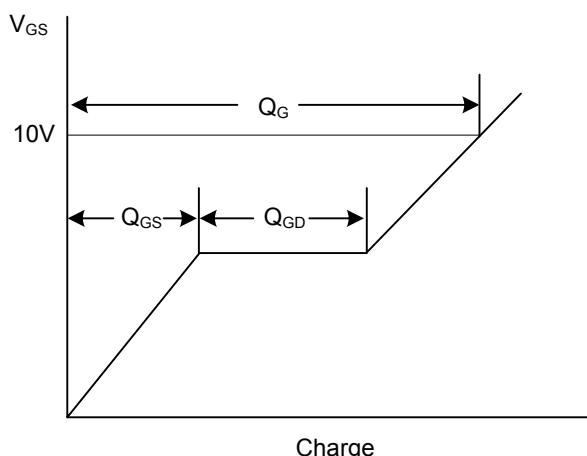
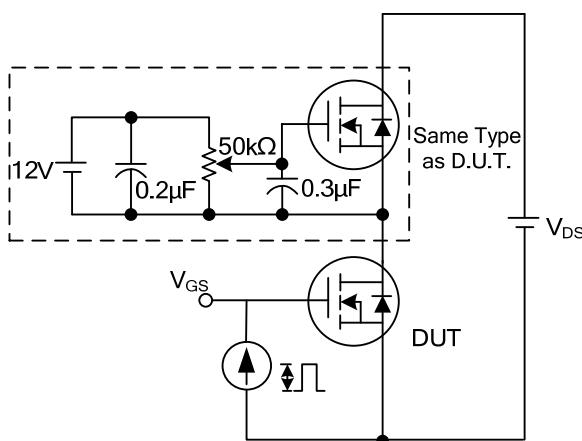
Peak Diode Recovery dV/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS



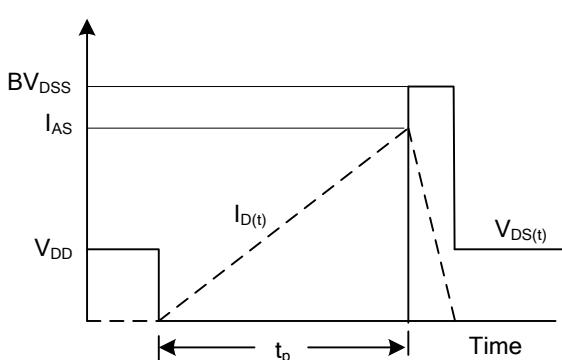
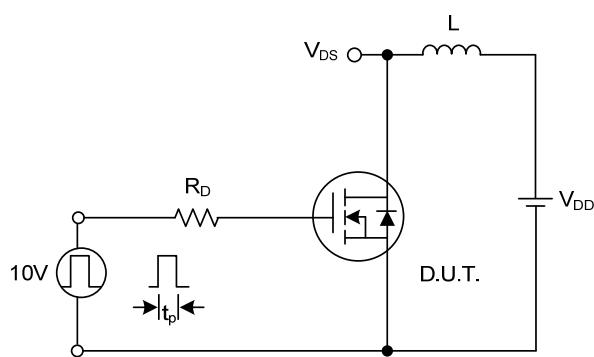
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

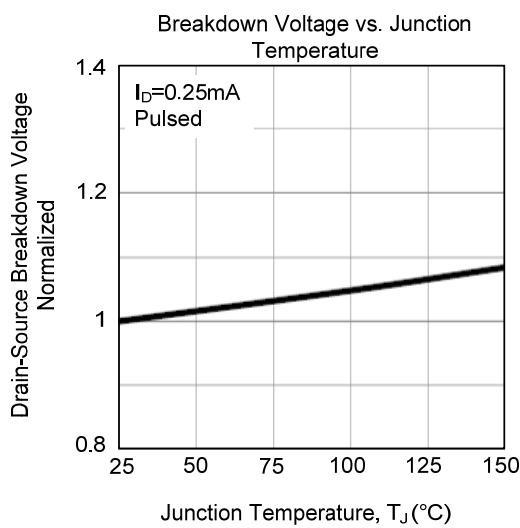
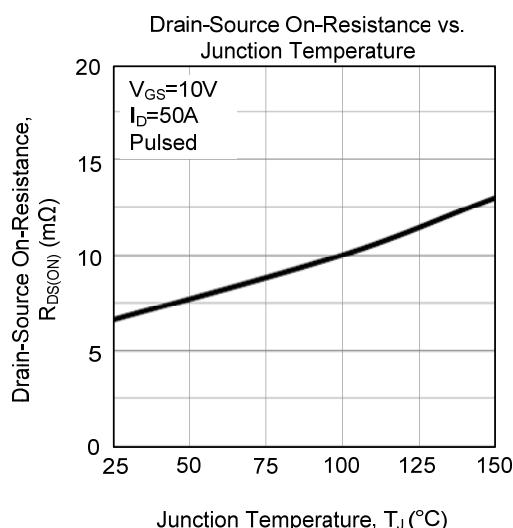
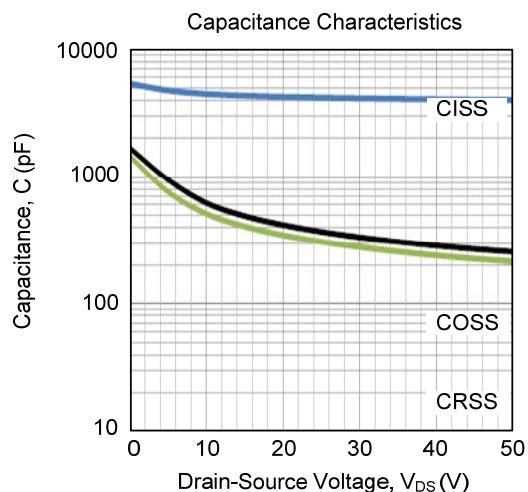
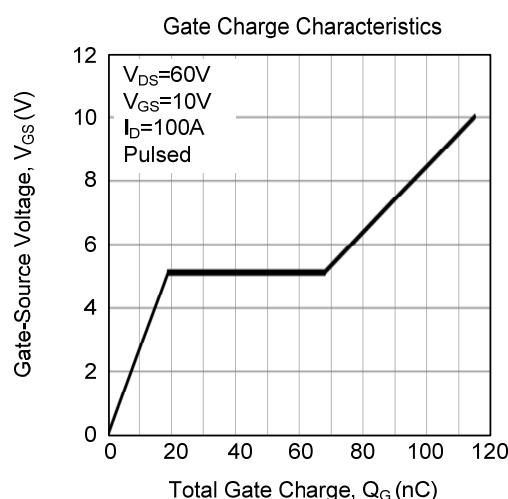
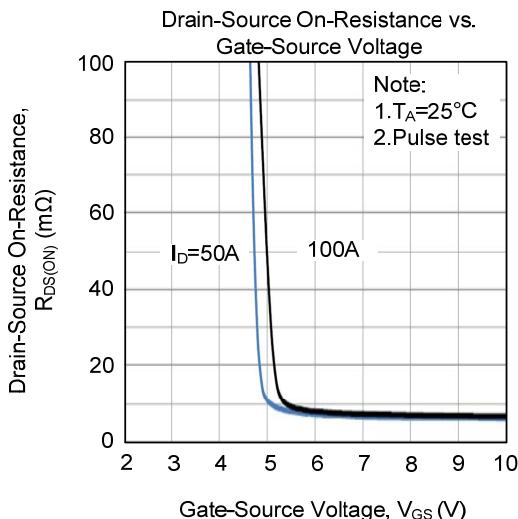
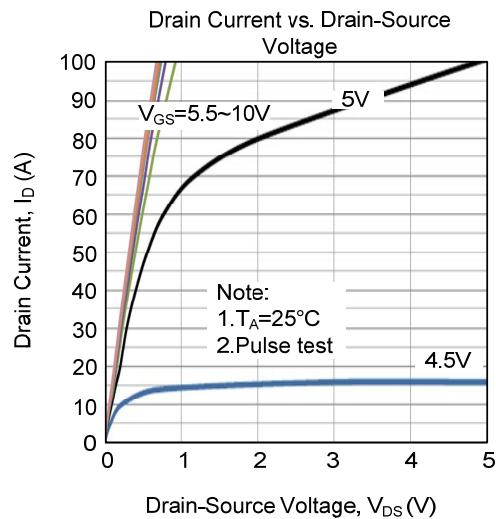
Gate Charge Waveform



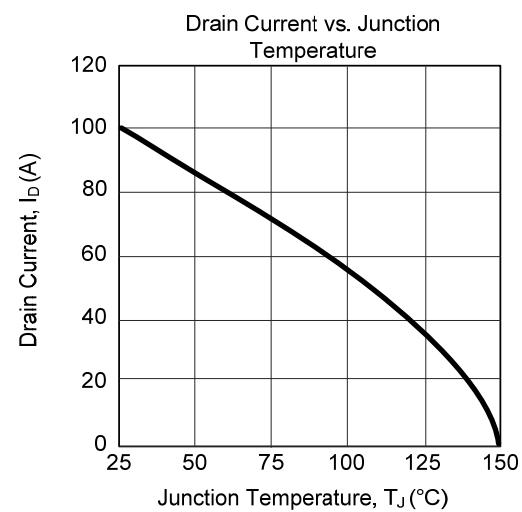
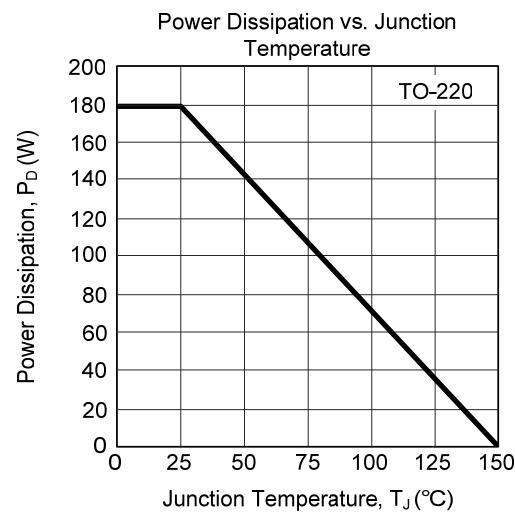
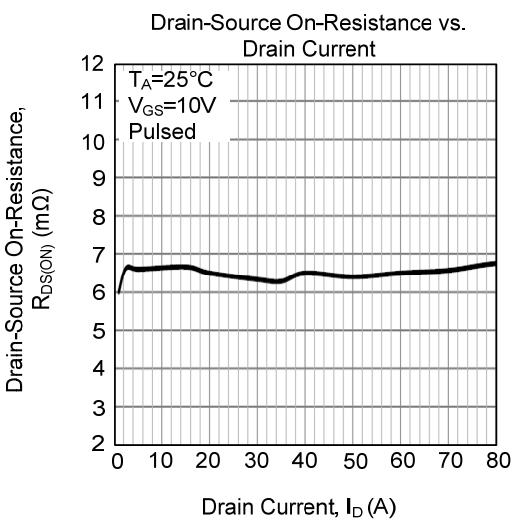
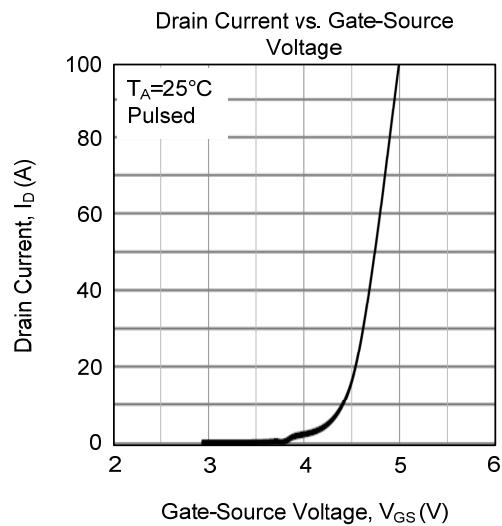
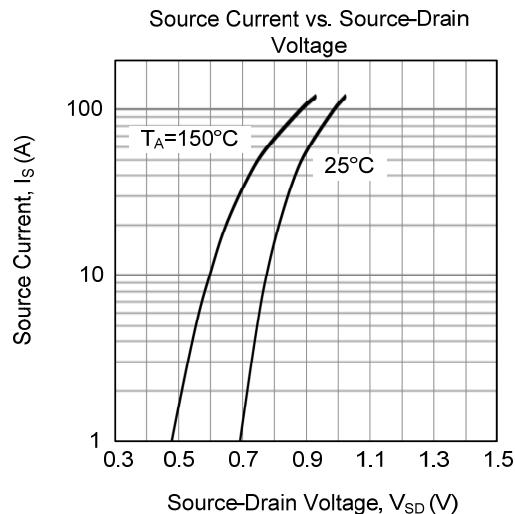
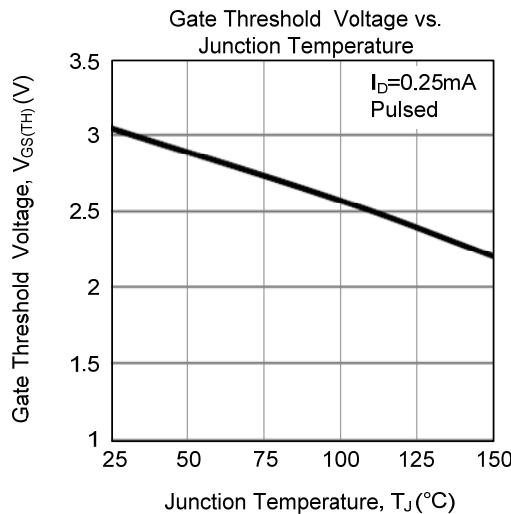
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

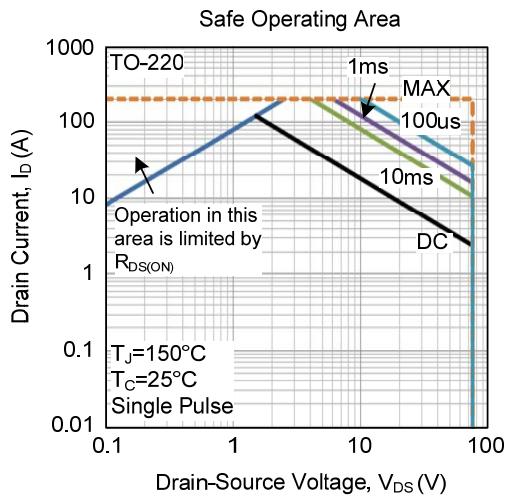
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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