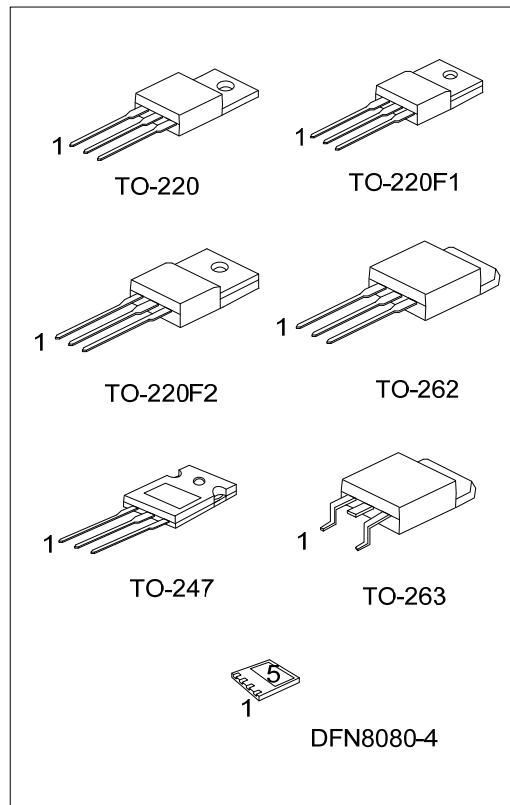
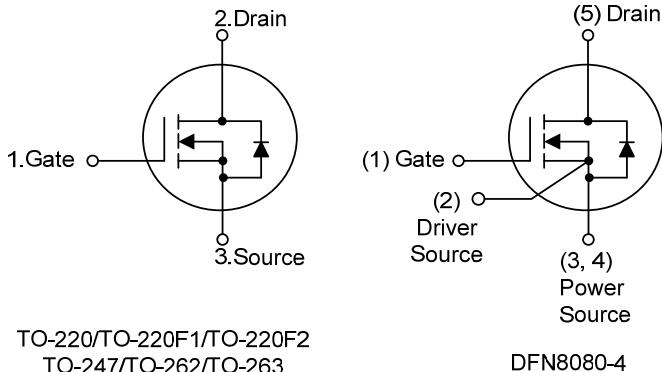


20NM60**Power MOSFET****20A, 600V N-CHANNEL
SUPER-JUNCTION MOSFET****■ DESCRIPTION**

The **UTC 20NM60** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)} \leq 0.21 \Omega$ @ $V_{GS}=10V$, $I_D=10A$
- * By using Super Junction Structure
- * Fast Switching
- * With 100% Avalanche Tested

**■ SYMBOL**

■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment					Packing
Lead Free	Halogen Free		1	2	3	4	5	
20NM60L-TA3-T	20NM60G-TA3-T	TO-220	G	D	S	-	-	Tube
20NM60L-TF1-T	20NM60G-TF1-T	TO-220F1	G	D	S	-	-	Tube
20NM60L-TF2-T	20NM60G-TF2-T	TO-220F2	G	D	S	-	-	Tube
20NM60L-T2Q-T	20NM60G-T2Q-T	TO-262	G	D	S	-	-	Tube
20NM60L-TQ2-T	20NM60G-TQ2-T	TO-263	G	D	S	-	-	Tube
20NM60L-TQ2-R	20NM60G-TQ2-R	TO-263	G	D	S	-	-	Tape Reel
20NM60L-T47-T	20NM60G-T47-T	TO-247	G	D	S	-	-	Tube
20NM60L-K04-8080-R	20NM60G-K04-8080-R	DFN8080-4	G	S	S	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 20NM60G-TA3-T	(1) T: Tube, R:Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, T2Q: TO-262, TQ2: TO-263, T47: TO-247, K04-8080: DFN8080-4 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

TO-220 / TO-220F1 / TO-220F2 TO-247 / TO-262/TO-263	DFN8080-4
 Lot Code ← 1 → Date Code	 Lot Code ← • → Date Code

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	Continuous	$T_c=25^\circ\text{C}$	20	A
		$T_c=100^\circ\text{C}$	13	A
Pulsed Drain Current	Pulsed (Note 2)	I_{DM}	60	A
Avalanche energy	Single Pulsed (Note 3)	E_{AS}	265	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.8	V/nS
Power Dissipation	TO-220/TO-262/TO-263	P_D	114	W
	TO-220F1/TO-220F2		34	W
	TO-247		130	W
	DFN8080-4		63.5	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
 3. $L=30\text{mH}$, $I_{AS}=4.2\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J = 25^\circ\text{C}$.
 4. $I_{SD} \leq 10\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(BR)DSS}$, $T_J = 25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F1	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F2/TO-262			
	TO-263		40	$^\circ\text{C/W}$
Junction to Case	TO-247	θ_{JC}	35 (Note)	$^\circ\text{C/W}$
	DFN8080-4			
	TO-220/TO-262		1.09	$^\circ\text{C/W}$
	TO-263		3.67	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		0.96	$^\circ\text{C/W}$
	TO-247		1.95 (Note)	$^\circ\text{C/W}$
	DFN8080-4			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

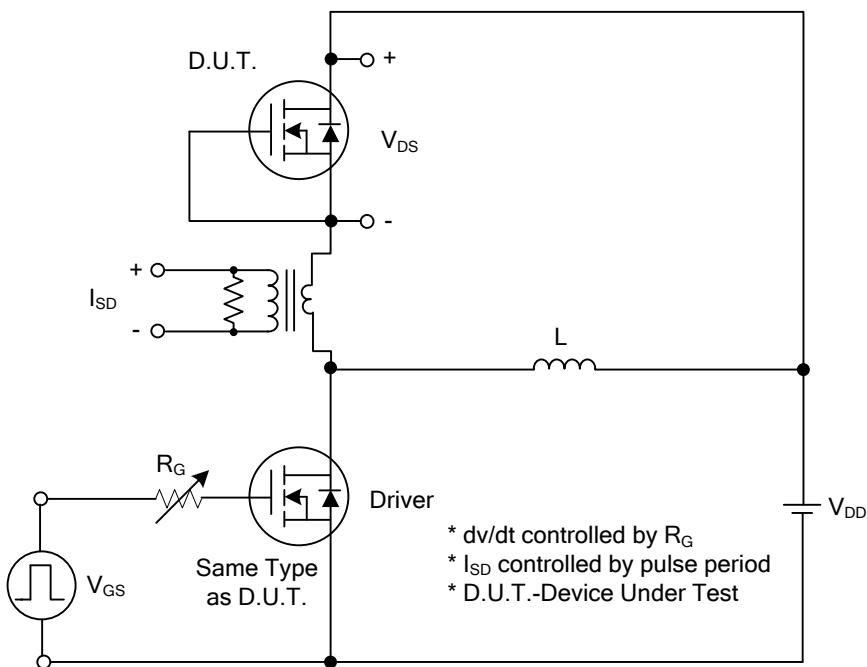
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=+30\text{V}$		+100		nA
	Reverse	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.5		4.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$			0.21	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=50\text{V}, f=1.0\text{MHz}$		1300		pF
Output Capacitance	C_{OSS}			145		pF
Reverse Transfer Capacitance	C_{RSS}			5		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{\text{DS}}=480\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}, I_{\text{G}}=1\text{mA}$ (Note 1, 2)		47		nC
Gate to Source Charge	Q_{GS}			12		nC
Gate to Drain Charge	Q_{GD}			20		nC
Turn-on Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$			20		ns
Rise Time	t_R			28		ns
Turn-off Delay Time	$t_{\text{D}(\text{OFF})}$			146		ns
Fall-Time	t_F			57		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Pulsed Current	I_S				20	A
Drain-Source Diode Forward Voltage (Note 1)	I_{SM}				60	A
Maximum Body-Diode Continuous Current	V_{SD}	$I_S=20\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$I_S=20\text{A}, V_{\text{GS}}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		421		ns
Reverse Recovery Charge	Q_{rr}			7.5		μC

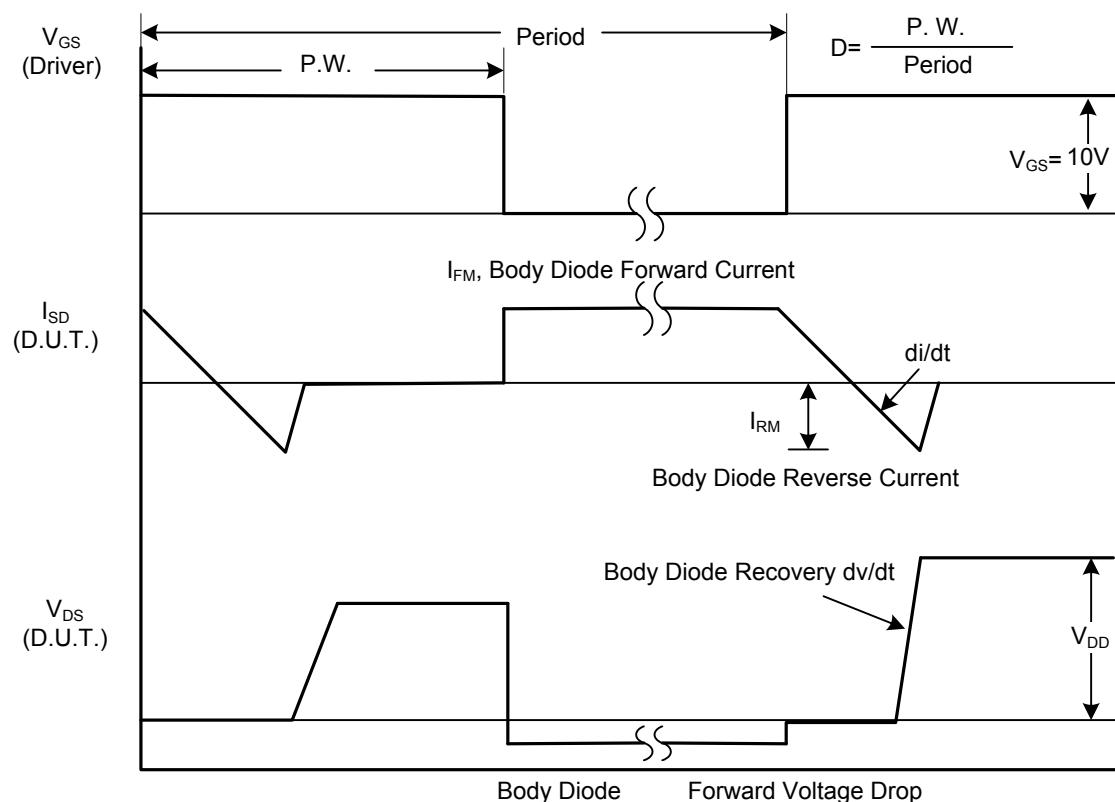
Note: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

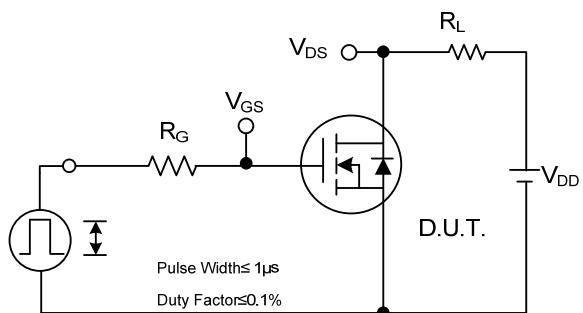


Peak Diode Recovery dv/dt Test Circuit

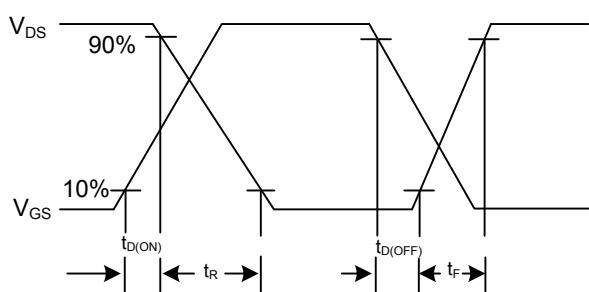


Peak Diode Recovery dv/dt Waveforms

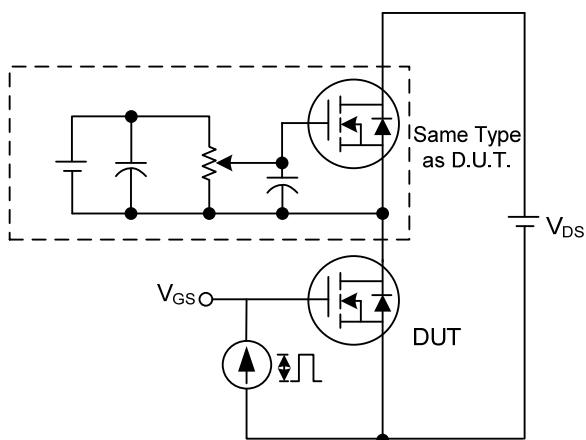
■ TEST CIRCUITS AND WAVEFORMS



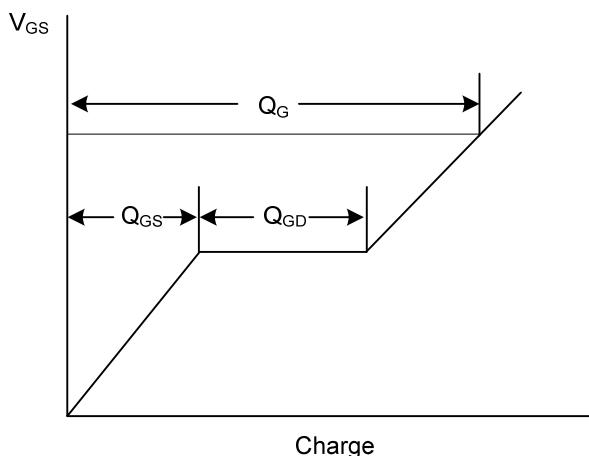
Switching Test Circuit



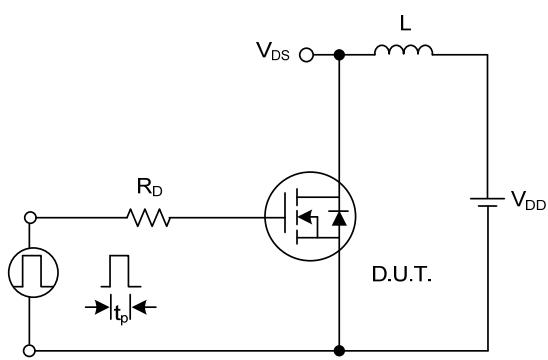
Switching Waveforms



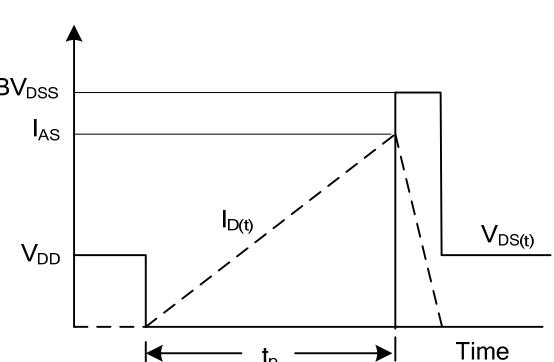
Gate Charge Test Circuit



Gate Charge Waveform

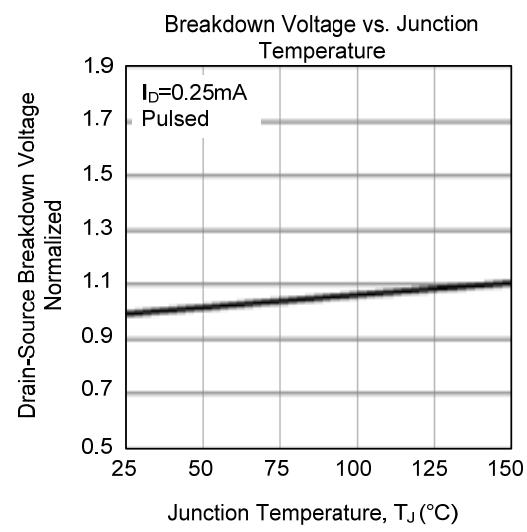
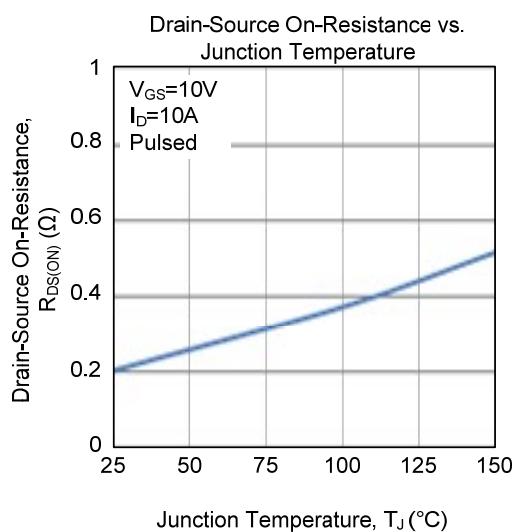
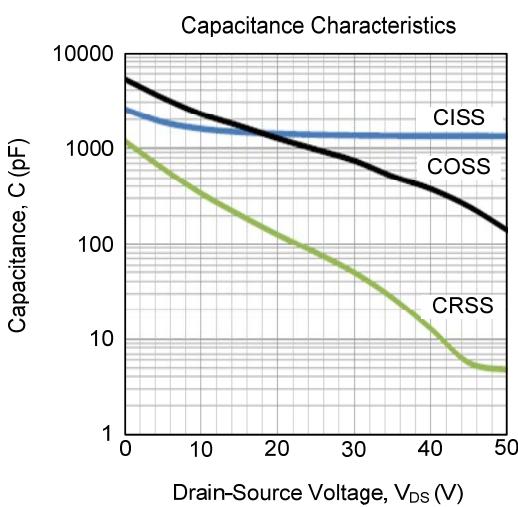
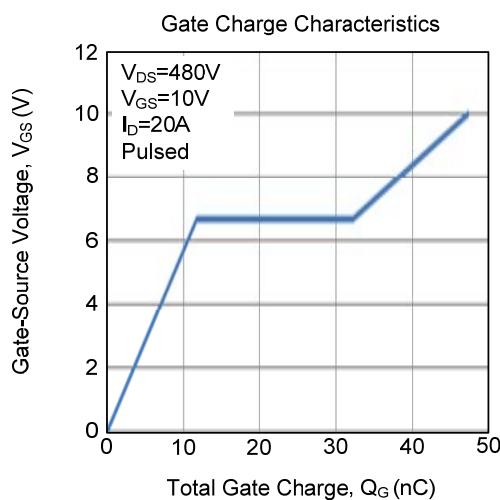
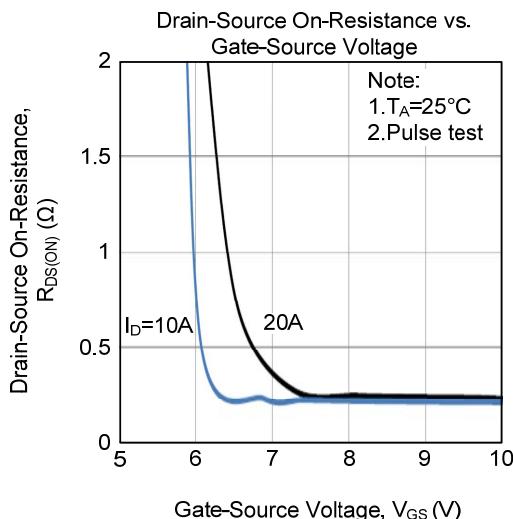
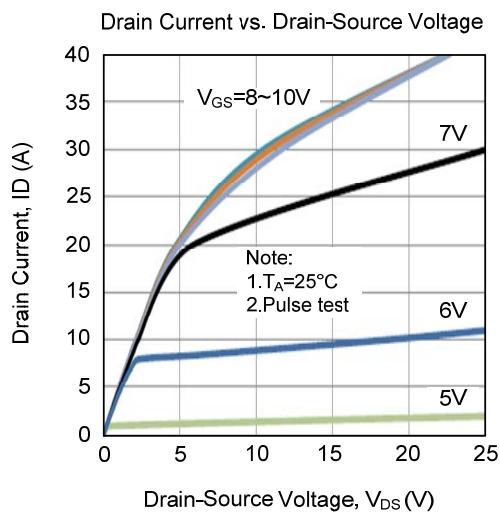


Unclamped Inductive Switching Test Circuit

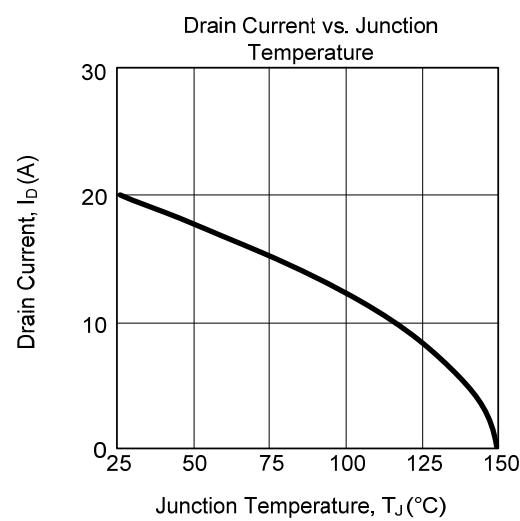
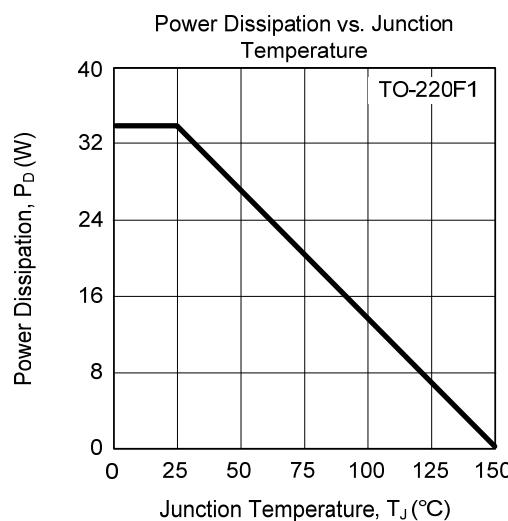
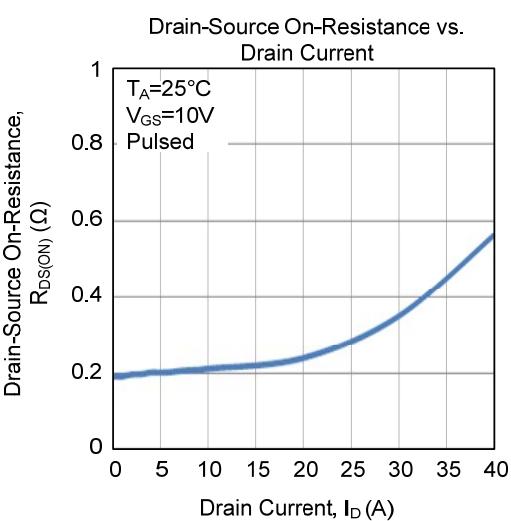
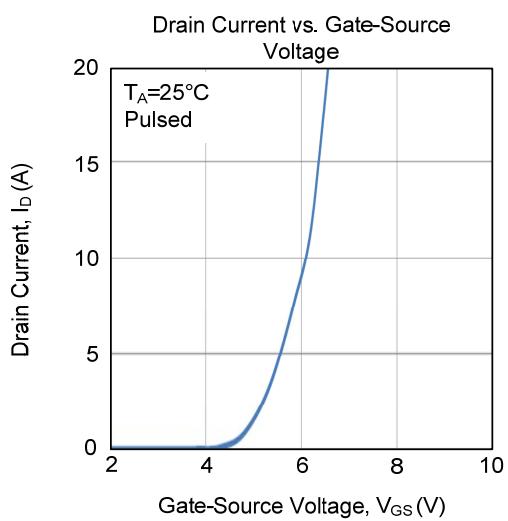
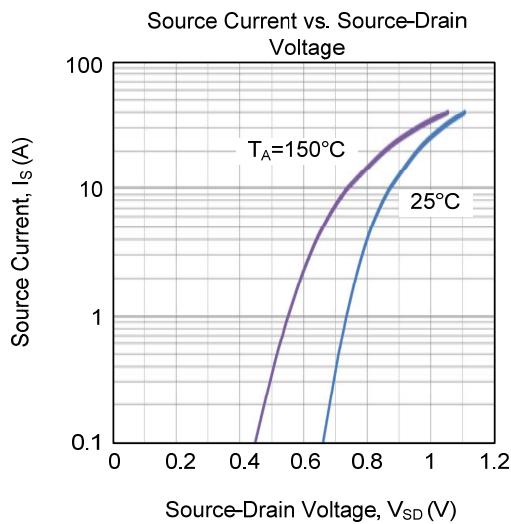
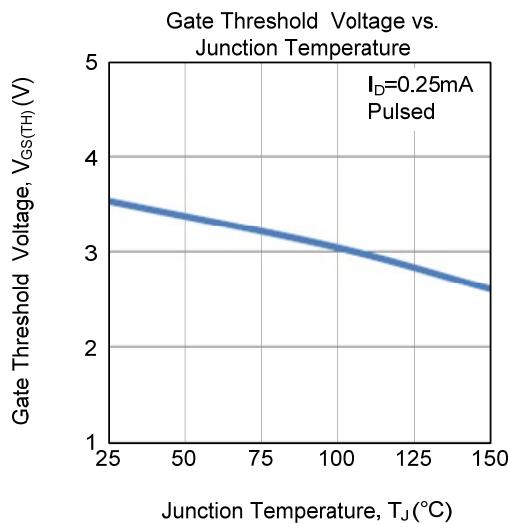


Unclamped Inductive Switching Waveforms

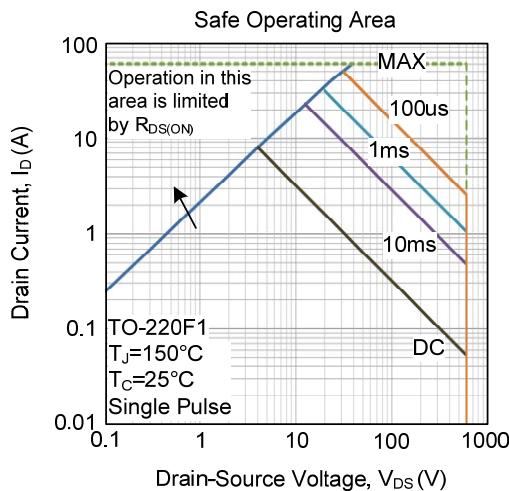
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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