# UTC UNISONIC TECHNOLOGIES CO., LTD

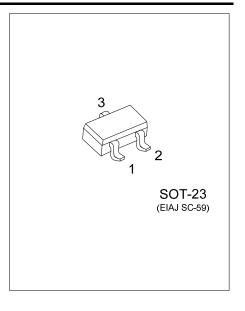
UT2309A **Preliminary Power MOSFET** 

# -3.7A, -30V P-CHANNEL **ENHANCEMENT MODE POWER MOSFET**

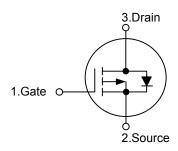
#### **DESCRIPTION**

The UT2309A is a P-channel power MOSFET, designed with high density cell with fast switching speed, ultra low on-resistance and excellent thermal and electrical capabilities.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



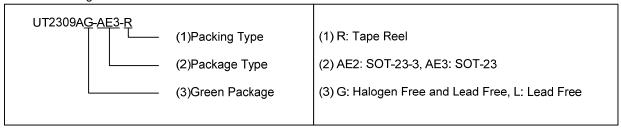
#### **SYMBOL**



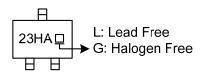
## ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT2309AL-AE2-R	UT2309AG-AE2-R	SOT-23-3	G	S	D	Tape Reel	
UT2309AL-AE3-R	UT2309AG-AE3-R	SOT-23	G	S	D	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



#### **MARKING**



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#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>C</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{ t DSS}$	-30	V	
Gate-Source Voltage		$V_{GSS}$	±20	V	
Continuous Drain Current	Continuous	$I_{D}$	-3.7	Α	
Pulsed Drain Current	Pulsed (Note 2)	$I_{DM}$	-12	Α	
Avalanche Current (Note 2)		$I_{AR}$	-12	Α	
Avalanche Energy	Single Pulsed (Note 3)	E <sub>AS</sub>	7.2	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.1	V/ns	
Total Power Dissipation		$P_{D}$	1.38	W	
Junction Temperature		$T_J$	+150	°C	
Storage Temperature		$T_{STG}$	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by T<sub>J</sub>.
- 3. L=0.1mH,  $I_{AS}$ =-12A,  $V_{DD}$ =-30V,  $R_{G}$ =25  $\Omega$ , Starting  $T_{J}$  = 25°C
- 4.  $I_{SD} \le 3.0A$ , di/dt $\le 200A/\mu s$ ,  $V_{DD} \le BV_{DSS}$ , Starting  $T_J = 25$ °C

#### ■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	90	°C/W

Note: Surface mounted on 1 in <sup>2</sup> copper pad of FR4 board.

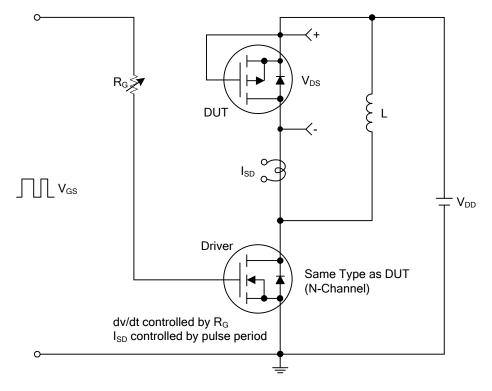
# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30			V		
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-0.5	uA		
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V			±100	nA		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250uA$			-3	V		
Static Drain-Source On-Resistance (Note 1)	В	$V_{GS}$ =-10V, $I_D$ =-3A			75	mΩ		
Static Drain-Source On-Resistance (Note 1)	$R_{DS(ON)}$	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.6A			120	mΩ		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C <sub>ISS</sub>	V -0V V - 25V		705		pF		
Output Capacitance	Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, -f=1.0MHz		85		pF		
Reverse Transfer Capacitance	$C_{RSS}$	1-1:0101112		75		pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge (Note 1)	$Q_G$	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V,		56.5		nC		
Gate-Source Charge	$Q_GS$	v <sub>DS</sub> =-30v, v <sub>GS</sub> =-10v,   I <sub>D</sub> =-0.5A		2.8		nC		
Gate-Drain Charge	$Q_GD$	ID0.3A		5.8		nC		
Turn-ON Delay Time (Note 1)	t <sub>D(ON)</sub>			34		ns		
Turn-ON Rise Time	t <sub>R</sub>	$V_{DS}$ =-30V, $I_{D}$ =-0.5A,		64		ns		
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	$R_G=25\Omega$ , $V_{GS}=-10V$		206		ns		
Turn-OFF Fall Time	t <sub>F</sub>			168		ns		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Continuous Drain-Source Current	Is				-3.7	Α		
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				-12	Α		
Forward On Voltage (Note 1)	$V_{SD}$	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V			-1.2	V		
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =-3A, V <sub>GS</sub> =0V,		540		ns		
Reverse Recovery Charge	Q <sub>rr</sub>	dI/dt=-100A/μs		1810		nC		

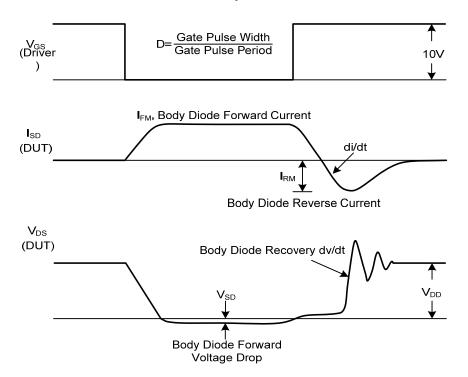
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle≤2%.

2. Essentially independent of operating temperature.

# **■ TEST CIRCUITS AND WAVEFORMS**

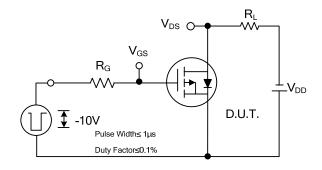


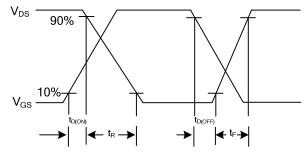
### Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

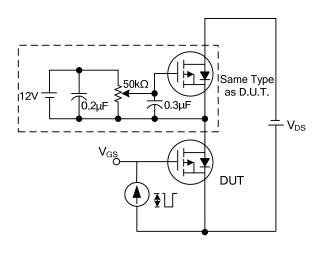
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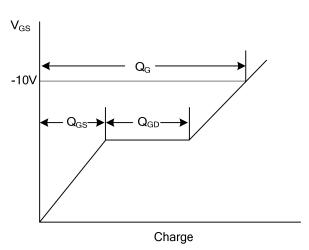




**Switching Test Circuit** 

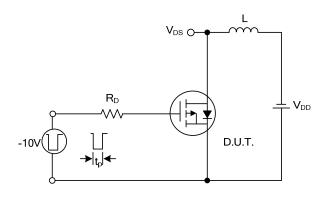
**Switching Waveforms** 

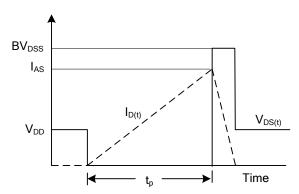




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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