



# 15NM70-U2

Power MOSFET

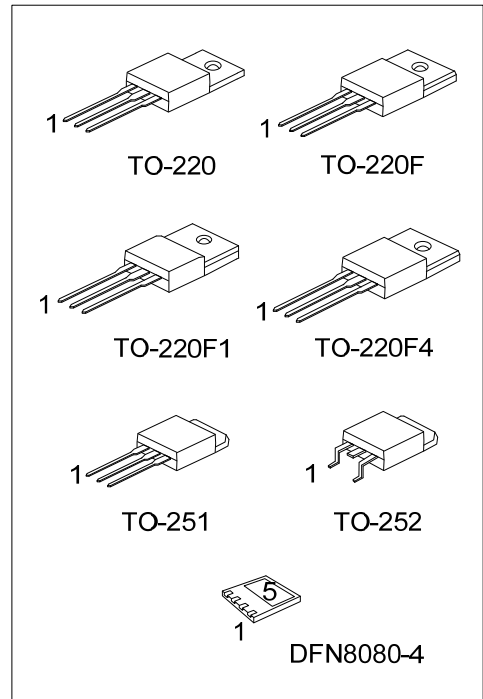
## 15A, 700V N-CHANNEL SUPER-JUNCTION MOSFET

### DESCRIPTION

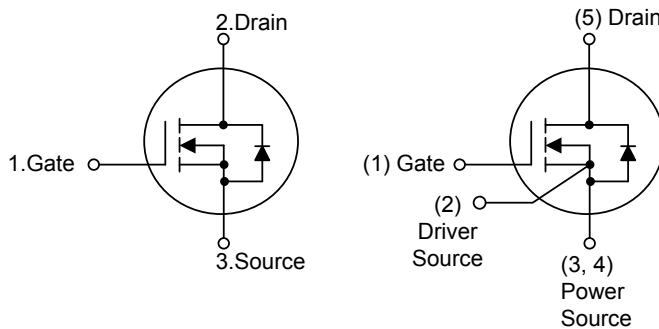
The **UTC 15NM70-U2** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

### FEATURES

- \*  $R_{DS(ON)} \leq 0.45 \Omega @ V_{GS}=10V, I_D=7.5A$
- \* By using Super Junction Structure
- \* Fast Switching
- \* With 100% Avalanche Tested



### SYMBOL



TO-220 / TO-220F / TO-220F1  
TO-220F4 / TO-251 / TO-252

DFN8080-4

### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment					Packing
Lead Free	Halogen Free		1	2	3	4	5	
15NM70L-TA3-T	15NM70G-TA3-T	TO-220	G	D	S	-	-	Tube
15NM70L-TF3-T	15NM70G-TF3-T	TO-220F	G	D	S	-	-	Tube
15NM70L-TF1-T	15NM70G-TF1-T	TO-220F1	G	D	S	-	-	Tube
15NM70L-TF34-T	15NM70G-TF34-T	TO-220F4	G	D	S	-	-	Tube
15NM70L-TM3-T	15NM70G-TM3-T	TO-251	G	D	S	-	-	Tube
15NM70L-TN3-R	15NM70G-TN3-R	TO-252	G	D	S	-	-	Tape Reel
15NM70L-K04-8080-R	15NM70G-K04-8080-R	DFN8080-4	G	S	S	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>15NM70G-TA3-T</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF34: TO-220F4, TM3: TO-251, TN3: TO-252 K04-8080: DFN8080-4 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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### MARKING

TO-220 / TO-220F / TO-220F1 TO-220F4 / TO-251 / TO-252	DFN8080-4
<p>UTC 15NM70</p> <p>Lot Code ← [ ] [ ] [ ] [ ] [ ] [ ] [ ] [ ] → Date Code</p> <p>L: Lead Free G: Halogen Free</p> <p>1</p>	<p>UTC 15NM70</p> <p>Lot Code ← [ ] [ ] [ ] [ ] [ ] [ ] [ ] [ ] • [ ] [ ] [ ] [ ] [ ] [ ] [ ] [ ] → Date Code</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	700	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	Continuous	$I_D$	15	A
Pulsed Drain Current	Pulsed (Note 2)	$I_{DM}$	30	A
Avalanche Current (Note 3)		$I_{AR}$	2.4	A
Avalanche energy	Single Pulsed (Note 3)	$E_{AS}$	202.1	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.7	V/nS
Power Dissipation	TO-220	$P_D$	94	W
	TO-220F/TO-220F1		32	W
	TO-220F4			
	TO-251/TO-252			
	DFN8080-4			
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature Range		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=66\text{mH}$ ,  $I_{AS}=2.4\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J = 25^\circ\text{C}$ .

4.  $I_{SD} \leq 15\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J = 25^\circ\text{C}$ .

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
	TO-220F1/TO-220F4			
	TO-251/TO-252		110	$^\circ\text{C}/\text{W}$
	DFN8080-4		35 (Note)	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220	$\theta_{JC}$	1.32	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1		3.9	$^\circ\text{C}/\text{W}$
	TO-220F4			
	TO-251/TO-252		1.62 (Note)	$^\circ\text{C}/\text{W}$
	DFN8080-4		3.125 (Note)	$^\circ\text{C}/\text{W}$

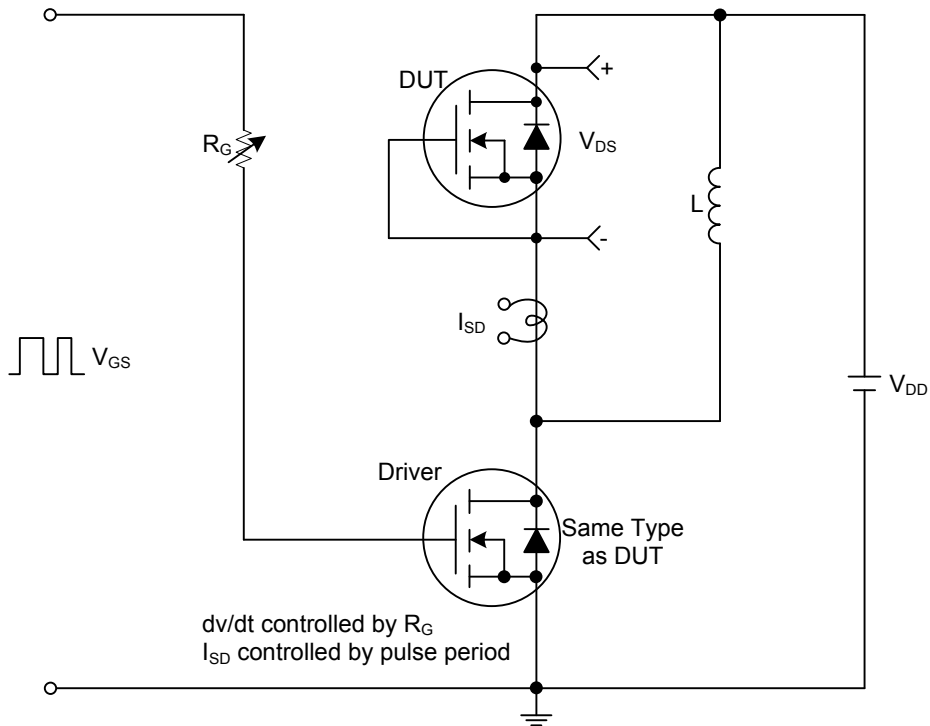
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub> =25°C, unless otherwise specified)

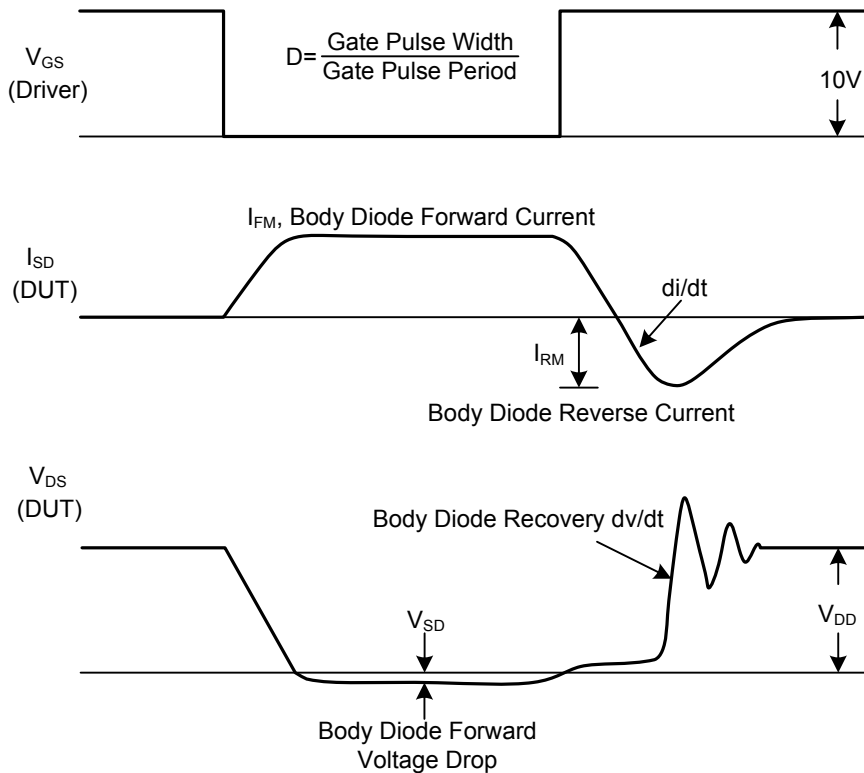
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	700			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V			10	μA
Gate-Source Leakage Current	Forward	V <sub>DS</sub> =0V, V <sub>GS</sub> =+30V			+100	nA
	Reverse		V <sub>DS</sub> =0V, V <sub>GS</sub> =-30V			-100
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	2.5		4.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =7.5A			0.45	Ω
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		933.7		pF
Output Capacitance	C <sub>OSS</sub>			594.7		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			52.8		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 1)	Q <sub>G</sub>	V <sub>DS</sub> =560V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A, I <sub>G</sub> =1mA (Note 1, 2)		32.4	45	nC
Gate to Source Charge	Q <sub>GS</sub>			9		nC
Gate to Drain Charge	Q <sub>GD</sub>			11.4		nC
Turn-on Delay Time (Note 1)	t <sub>D(ON)</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =15A, R <sub>G</sub> =25Ω (Note 1, 2)		10.6		ns
Rise Time	t <sub>R</sub>			23.6		ns
Turn-off Delay Time	t <sub>D(OFF)</sub>			110.6		ns
Fall-Time	t <sub>F</sub>			59.3		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Pulsed Current	I <sub>S</sub>				15	A
Maximum Body-Diode Continuous Current	I <sub>SM</sub>				60	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =15A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	I <sub>S</sub> =15A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs		410.6		ns
Reverse Recovery Charge	Q <sub>rr</sub>				13.1	

Note: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%  
 2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



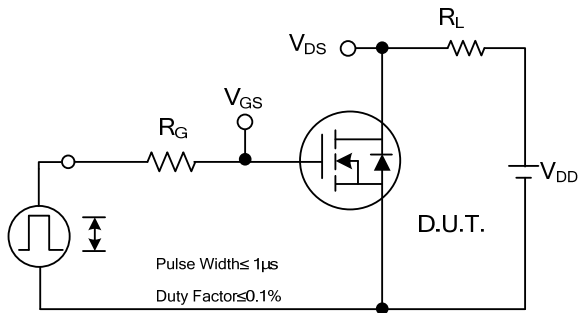
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

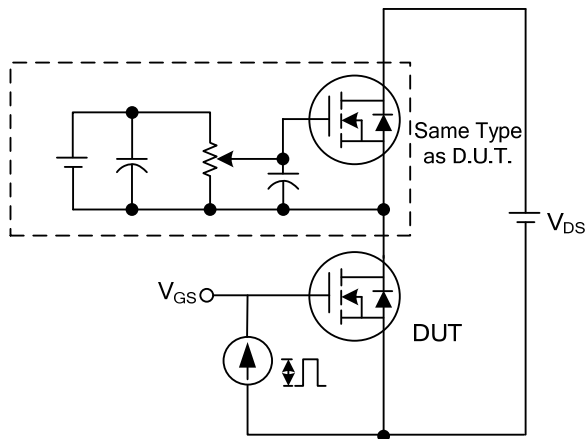
## TEST CIRCUITS AND WAVEFORMS



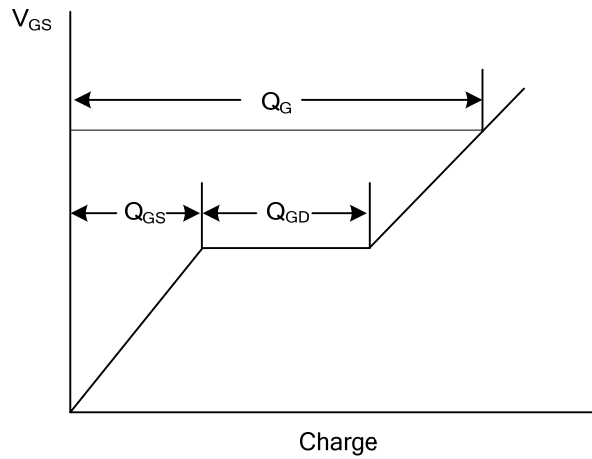
**Switching Test Circuit**



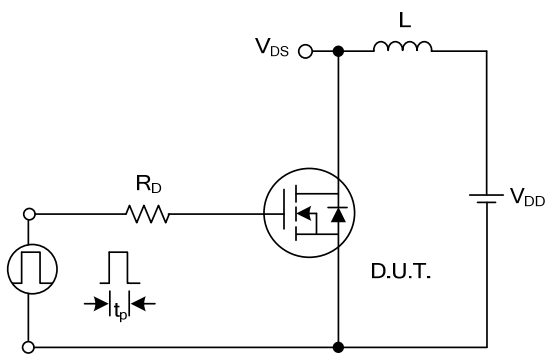
**Switching Waveforms**



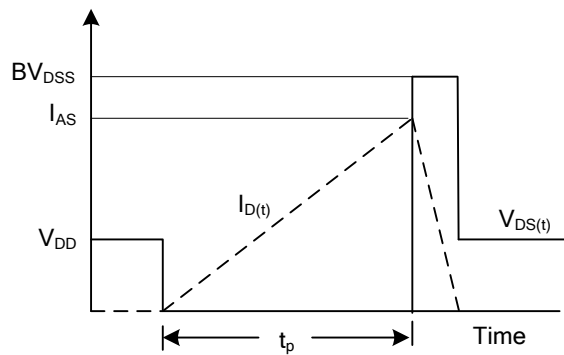
**Gate Charge Test Circuit**



**Gate Charge Waveform**

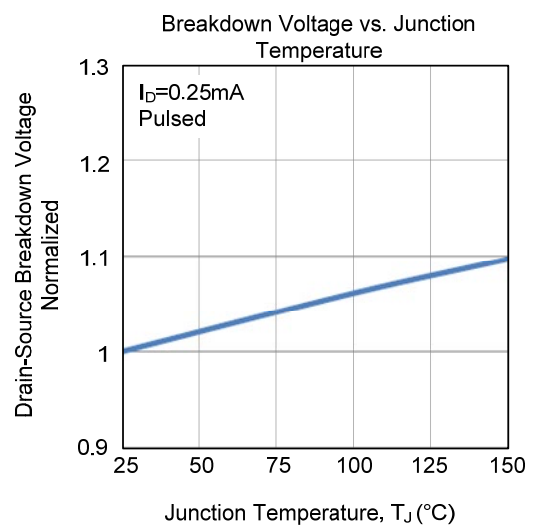
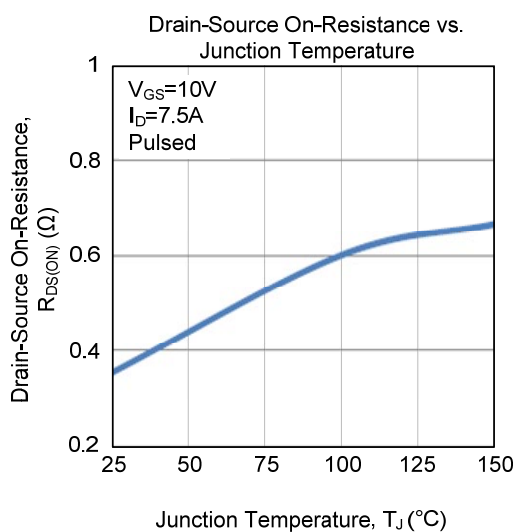
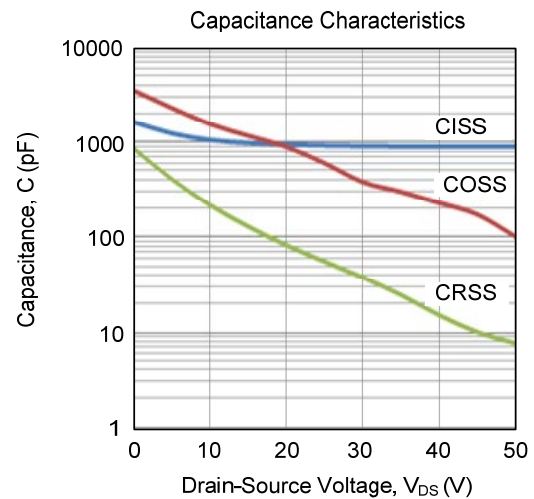
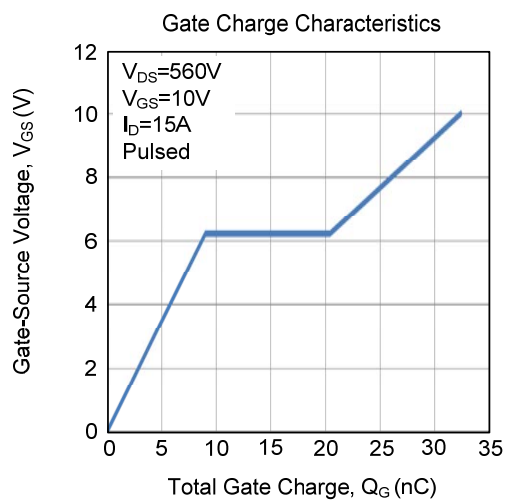
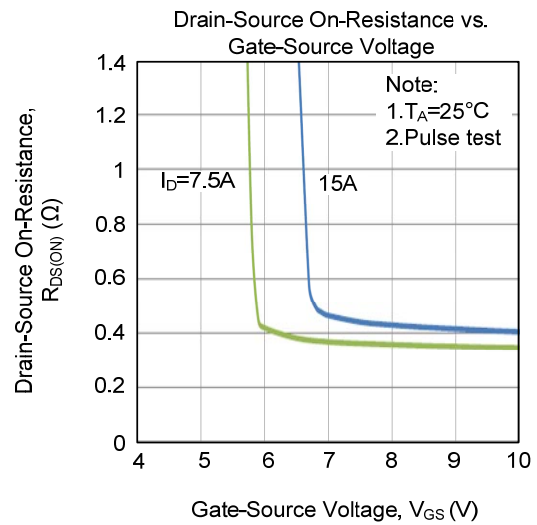
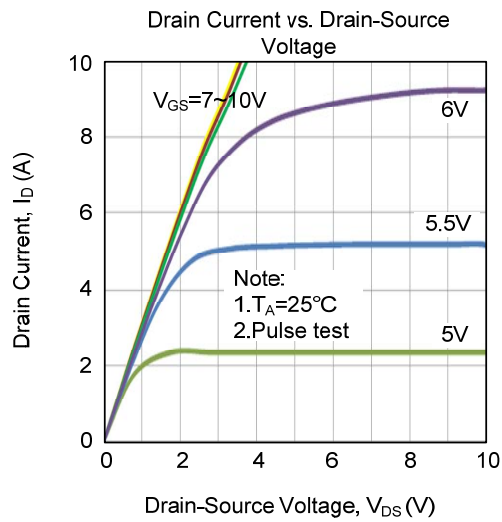


**Unclamped Inductive Switching Test Circuit**

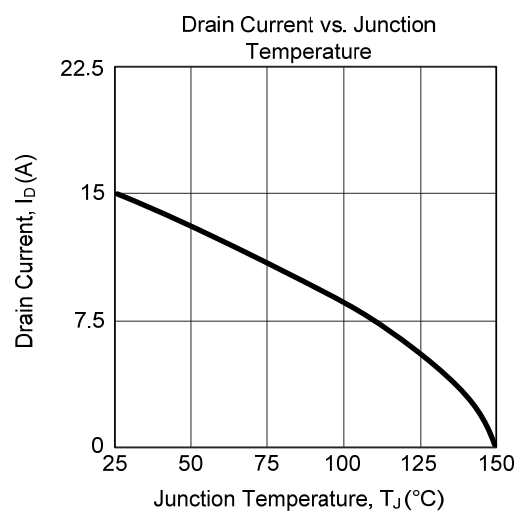
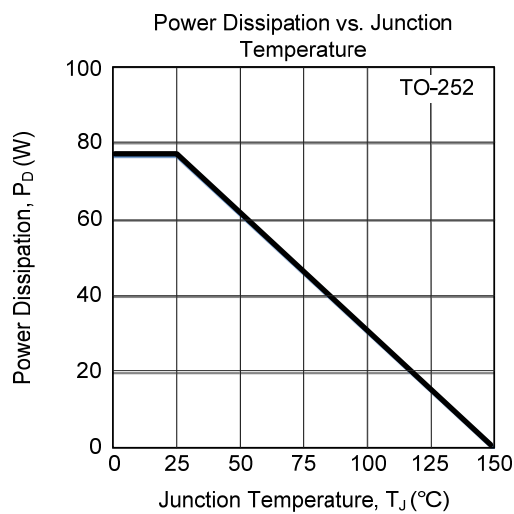
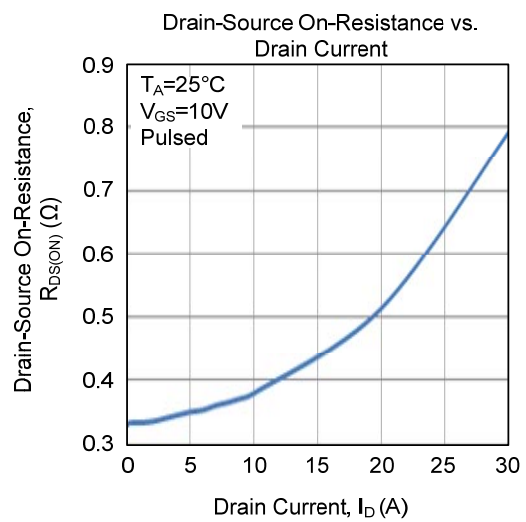
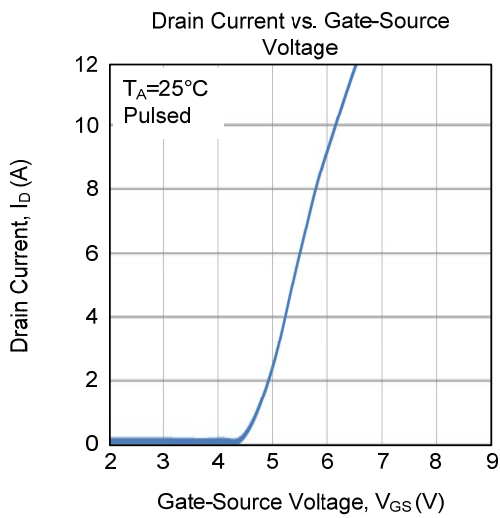
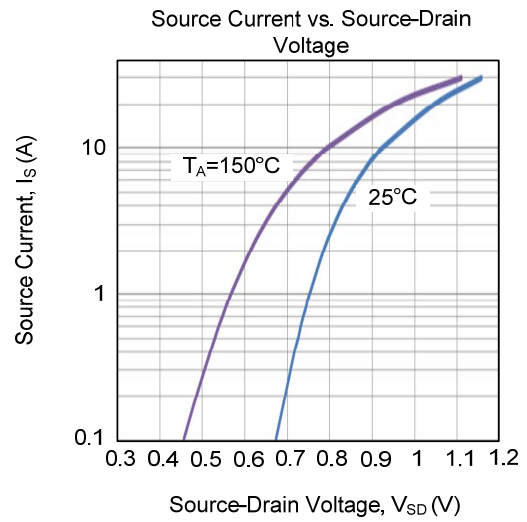
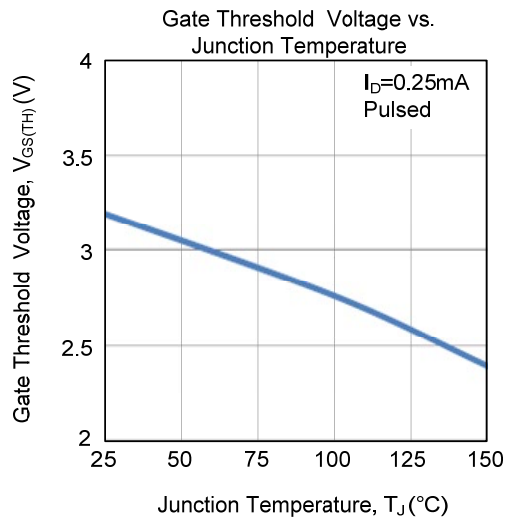


**Unclamped Inductive Switching Waveforms**

### TYPICAL CHARACTERISTICS

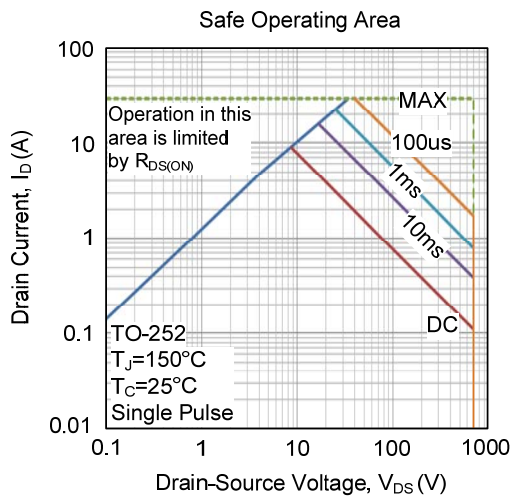


### ■ TYPICAL CHARACTERISTICS (Cont.)





■ TYPICAL CHARACTERISTICS (Cont.)



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