



08NM70

Preliminary

Power MOSFET

**0.8A, 700V N-CHANNEL
SUPER-JUNCTION MOSFET**

■ DESCRIPTION

The UTC **08NM70** is an Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(on)} < 7.2 \Omega @ V_{GS}=10V, I_D=0.4A$
- * High breakdown voltage

■ ORDERING INFORMATION

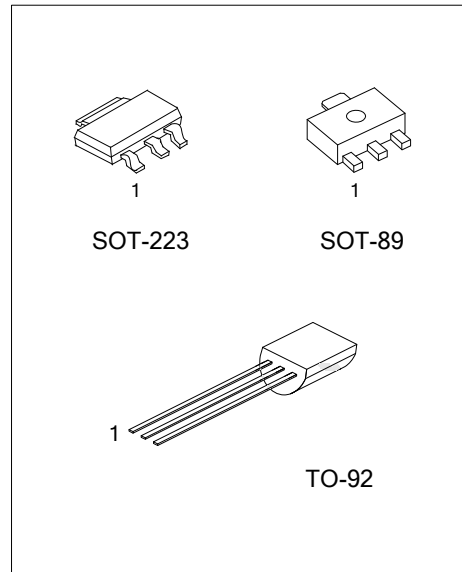
Order Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
-	08NM70G-AA3-R	SOT-223	G	D	S	Tape Reel
-	08NM70G-AB3-R	SOT-89	G	D	S	Tape Reel
08NM70L-T92-B	08NM70G-T92-B	TO-92	G	D	S	Tape Box
08NM70L-T92-K	08NM70G-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>08NM70G-AA3-R</p> <p>(1)Packing Type (2)Package Type (3)Green Package</p>	<p>(1) R: Tape Reel, B: Tape Box, K: Bulk (2) AA3: SOT-223, AB3: SOT-89, T92: TO-92 (3) L: Lead Free, G: Halogen Free and Lead Free</p>
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■ MARKING

SOT-223	SOT-89	TO-92
<p>08NM70G □□□□ → Data Code 1</p>	<p>Data Code ← □□□□ 08NM70G 1</p>	<p>UTC 08NM70□ → L: Lead Free G: Halogen Free □□□ → Data Code 1</p>



■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	0.8	A
	Pulsed	I_{DM}	3.2	A
Avalanche Current (Note 2)		I_{AR}	1.2	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	7.2	mJ
Power Dissipation	SOT-223	P_D	9	W
	SOT-89		3.3	W
	SOT-92		1.4	W
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 1.2\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150	$^\circ\text{C/W}$
	SOT-89/SOT-92		180	$^\circ\text{C/W}$
Junction to Case	SOT-223	θ_{JC}	14	$^\circ\text{C/W}$
	SOT-89		38	$^\circ\text{C/W}$
	SOT-92		88	$^\circ\text{C/W}$

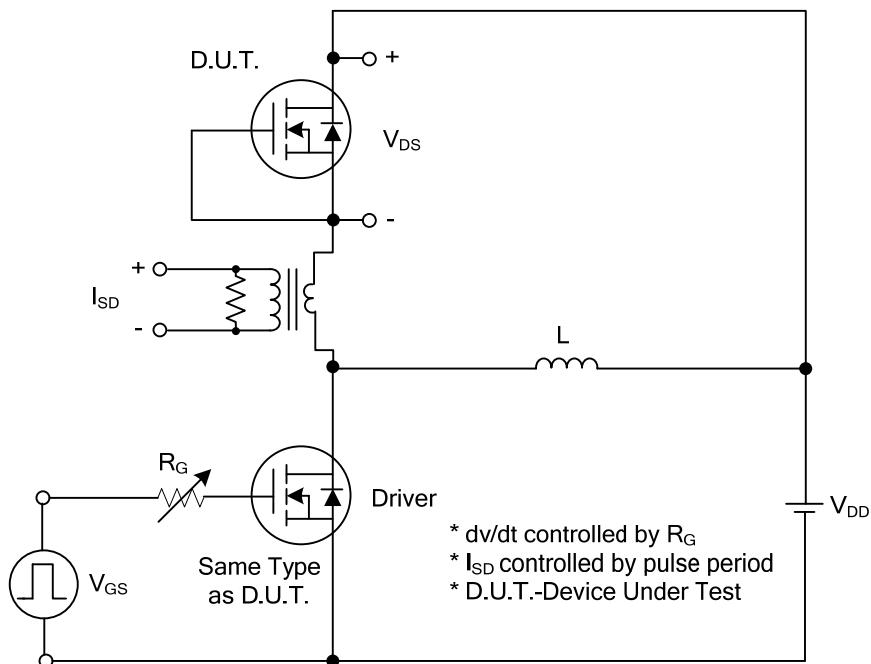
■ ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\ \mu\text{A}$, $V_{GS} = 0\text{V}$	700			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 700\text{V}$, $V_{GS} = 0\text{V}$			10	μA
Gate-Source Leakage Current		I_{GSS}			+100	nA
					$V_{GS} = +30\text{V}$, $V_{DS} = 0\text{V}$	-100
						nA
					$V_{GS} = -30\text{V}$, $V_{DS} = 0\text{V}$	
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{V}$, $I_D = 0.4\text{A}$			7.2	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1.0\text{MHz}$		73		pF
Output Capacitance	C_{OSS}			31		pF
Reverse Transfer Capacitance	C_{RSS}			5		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{DS} = 50\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 1.3\text{A}$, $I_D = 100\ \mu\text{A}$ (Note 1, 2)		12		nC
Gate to Source Charge	Q_{GS}			2		nC
Gate to Drain Charge	Q_{GD}			3		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DS} = 30\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 0.5\text{A}$, $R_G = 25\ \Omega$ (Note 1, 2)		34		ns
Rise Time	t_R			24		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			44		ns
Fall-Time	t_F			31		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				0.8	A
Maximum Body-Diode Pulsed Current	I_{SM}				3.2	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S = 0.8\text{A}$, $V_{GS} = 0\text{V}$			1.4	V

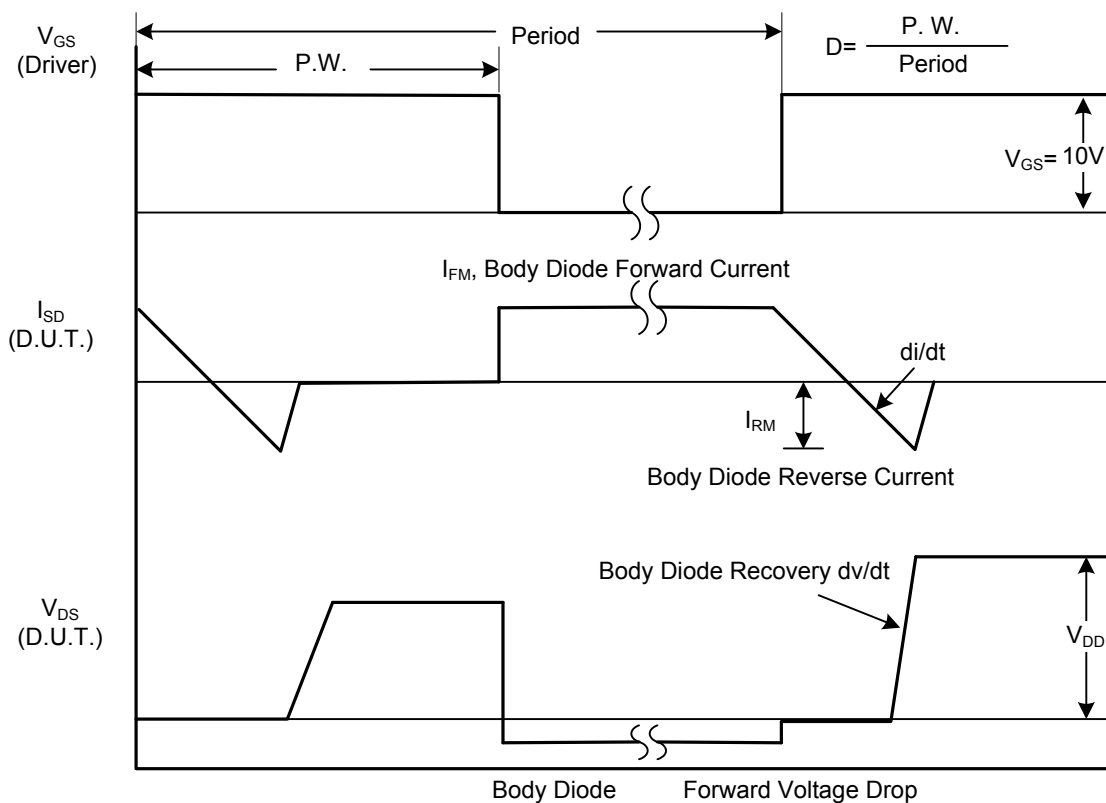
Notes: 1. Pulse Test: Pulse width $\leq 300\ \mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

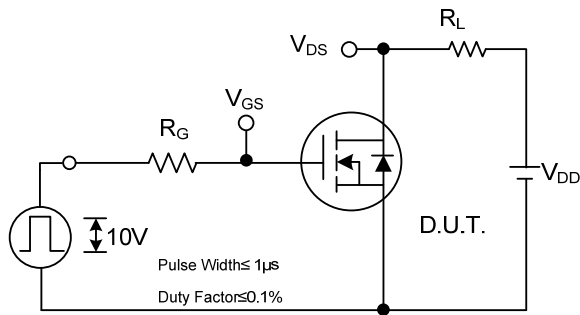


Peak Diode Recovery dv/dt Test Circuit

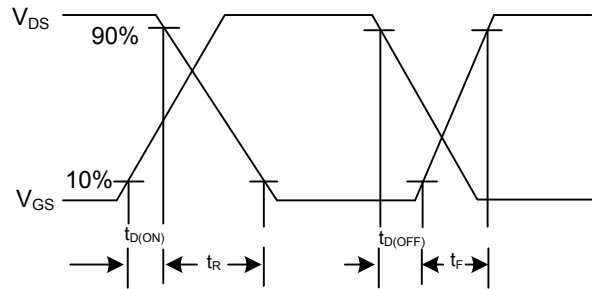


Peak Diode Recovery dv/dt Waveforms

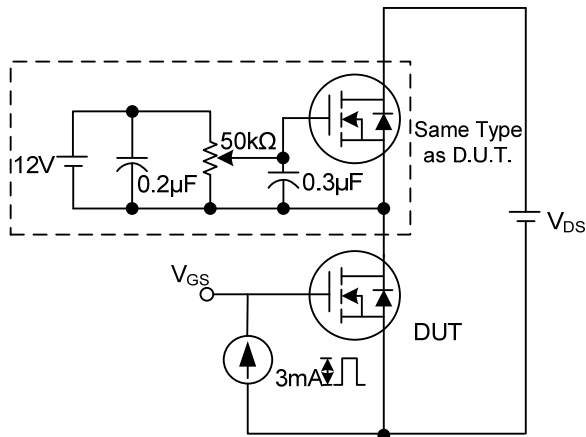
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



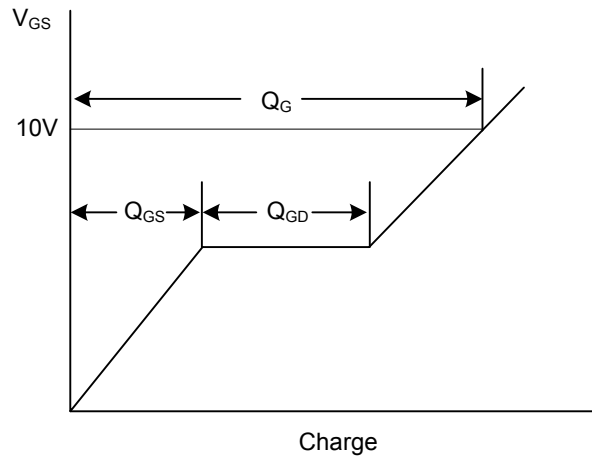
Switching Test Circuit



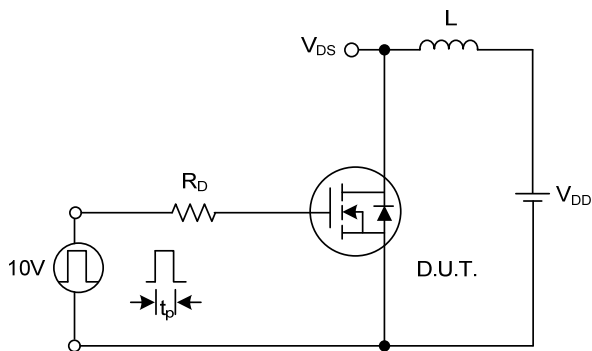
Switching Waveforms



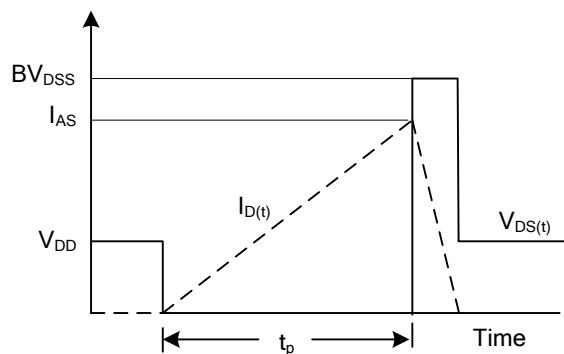
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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