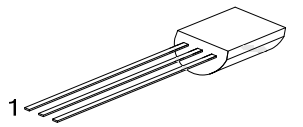
0.3A, 500V N-CHANNEL POWER MOSFET



TO-92

■ DESCRIPTION

The UTC **03N50-KW** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(on)}$ < 15Ω @ V_{GS}=10V, I_D=0.15A
* High breakdown voltage

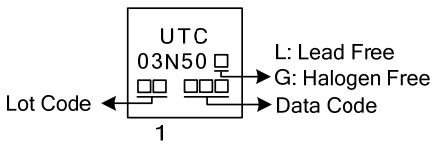
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
03N50L-T92-B	03N50G-T92-B	TO-92	G	D	S	Tape Box
03N50L-T92-K	03N50G-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

03N50L-T92-B (1)Packing Type (2)Package Type (3)Green Package	(1) B: Tape Box, K: Bulk (2) T92: TO-92 (3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	0.3	A
	Pulsed	I_{DM}	1.2	A
Avalanche Current		I_{AR}	0.3	A
Power Dissipation		P_D	425	mW
Junction Temperature		T_J	150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating : Pulse width limited by maximum junction temperature

■ THERMAL DATA

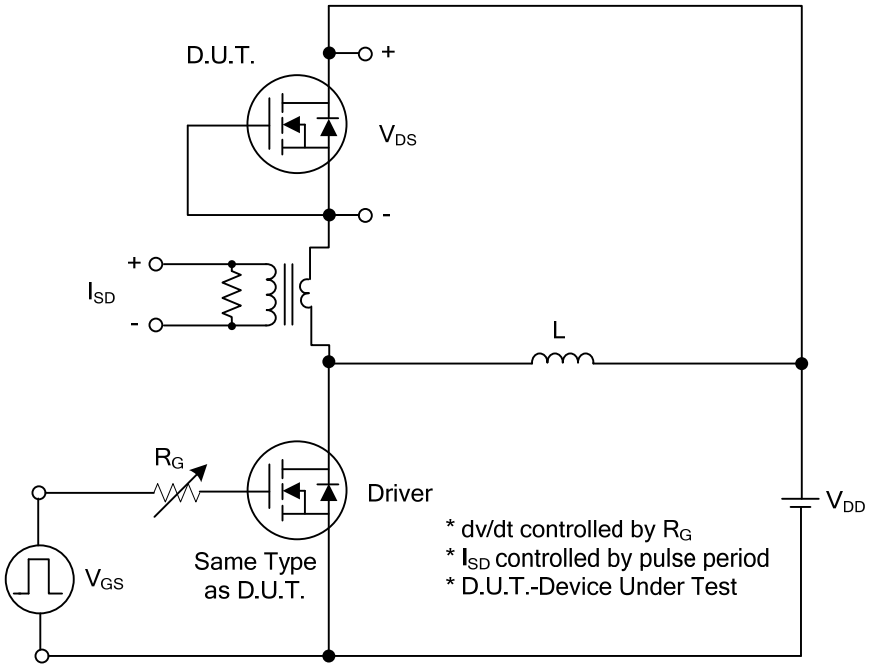
PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	180	°C/W
Junction to Case	θ_{JC}	38	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

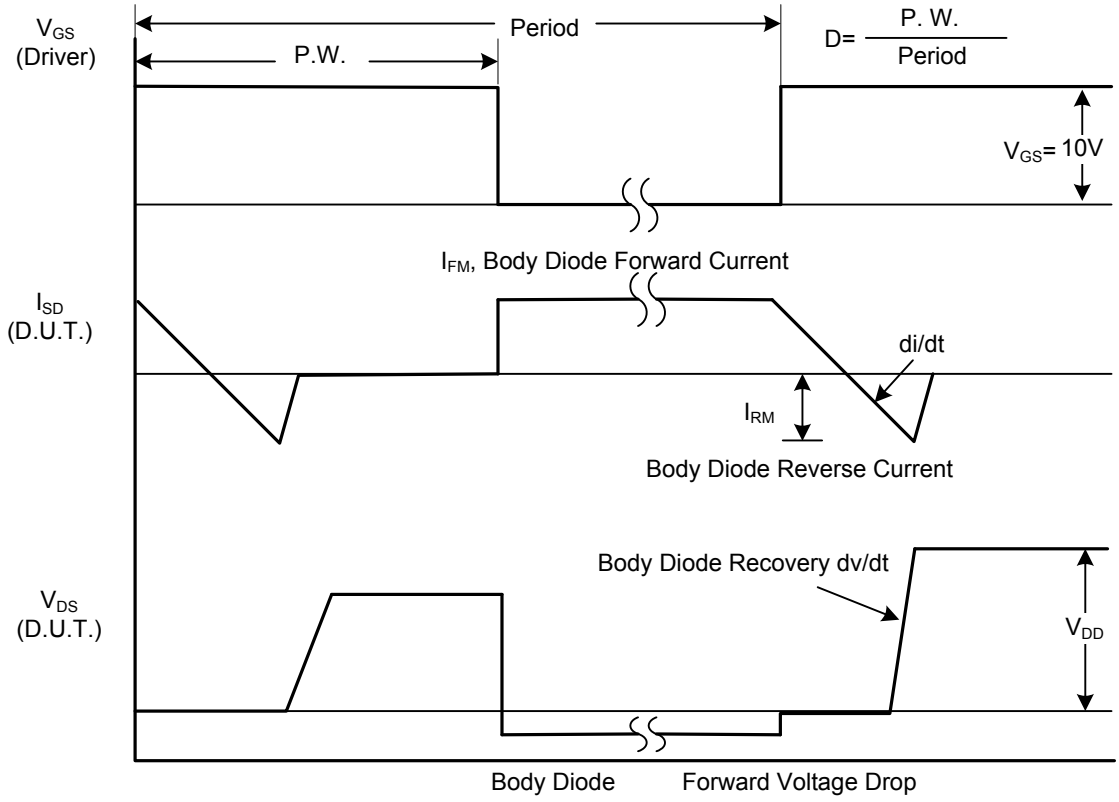
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	500			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=500V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=0.15A			15	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		74		pF
Output Capacitance		C_{OSS}			12.7		pF
Reverse Transfer Capacitance		C_{RSS}			4.6		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{DS}= 50V, V_{GS}= 10V, I_D= 0.3A, I_D=100μA (Note 1, 2)		7.84		nC
Gate to Source Charge		Q_{GS}			0.94		nC
Gate to Drain Charge		Q_{GD}			0.55		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DS}= 30V, V_{GS}= 10V, I_D = 0.3A, R_G = 25Ω (Note 1, 2)		50		ns
Rise Time		t_R			17.8		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			44		ns
Fall-Time		t_F			28.2		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				0.3	A
Maximum Body-Diode Pulsed Current		I_{SM}				1.2	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=0.3A, V_{GS}=0V			1.4	V

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

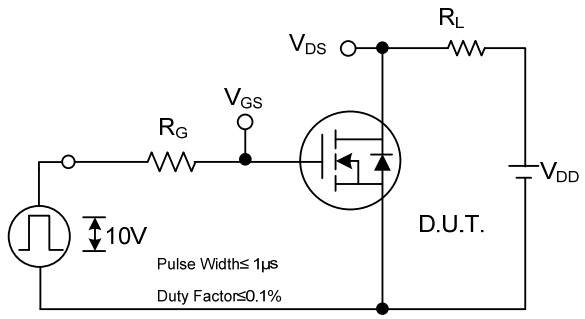


Peak Diode Recovery dv/dt Test Circuit

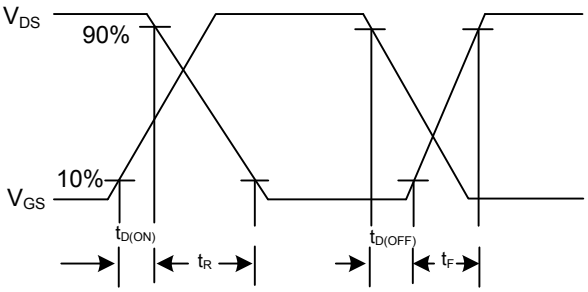


Peak Diode Recovery dv/dt Waveforms

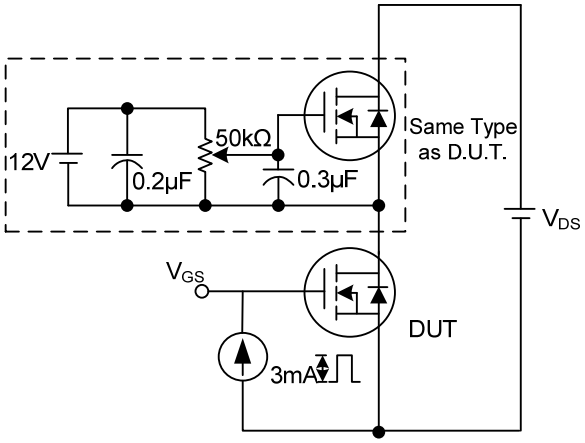
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



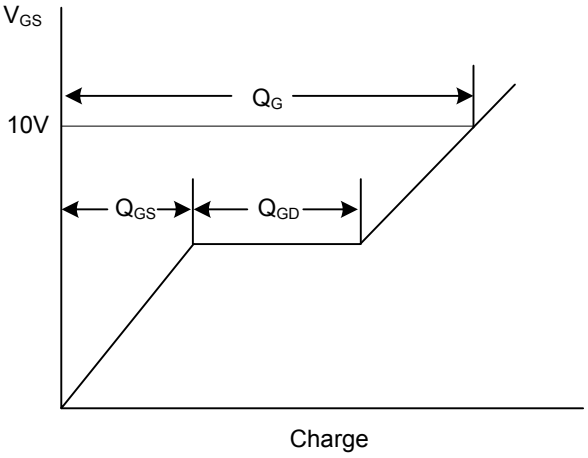
Switching Test Circuit



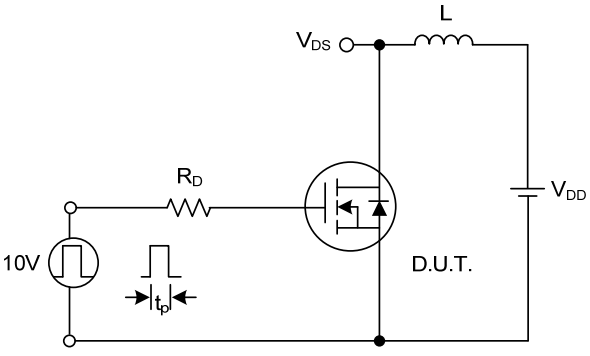
Switching Waveforms



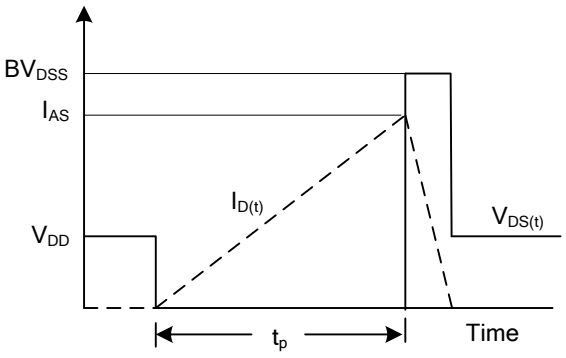
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms