



# 1N65Q-TA

**Power MOSFET**

## 1.0A, 650V N-CHANNEL POWER MOSFET

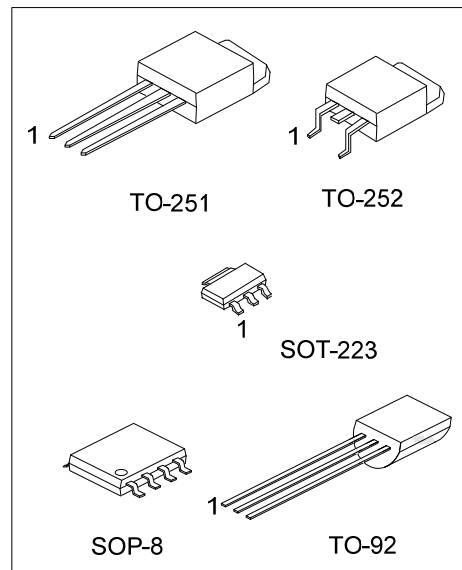
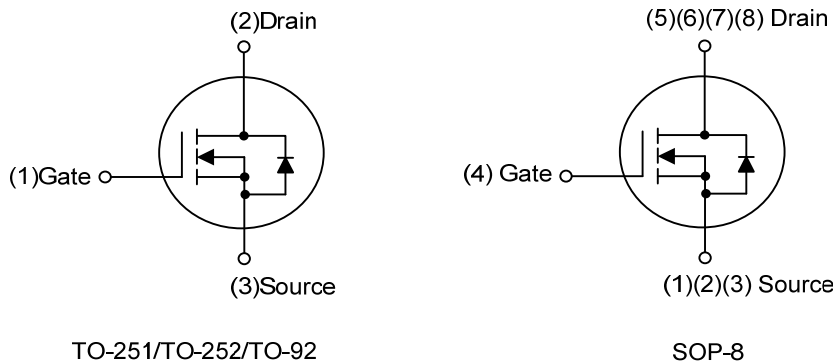
■ DESCRIPTION

The UTC **1N65Q-TA** is a high voltage MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

- \*  $R_{DS(ON)} \leq 9.5 \Omega @ V_{GS}=10V, I_D=0.5A$
- \* Fast switching capability
- \* Avalanche energy specified
- \* Improved dv/dt capability, high ruggedness

■ SYMBOL



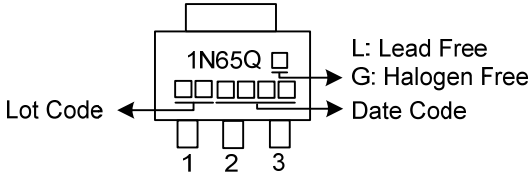
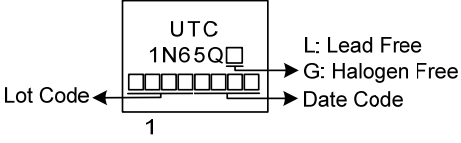
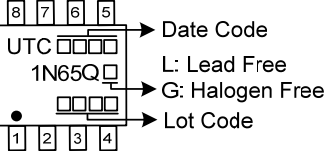
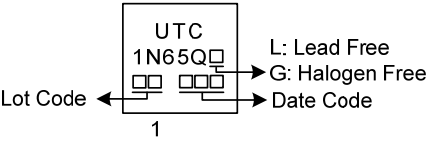
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment						Packing		
Lead Free	Halogen Free		1	2	3	4	5	6		7	8
1N65QL-AA3-R	1N65QG-AA3-R	SOT-223	G	D	S	-	-	-	-	-	Tape Reel
1N65QL-TM3-T	1N65QG-TM3-T	TO-251	G	D	S	-	-	-	-	-	Tube
1N65QL-TN3-R	1N65QG-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
1N65QL-S08-R	1N65QG-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
1N65QL-T92-B	1N65QG-T92-B	TO-92	G	D	S	-	-	-	-	-	Tape Box
1N65QL-T92-K	1N65QG-T92-K	TO-92	G	D	S	-	-	-	-	-	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>1N65QG-AA3-R</p> <ul style="list-style-type: none"> <li>(1) Packing Type</li> <li>(2) Package Type</li> <li>(3) Green Package</li> </ul>	<ul style="list-style-type: none"> <li>(1) T: Tube, R: Tape Reel, B: Tape Box, K: Bulk</li> <li>(2) AA3: SOT-223, TM3: TO-251, TN3: TO-252 S08: SOP-8, T92: TO-92</li> <li>(3) G: Halogen Free and Lead Free, L: Lead Free</li> </ul>
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■ MARKING

SOT-223	TO-251 / TO-252
 <p>1N65Q □</p> <p>Lot Code ← □ □ □ □ □ → Date Code</p> <p>1 2 3</p> <p>L: Lead Free G: Halogen Free</p>	 <p>UTC 1N65Q □</p> <p>Lot Code ← □ □ □ □ □ → Date Code</p> <p>1</p> <p>L: Lead Free G: Halogen Free</p>
SOP-8	TO-92
 <p>8 7 6 5 → Date Code</p> <p>UTC □ □ □ □</p> <p>1N65Q □</p> <p>L: Lead Free G: Halogen Free</p> <p>□ □ □ □ → Lot Code</p> <p>1 2 3 4</p>	 <p>UTC 1N65Q □</p> <p>Lot Code ← □ □ □ □ → Date Code</p> <p>1</p> <p>L: Lead Free G: Halogen Free</p>

■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	Continuous	$I_D$	1	A
Pulsed Drain Current (Note 2)	Pulsed (Note 2)	$I_{DM}$	2	A
Avalanche Current (Note 2)		$I_{AR}$	1	A
Avalanche Energy (Note 3)	Single Pulsed	$E_{AS}$	25	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.1	V/ns
Power Dissipation	SOT-223	$P_D$	2.2	W
	TO-251/TO-252		26	W
	SOP-8		2.1	W
	TO-92		1.42	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=30\text{mH}$ ,  $I_{AS}=1.3\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD}\leq 1.0\text{A}$ ,  $di/dt\leq 200\text{A}/\mu\text{s}$ ,  $V_{DD}\leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA (Note)

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	$\theta_{JA}$	150	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		110	$^\circ\text{C}/\text{W}$
	SOP-8		90	$^\circ\text{C}/\text{W}$
	TO-92		160	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-223	$\theta_{JC}$	56.8	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		4.8	$^\circ\text{C}/\text{W}$
	SOP-8		59.5	$^\circ\text{C}/\text{W}$
	TO-92		88	$^\circ\text{C}/\text{W}$

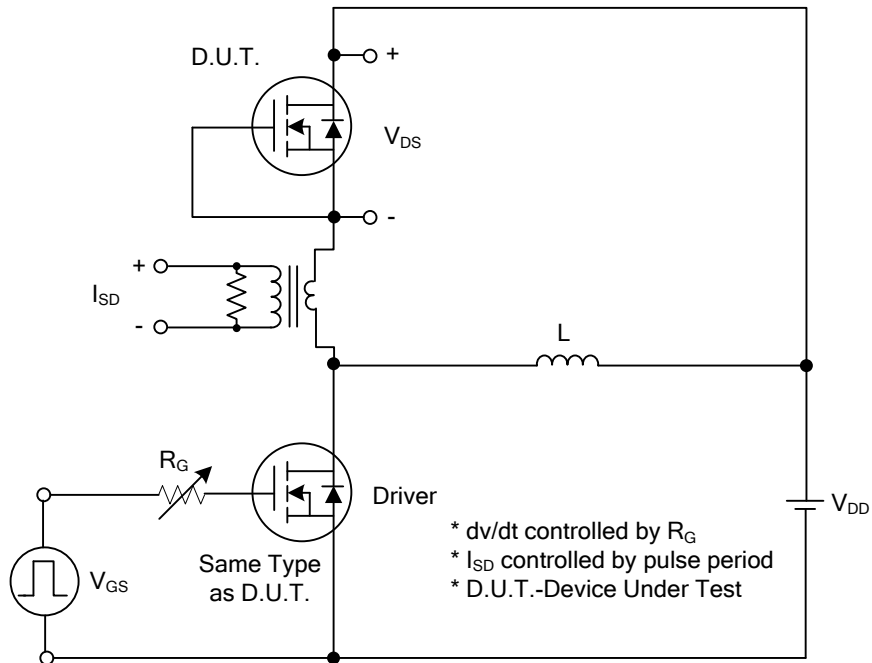
Note: The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C, unless otherwise specified)

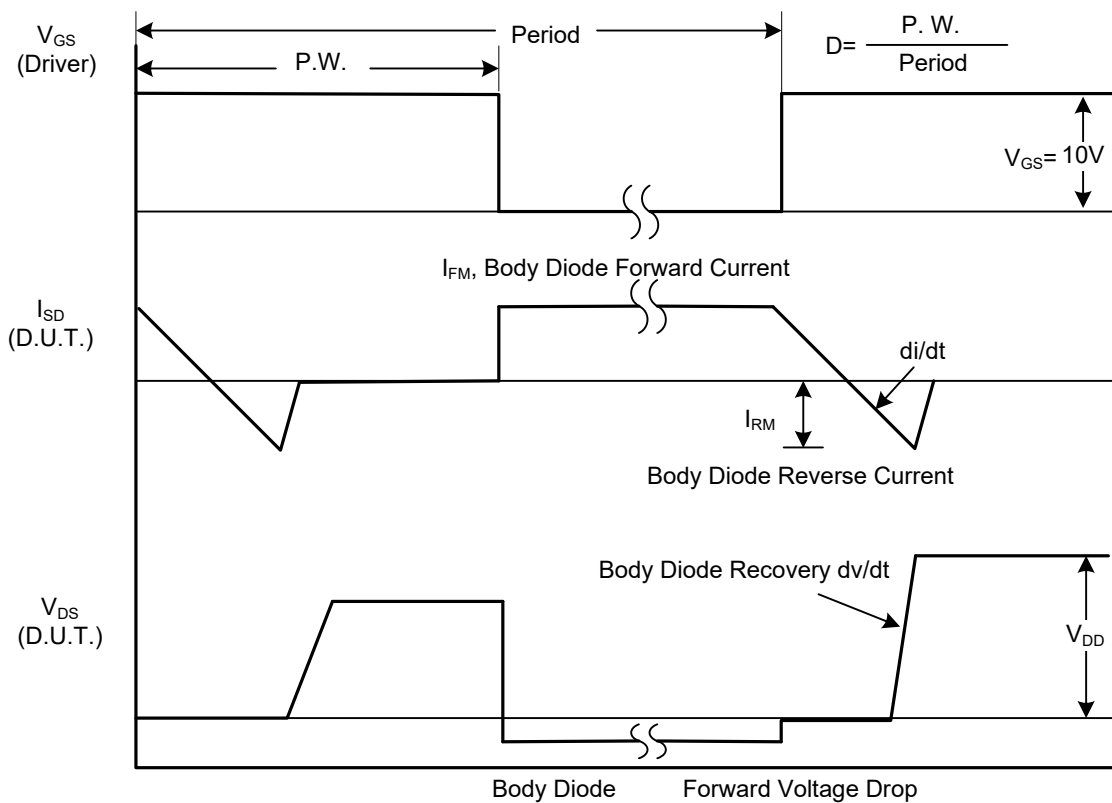
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	650			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V			10	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>			100	nA
	Reverse					
		V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A			9.5	Ω
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		160		pF
Output Capacitance	C <sub>OSS</sub>			21		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			2		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> = 520V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.0A (Note 1, 2)		10		nC
Gate-Source Charge	Q <sub>GS</sub>			4		nC
Gate-Drain Charge	Q <sub>GD</sub>			1.2		nC
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = 100V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.0A, R <sub>G</sub> = 25Ω (Note 1, 2)		5		ns
Turn-On Rise Time	t <sub>R</sub>			16		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			16.5		ns
Turn-Off Fall Time	t <sub>F</sub>			40		ns
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>				1	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				2	A
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>SD</sub> = 1.0A			1.4	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.0A, V <sub>DD</sub> = 100V		148		ns
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs		437		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%  
 2. Essentially independent of operating temperature

## TEST CIRCUITS AND WAVEFORMS

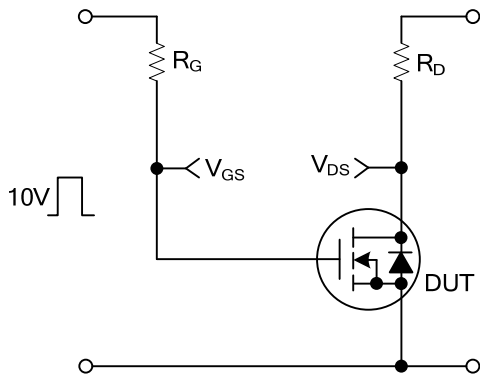


**Peak Diode Recovery  $dv/dt$  Test Circuit**

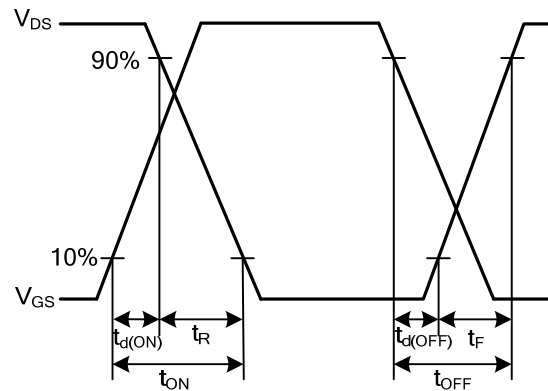


**Peak Diode Recovery  $dv/dt$  Waveforms**

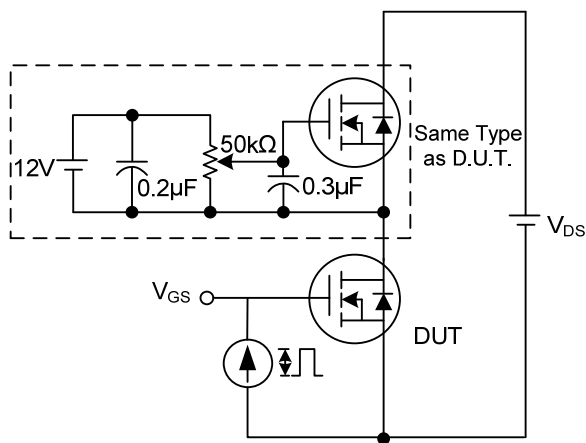
### TEST CIRCUITS AND WAVEFORMS



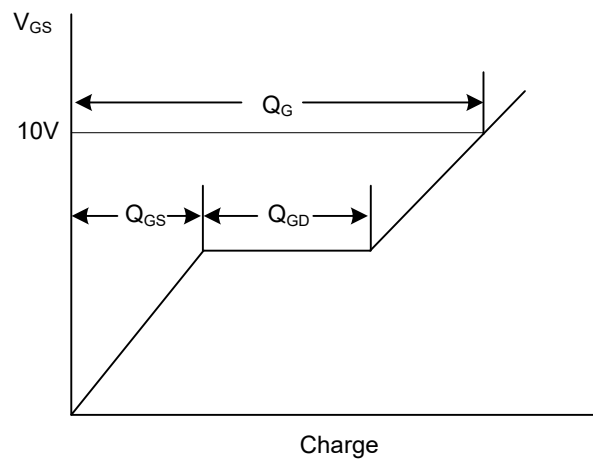
Switching Test Circuit



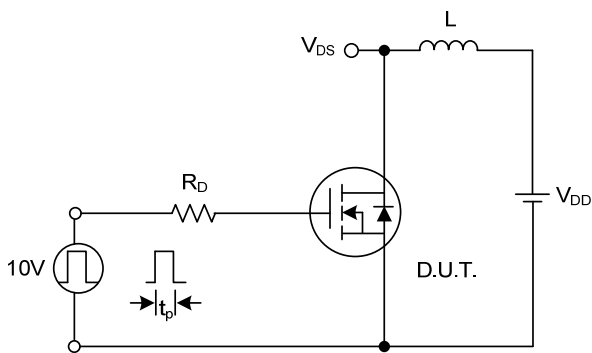
Switching Waveforms



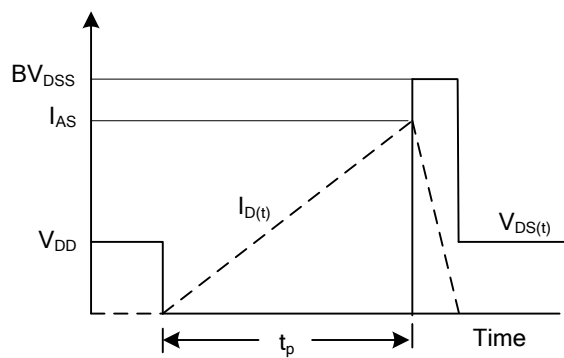
Gate Charge Test Circuit



Gate Charge Waveform

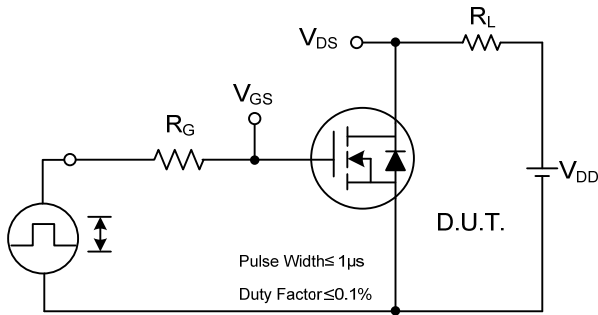


Unclamped Inductive Switching Test Circuit

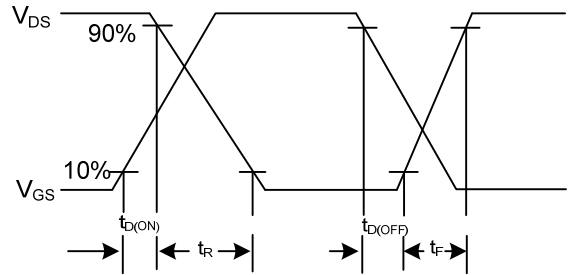


Unclamped Inductive Switching Waveforms

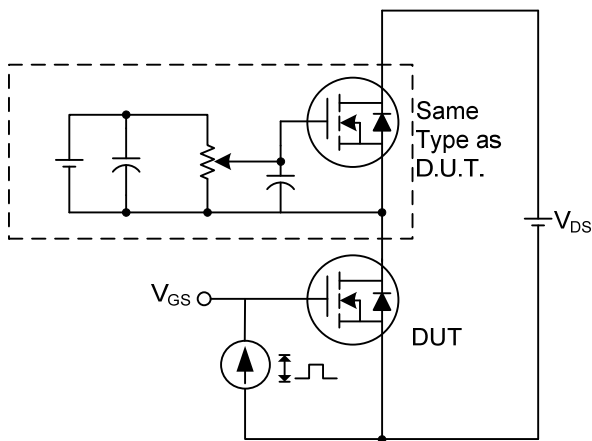
### TEST CIRCUITS AND WAVEFORMS



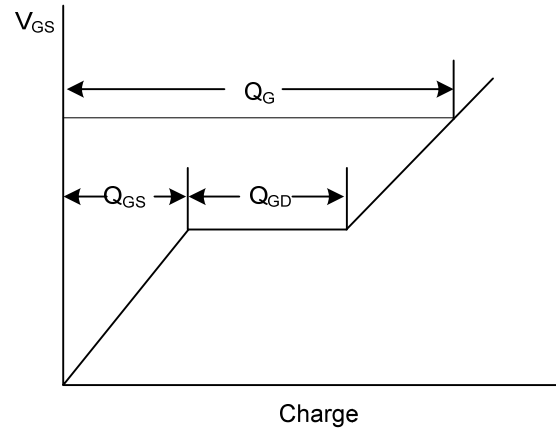
**Switching Test Circuit**



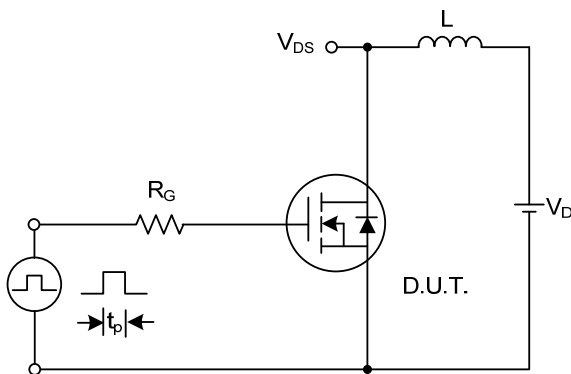
**Switching Waveforms**



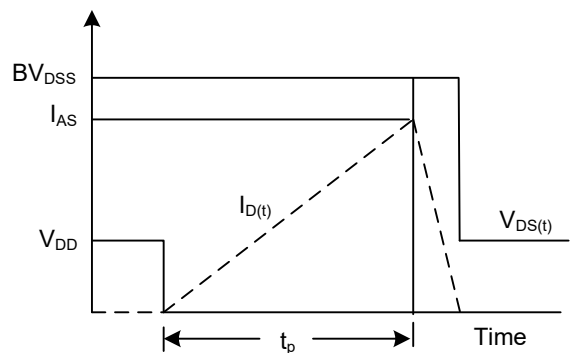
**Gate Charge Test Circuit**



**Gate Charge Waveform**

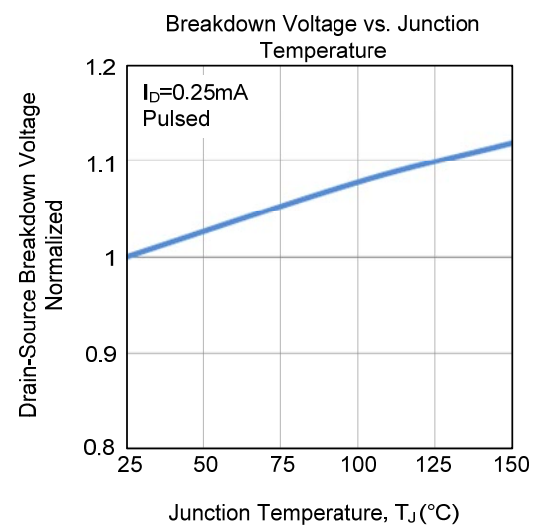
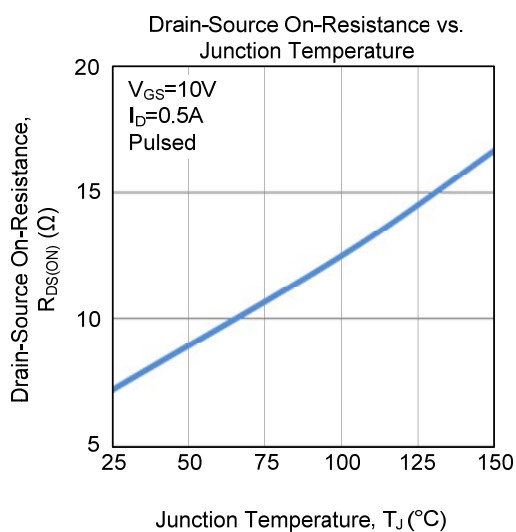
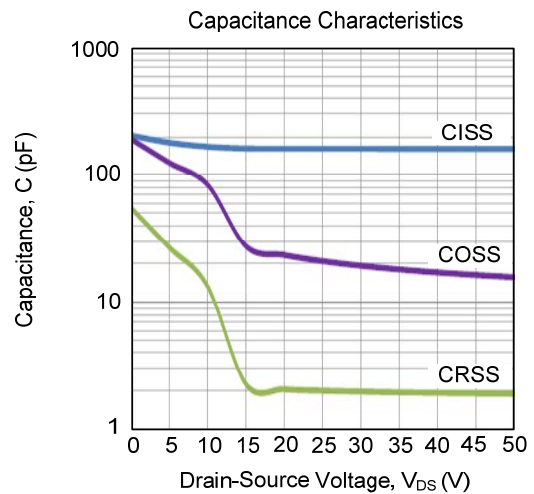
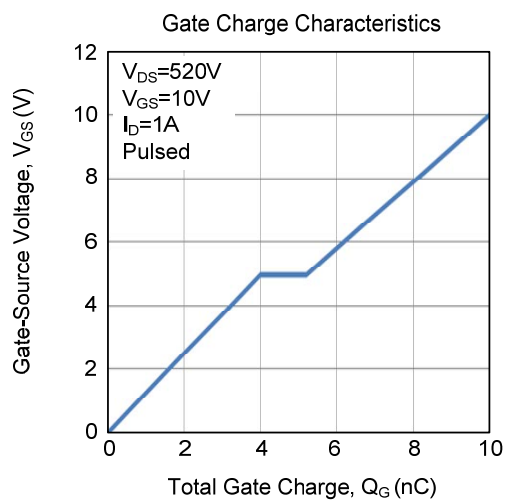
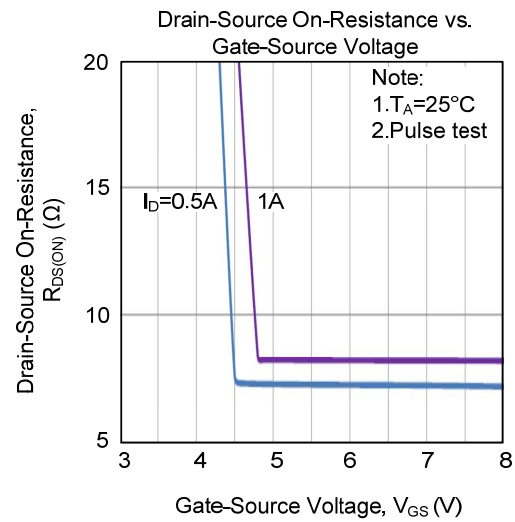
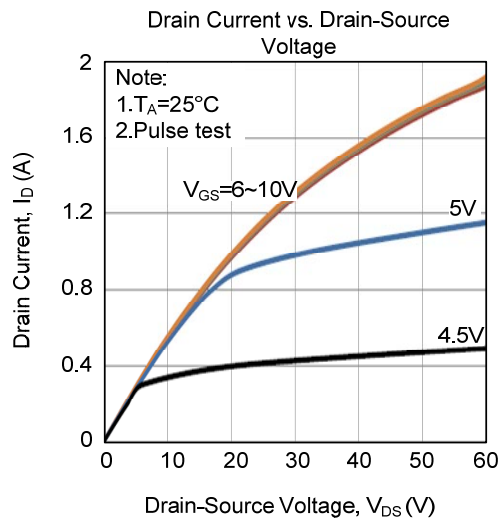


**Unclamped Inductive Switching Test Circuit**



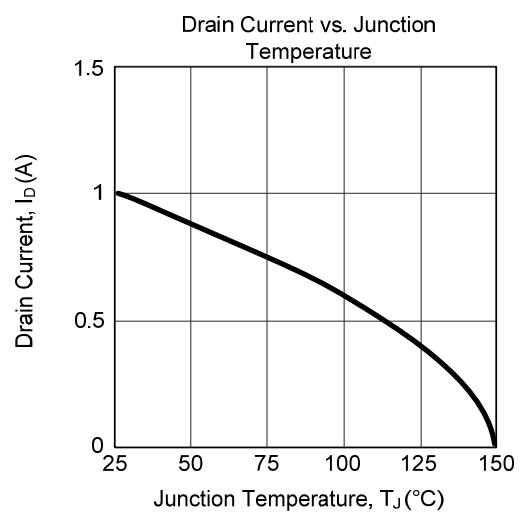
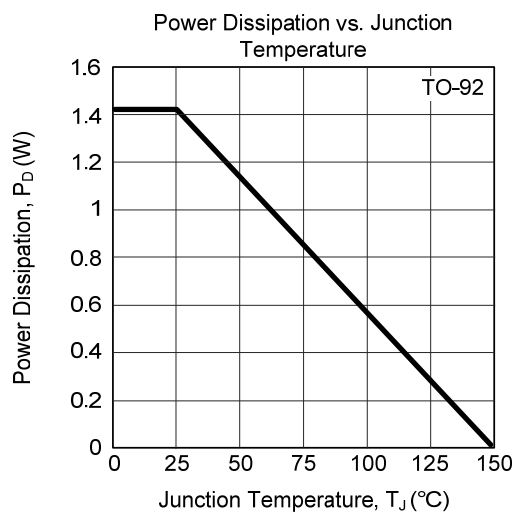
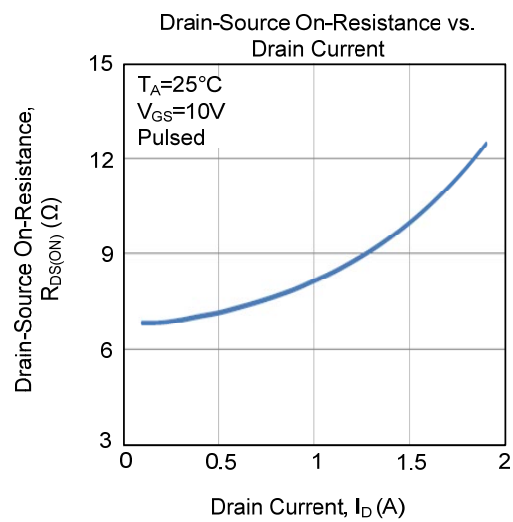
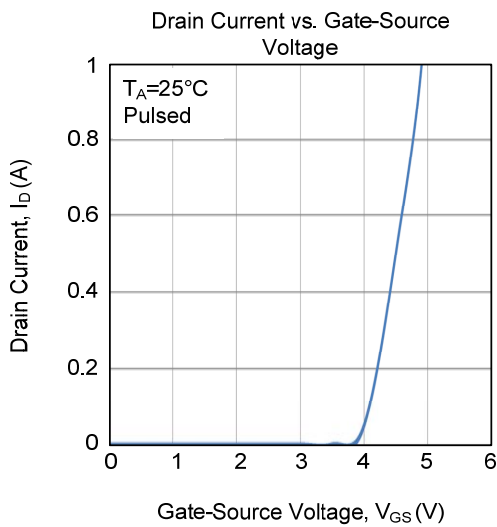
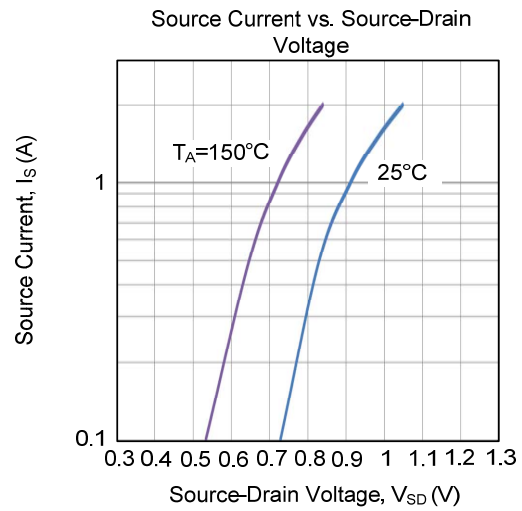
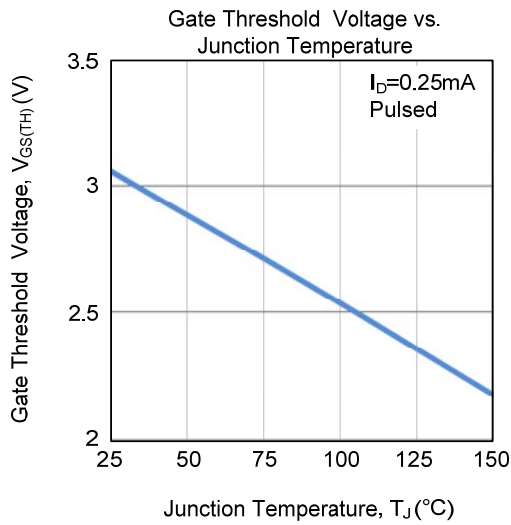
**Unclamped Inductive Switching Waveforms**

### TYPICAL CHARACTERISTICS

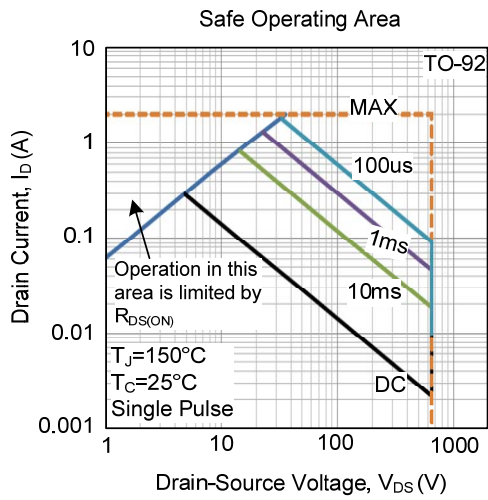




### TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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