

13NM90

Power MOSFET

13A, 900V N-CHANNEL SUPER-JUNCTION MOSFET

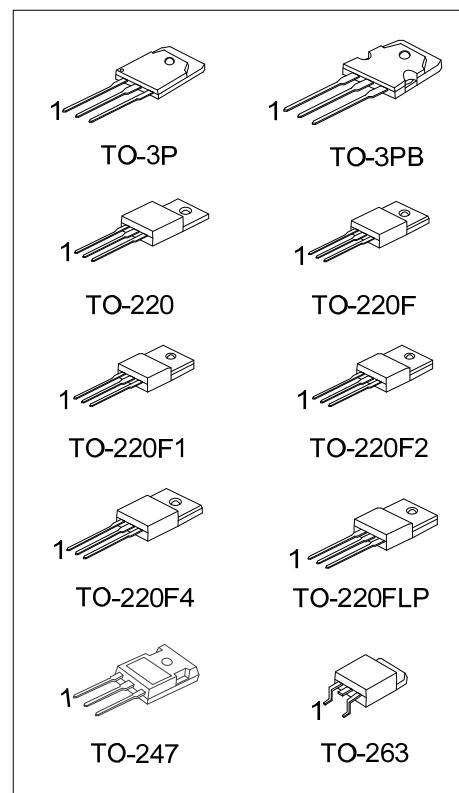
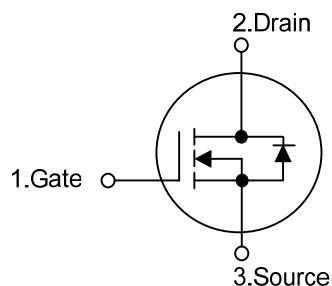
■ DESCRIPTION

The **UTC 13NM90** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)} \leq 0.5 \Omega$ @ $V_{GS}=10V$, $I_D=6.5A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

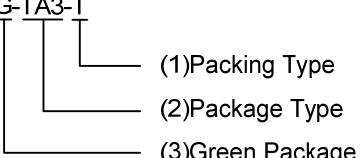
■ SYMBOL



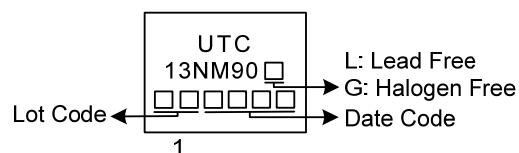
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
13NM90L-TA3-T	13NM90G-TA3-T	TO-220	G	D	S	Tube
13NM90L-TF1-T	13NM90G-TF1-T	TO-220F1	G	D	S	Tube
13NM90L-TF2-T	13NM90G-TF2-T	TO-220F2	G	D	S	Tube
13NM90L-TF3-T	13NM90G-TF3-T	TO-220F	G	D	S	Tube
13NM90L-TF34-T	13NM90G-TF34-T	TO-220F4	G	D	S	Tube
13NM90G-TFLP-T	13NM90G-TFLP-T	TO-220FLP	G	D	S	Tube
13NM90L-TQ2-T	13NM90G-TQ2-T	TO-263	G	D	S	Tube
13NM90L-TQ2-R	13NM90G-TQ2-R	TO-263	G	D	S	Tape Reel
13NM90L-T3B-T	13NM90G-T3B-T	TO-3PB	G	D	S	Tube
13NM90L-T3P-T	13NM90G-T3P-T	TO-3P	G	D	S	Tube
13NM90L-T47-T	13NM90G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

 13NM90G-TA3-T	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2 TF3: TO-220F, TF34: TO-220F4 TFLP: TO-220FLP, TQ2: TO-263, T47: TO-247, T3B: TO-3PB, T3P: TO-3P (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	900	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	Continuous	I_D	13	A
Pulsed Drain Current	Pulsed (Note 2)	I_{DM}	52	A
Avalanche Current (Note 2)		I_{AR}	3.0	A
Single Pulsed Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	180	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.68	V/ns
Power Dissipation	TO-220/TO-263	P_D	101	W
	TO-220F1/TO-220F2		32	W
	TO-220F4/TO-220FLP		405	W
	TO-3P/TO-3PB		370	W
	TO-247		+150	°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 4.2\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$.

4. $I_{SD} \leq 13\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F1	θ_{JA}	62.5	°C/W
	TO-220F2/TO-220F4			
	TO-220FLP/TO-263			
	TO-3P/TO-3PB			
	TO-247			
Junction to Case	TO-220/TO-220F1	θ_{JC}	1.24	°C/W
	TO-263			
	TO-220F1/TO-220F2			
	TO-220F4/TO-220FLP			
	TO-3P/TO-3PB			
	TO-247		0.31	°C/W
			0.34	°C/W

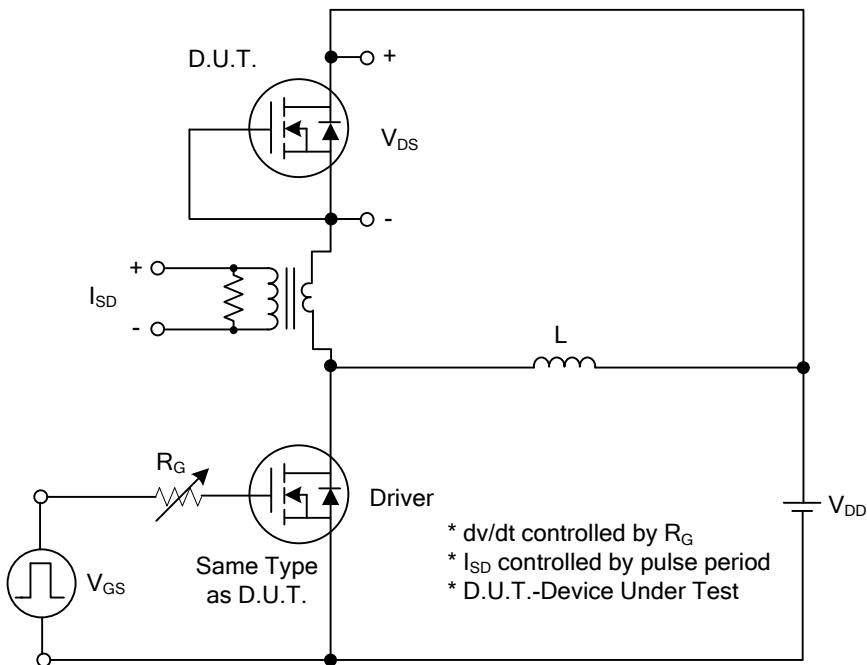
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	900			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=900\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6.5\text{A}$			0.5	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		1650		pF
Output Capacitance	C_{OSS}			390		pF
Reverse Transfer Capacitance	C_{RSS}			0.3		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=13\text{A}, I_G=1\text{mA}$ (Note 1,2)		60		nC
Gate to Source Charge	Q_{GS}			11		nC
Gate to Drain Charge	Q_{GD}			22		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D(ON)}}$	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=13\text{A}, R_G=25\Omega$ (Note 1,2)		25		nS
Rise Time	t_R			24		nS
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			190		nS
Fall-Time	t_F			37		nS
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				13	A
Maximum Body-Diode Pulsed Current	I_{SM}				52	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=13\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=13\text{A}, V_{\text{GS}}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		640		nS
Body Diode Reverse Recovery Charge	Q_{rr}			12.7		μC

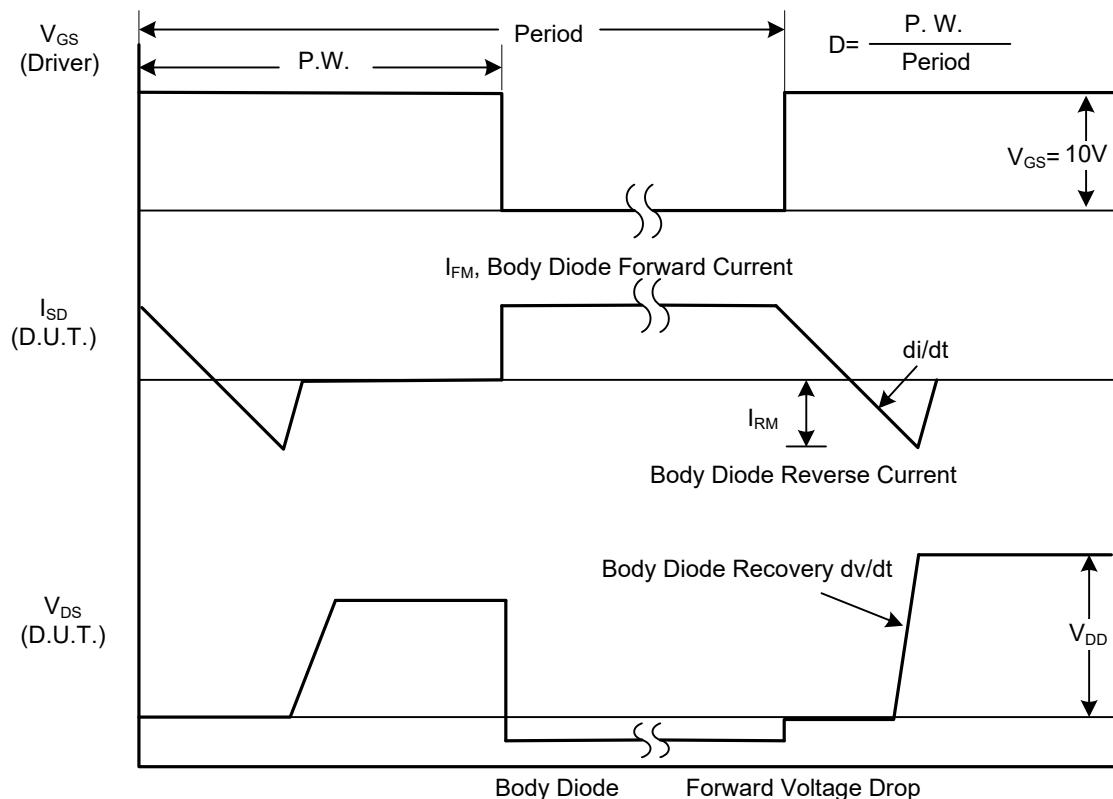
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

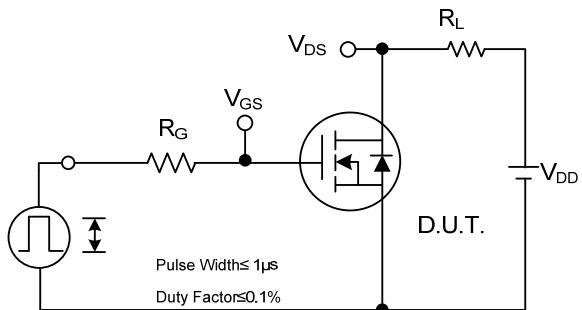


Peak Diode Recovery dv/dt Test Circuit

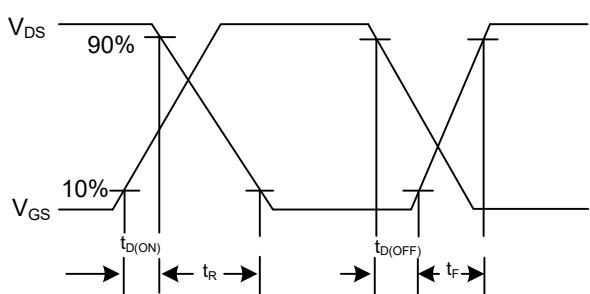


Peak Diode Recovery dv/dt Waveforms

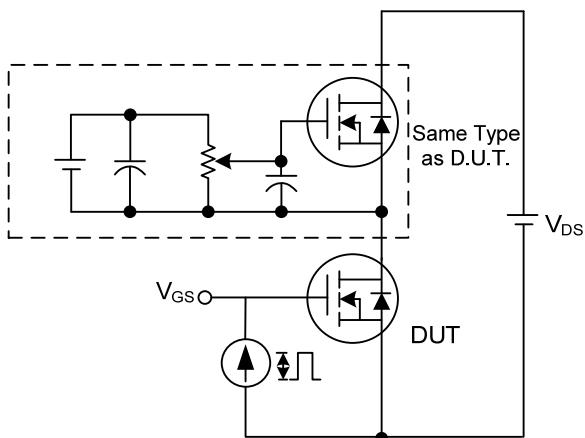
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



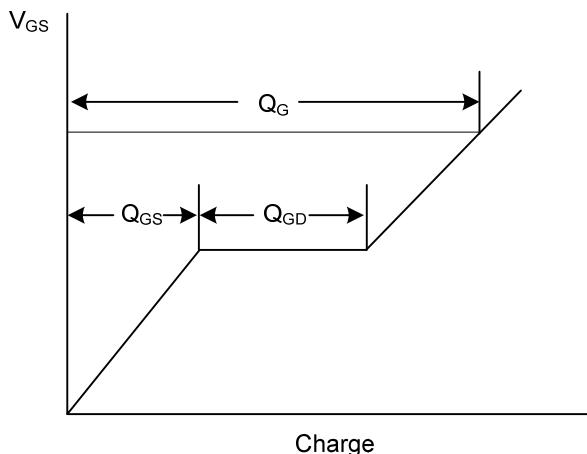
Switching Test Circuit



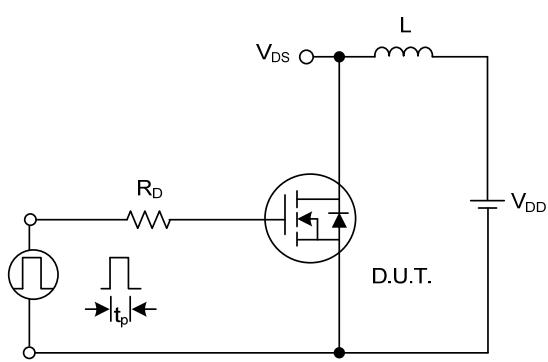
Switching Waveforms



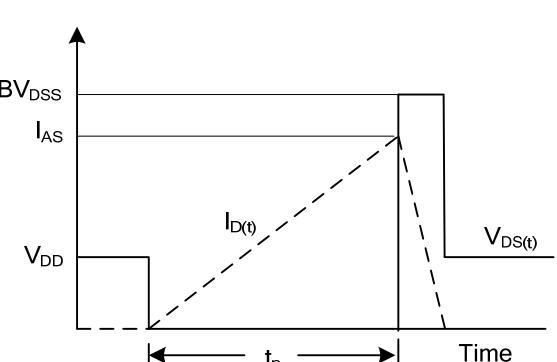
Gate Charge Test Circuit



Gate Charge Waveform

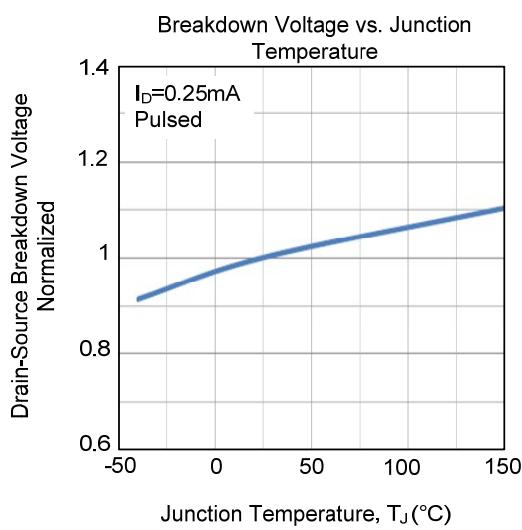
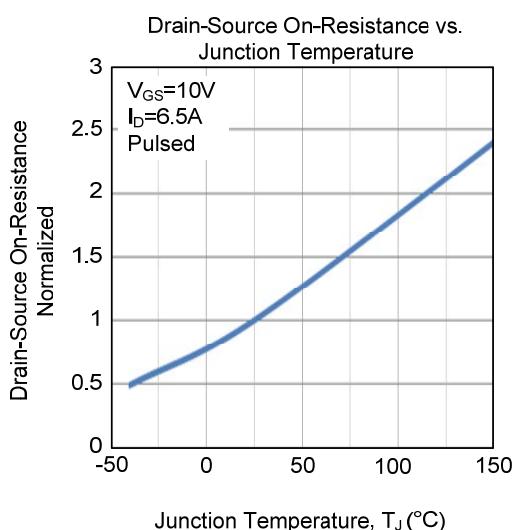
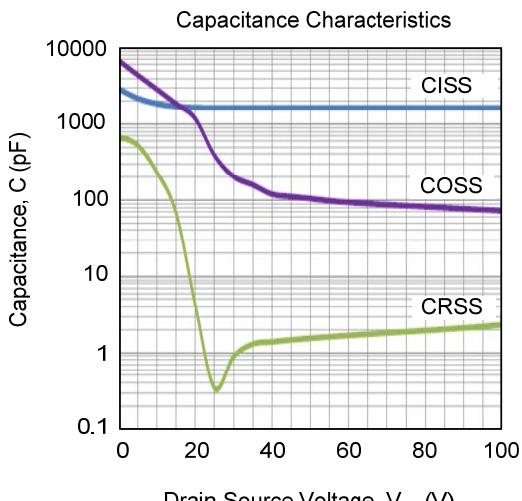
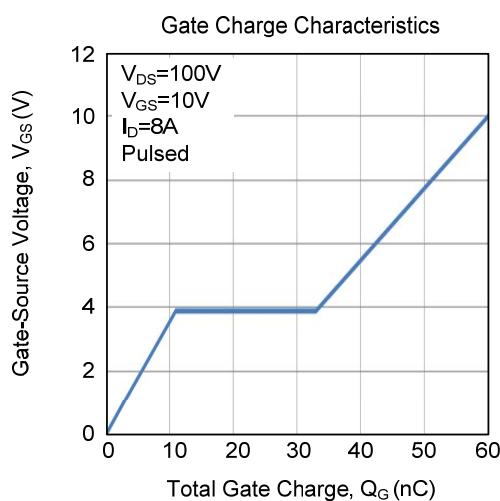
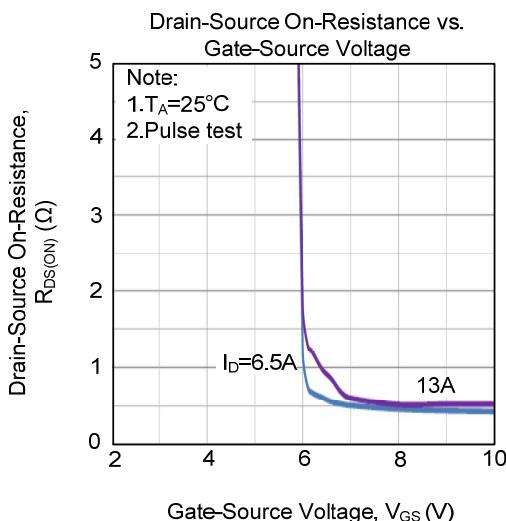
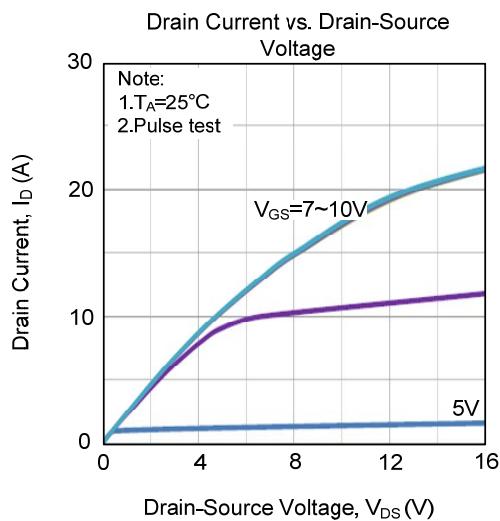


Unclamped Inductive Switching Test Circuit

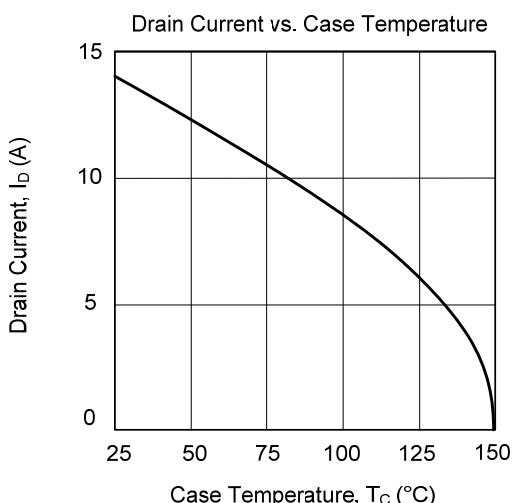
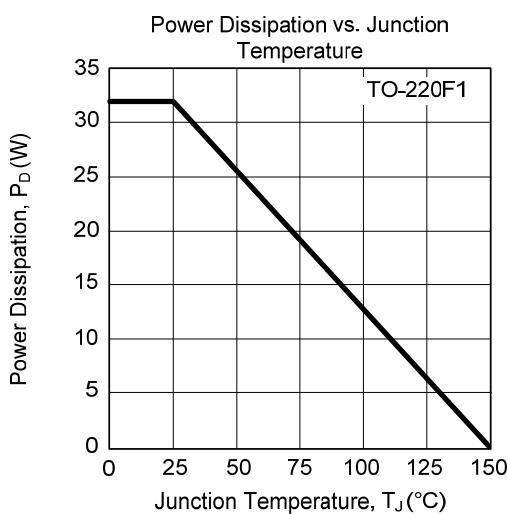
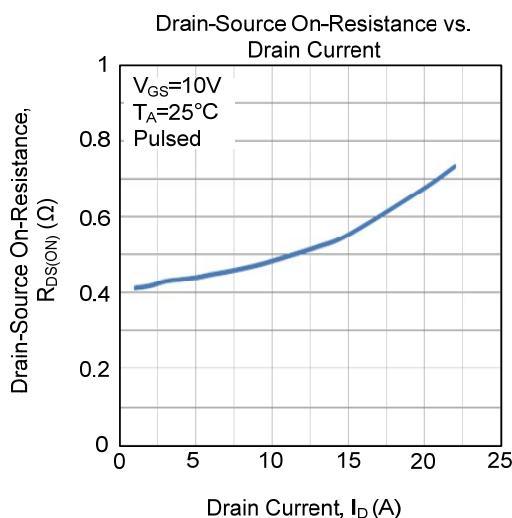
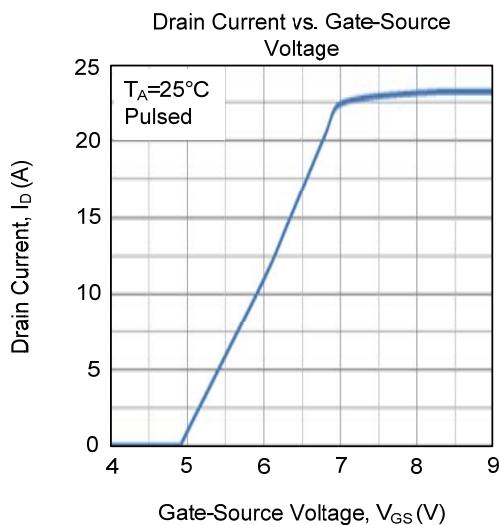
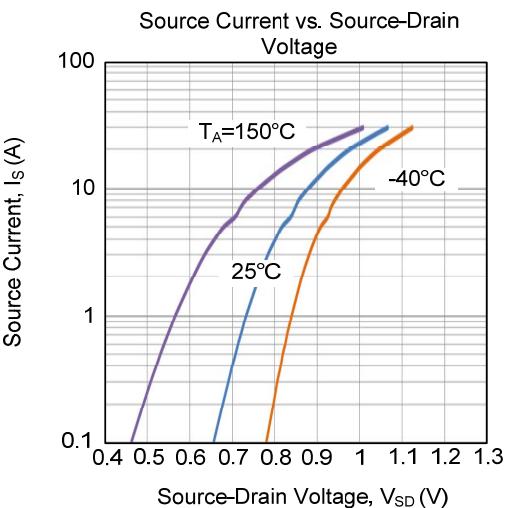
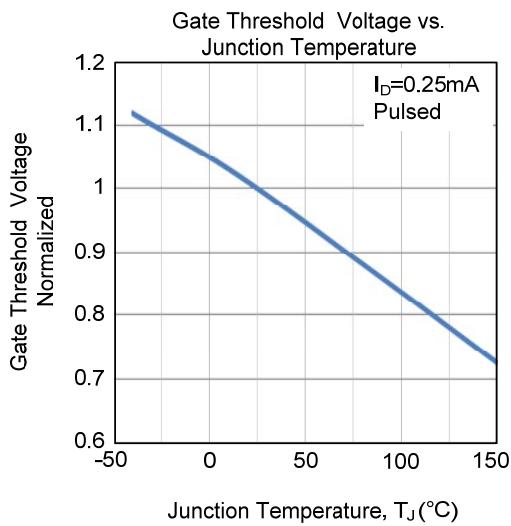


Unclamped Inductive Switching Waveforms

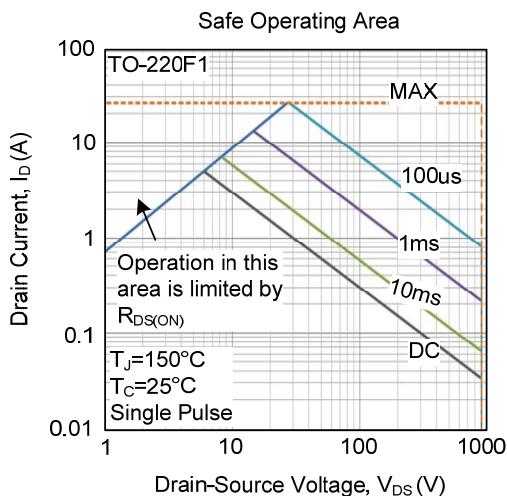
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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