



3NM90

Power MOSFET

3.0A, 900V N-CHANNEL SUPER-JUNCTION MOSFET

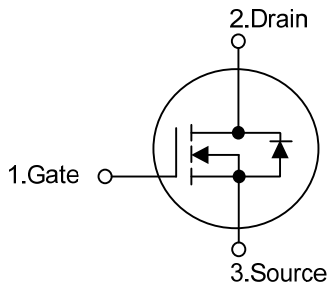
DESCRIPTION

The **UTC 3NM90** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \leq 4.9 \Omega @ V_{GS}=10V, I_D=1.5A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL

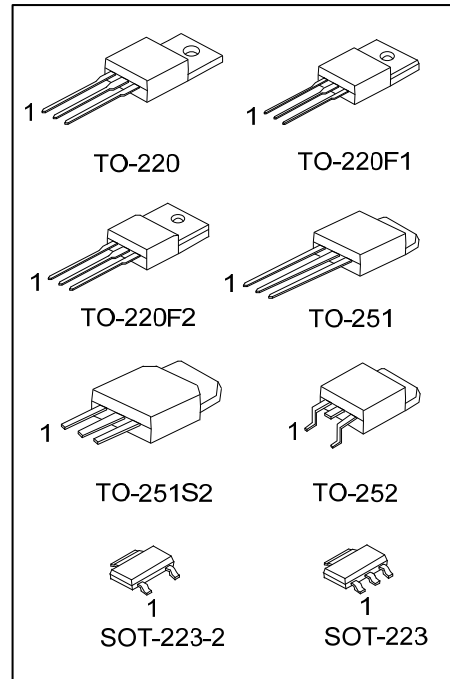


ORDERING INFORMATION

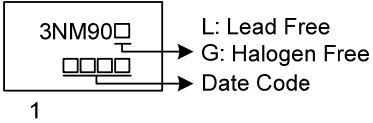
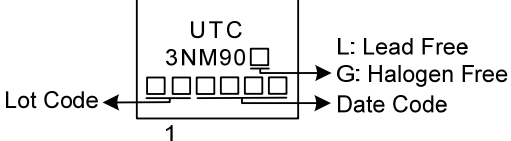
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	5	
3NM90L-AA2-R	3NM90G-AA2-R	SOT-223-2	G	D	S	Tape Reel
3NM90L-AA3-R	3NM90G-AA3-R	SOT-223	G	D	S	Tape Reel
3NM90L-TA3-T	3NM90G-TA3-T	TO-220	G	D	S	Tube
3NM90L-TF1-T	3NM90G-TF1-T	TO-220F1	G	D	S	Tube
3NM90L-TF2-T	3NM90G-TF2-T	TO-220F2	G	D	S	Tube
3NM90L-TM3-T	3NM90G-TM3-T	TO-251	G	D	S	Tube
3NM90L-TMS2-T	3NM90G-TMS2-T	TO-251S2	G	D	S	Tube
3NM90L-TN3-R	3NM90G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>3NM90G-AA3-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel, T: Tube</p> <p>(2) AA2: SOT-223-2, AA3: SOT-223, TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TM3: TO-251, TMS2: TO-251S2, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING

SOT-223-2 / SOT-223	TO-220 / TO-220F1 / TO-220F2 TO-251 / TO-251S2 / TO-252
 <p>3NM90</p> <p>L: Lead Free G: Halogen Free Date Code</p> <p>1</p>	 <p>UTC</p> <p>3NM90</p> <p>L: Lead Free G: Halogen Free Date Code</p> <p>Lot Code</p> <p>1</p>

■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	900	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	Continuous	I_D	3.0	A
Pulsed Drain Current	Pulsed (Note 2)	I_{DM}	12	A
Avalanche Current (Note 2)		I_{AR}	1.1	A
Single Pulsed Avalanche Energy	Single Pulsed (Note 5)	E_{AS}	96	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.17	V/ns
Power Dissipation	SOT-223-2/SOT-223	P_D	2.5	W
	TO-220		37	W
	TO-220F1/TO-220F2		21	W
	TO-251/TO-251S2		22	W
	TO-252			
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

5. $L = 159\text{mH}$, $I_{AS} = 1.1\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$.

4. $I_{SD} \leq 2.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	SOT-223-2/SOT-223	θ_{JA}	150	$^\circ\text{C}/\text{W}$
	TO-220/TO-220F1		62.5	$^\circ\text{C}/\text{W}$
	TO-220F2			
	TO-251/TO-251S2 TO-252		110	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-223-2/SOT-223	θ_{JC}	50 (Note)	$^\circ\text{C}/\text{W}$
	TO-220		3.38	$^\circ\text{C}/\text{W}$
	TO-220F1/TO-220F2		5.95	$^\circ\text{C}/\text{W}$
	TO-251/TO-251S2		5.68 (Note)	$^\circ\text{C}/\text{W}$
	TO-252			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

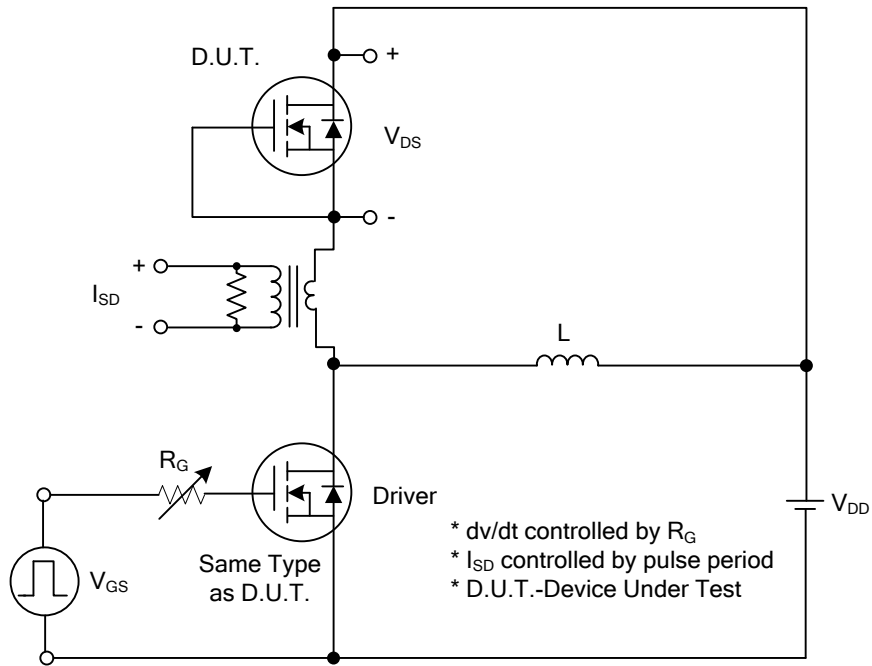
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250μA	900			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 900V, V _{GS} = 0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}			100	nA
	Reverse					
		V _{GS} = -30V, V _{DS} = 0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	2.5		4.5	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 1.5A			4.9	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =50V, f=1.0MHz		240		pF
Output Capacitance	C _{OSS}			25		pF
Reverse Transfer Capacitance	C _{RSS}			1.9		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =720V, V _{GS} =10V, I _D =3.0A, (Note 1,2)		16		nC
Gate to Source Charge	Q _{GS}			6.1		nC
Gate to Drain Charge	Q _{GD}			4.2		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DD} = 100V, V _{GS} =10V, I _D =3.0A, R _G = 25Ω, (Note 1,2)		6		nS
Rise Time	t _R			16		nS
Turn-OFF Delay Time	t _{D(OFF)}			29		nS
Fall-Time	t _F			30		nS
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				3	A
Maximum Body-Diode Pulsed Current	I _{SM}				12	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =3.0A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =3.0A, V _{GS} =0V, dI _F /dt=100A/μs		470		nS
Body Diode Reverse Recovery Charge	Q _{rr}				2.9	

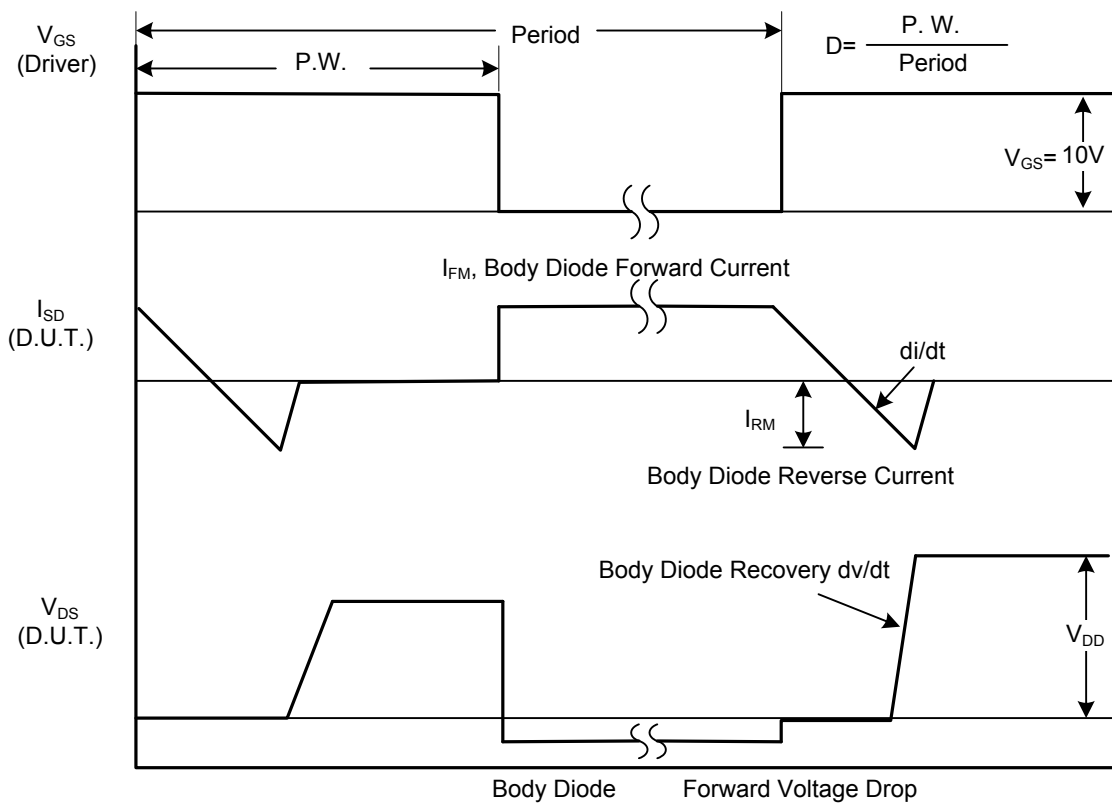
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

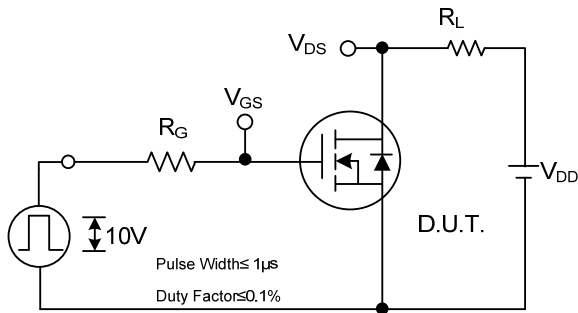


Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

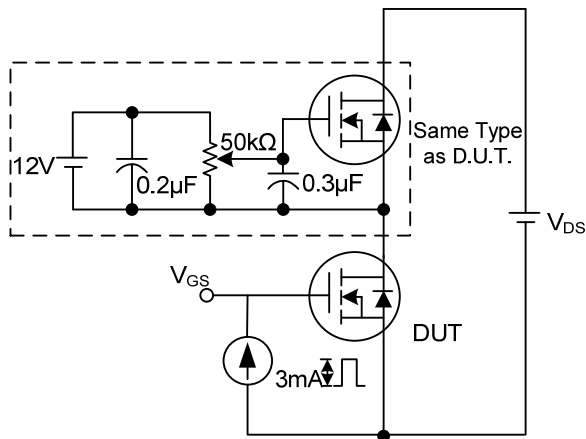
TEST CIRCUITS AND WAVEFORMS (Cont.)



Switching Test Circuit



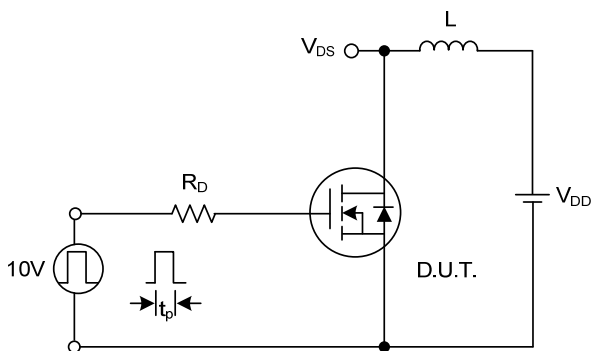
Switching Waveforms



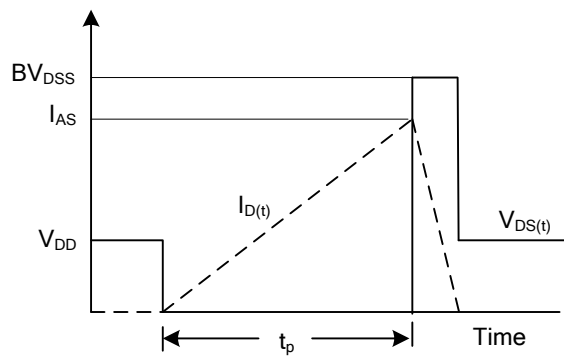
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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