

# 4NM50

**Power MOSFET**

## 4A, 500V N-CHANNEL SUPER-JUNCTION MOSFET

### ■ DESCRIPTION

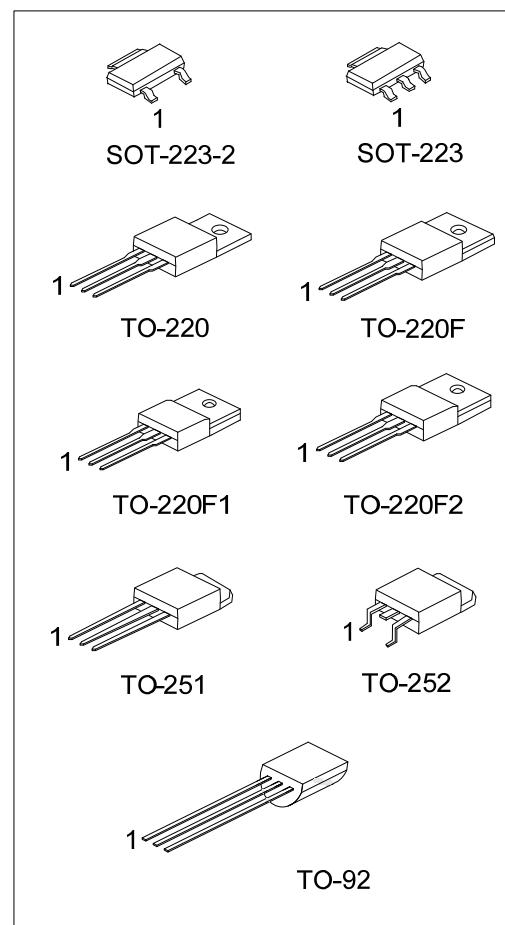
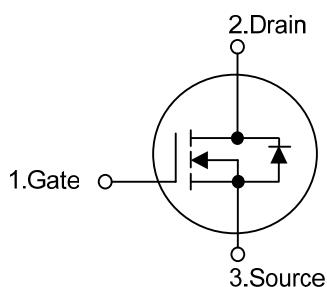
The UTC **4NM50** is a high voltage super junction MOSFET and is designed to have better characteristics.

The UTC **4NM50** Utilizing an advanced charge-balance technology, enhance system efficiency, improve EMI and reliability. such as low gate charge, low on-state resistance and have a high power density and high rugged avalanche characteristics. This super junction MOSFET usually used at AC/DC power conversion, and industrial power applications.

### ■ FEATURES

- \*  $R_{DS(ON)} \leq 1.3\Omega$  @  $V_{GS}=10V$ ,  $I_D=2.0A$
- \* Fast Switching Capability
- \* Avalanche Energy Tested
- \* Improved dv/dt Capability, High Ruggedness

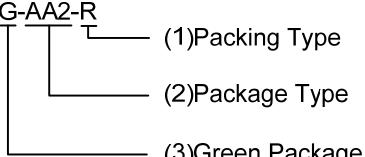
### ■ SYMBOL



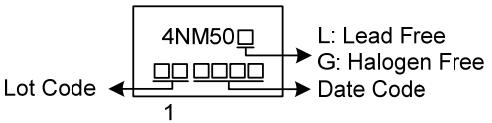
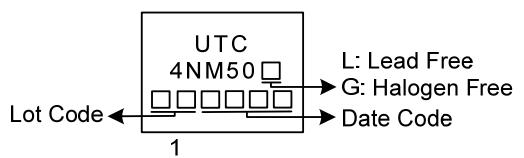
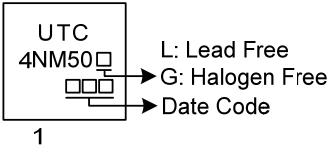
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
4NM50L-AA2-R	4NM50G-AA2-R	SOT-223-2	G	D	S	Tape Reel
4NM50L-AA3-R	4NM50G-AA3-R	SOT-223	G	D	S	Tape Reel
4NM50L-TA3-T	4NM50G-TA3-T	TO-220	G	D	S	Tube
4NM50L-TF1-T	4NM50G-TF1-T	TO-220F1	G	D	S	Tube
4NM50L-TF2-T	4NM50G-TF2-T	TO-220F2	G	D	S	Tube
4NM50L-TF3-T	4NM50G-TF3-T	TO-220F	G	D	S	Tube
4NM50L-TM3-T	4NM50G-TM3-T	TO-251	G	D	S	Tube
4NM50L-TN3-R	4NM50G-TN3-R	TO-252	G	D	S	Tape Reel
4NM50L-T92-B	4NM50G-T92-B	TO-92	G	D	S	Tape Box
4NM50L-T92-K	4NM50G-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

 4NM50G-AA2-R	(1) T: Tube, R: Tape Reel (2) AA2: SOT-223-2, AA3: SOT-223, TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TM3: TO-251, TN3: TO-252, T92: TO-92 (3) G: Halogen Free and Lead Free, L: Lead Free
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### ■ MARKING

Package	Marking
SOT-223-2 / SOT-223	 Lot Code ←
TO-220F / TO-220F / TO-220F1 TO-220F2 / TO-251 / TO-252	 Lot Code ←
TO-92	 1

■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	$V_{DSS}$	500	V	
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V	
Drain Current	Continuous Pulsed (Note 2)	$I_D$ $I_{DM}$	4 8	A A
Avalanche Current (Note 2)		$I_{AR}$	1.7	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	38	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.5	V/ns
Power Dissipation	SOT-223-2/SOT-223	$P_D$	5	W
	TO-220		85	W
	TO-220F/TO-220F1		28	W
	TO-220F2		30	W
	TO-251/TO-252		52	W
	TO-92		1.78	W
Junction Temperature	$T_J$	+150	$^\circ\text{C}$	
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L=10\text{mH}$ ,  $I_{AS}=2.76\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 4.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	SOT-223-2/SOT-223	$\theta_{JA}$	150	$^\circ\text{C/W}$
	TO-220/TO-220F		62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		110	$^\circ\text{C/W}$
	TO-251/TO-252		160	$^\circ\text{C/W}$
	TO-92		25	$^\circ\text{C/W}$
Junction to Case	TO-220	$\theta_{JC}$	1.47	$^\circ\text{C/W}$
	TO-220F/TO-220F1		4.46	$^\circ\text{C/W}$
	TO-220F2		4.16	$^\circ\text{C/W}$
	TO-251/TO-252		2.4	$^\circ\text{C/W}$
	TO-92		70	$^\circ\text{C/W}$

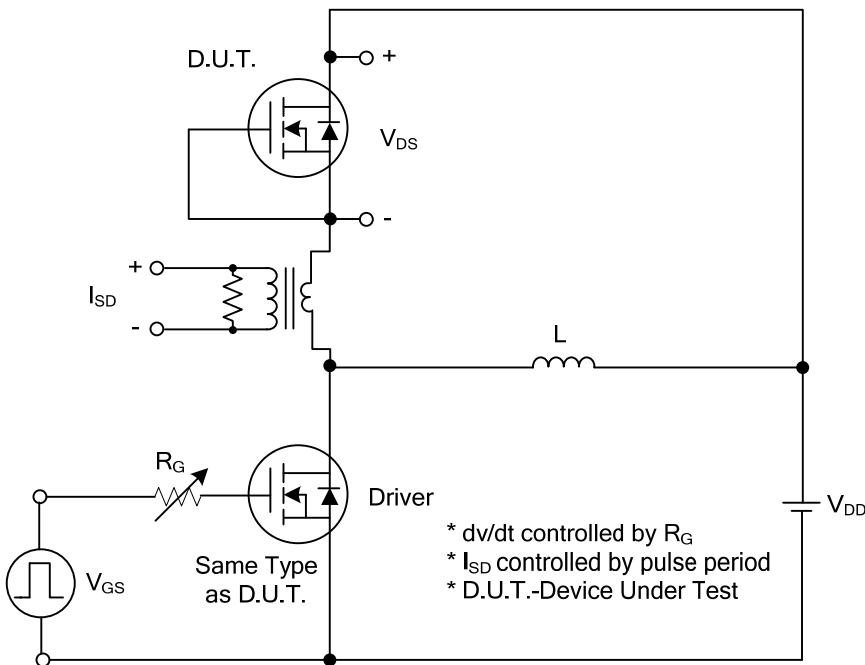
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	500			V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 500\text{V}, V_{\text{GS}} = 0\text{V}$			10	$\mu\text{A}$
Gate- Source Leakage Current	Forward	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0\text{V}$			100	nA
	Reverse	$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0\text{V}$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 2.0\text{A}$			1.3	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1.0 \text{ MHz}$		215		pF
Output Capacitance	$C_{\text{OSS}}$			205		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			27		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{\text{DS}}=400\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}, I_{\text{G}}=100\mu\text{A}(\text{Note 1,2})$		26		nC
Gate to Source Charge	$Q_{\text{GS}}$			2.5		nC
Gate to Drain Charge	$Q_{\text{GD}}$			3.4		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=100\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}, R_{\text{G}}=25\Omega(\text{Note 1,2})$		4		ns
Rise Time	$t_R$			18		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			30		ns
Fall-Time	$t_F$			26		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_S$				4	A
Maximum Body-Diode Pulsed Current	$I_{\text{SM}}$				8	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{\text{SD}}$	$I_S=4.0\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	$t_{\text{rr}}$	$I_S=4.0\text{A}, V_{\text{GS}}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		210		ns
Body Diode Reverse Recovery Charge	$Q_{\text{rr}}$				1.52	$\mu\text{C}$

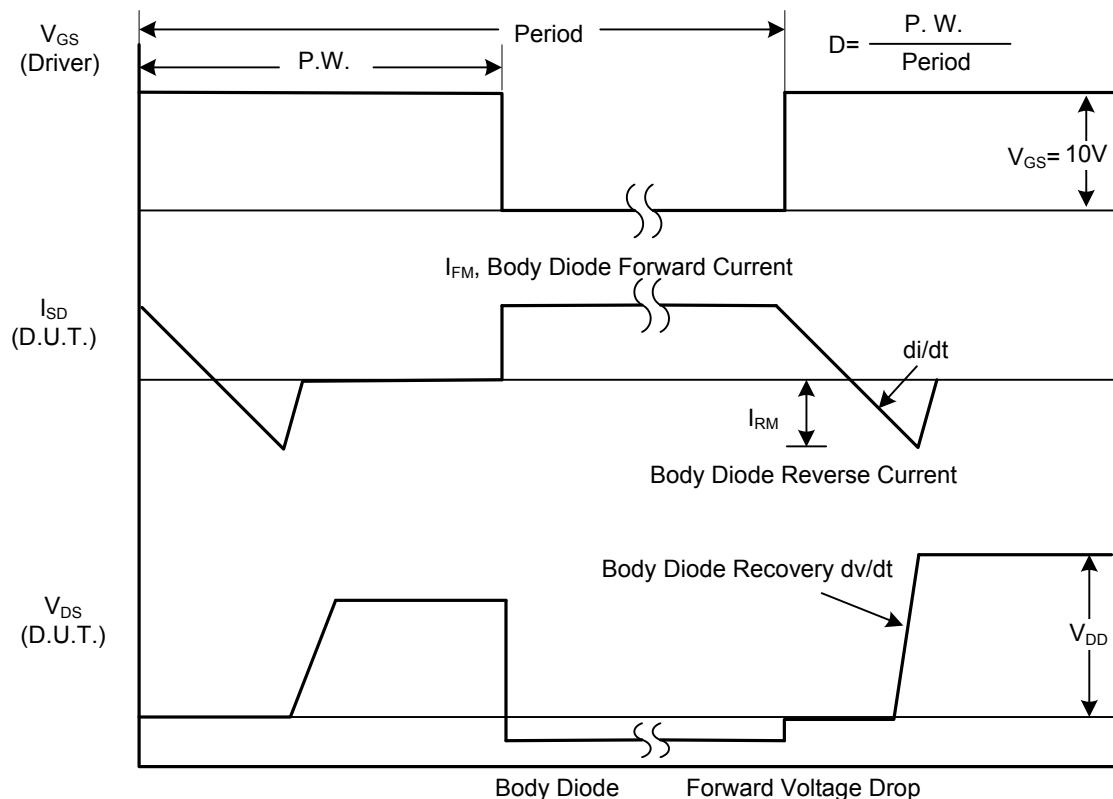
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

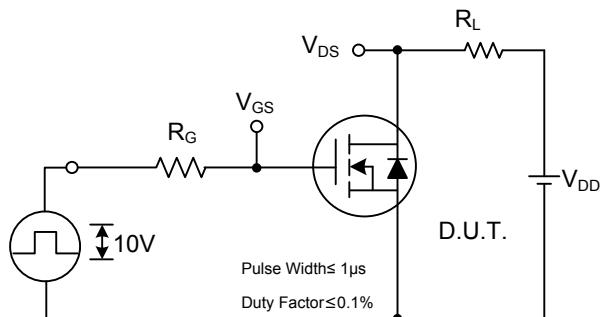


**Peak Diode Recovery dv/dt Test Circuit**

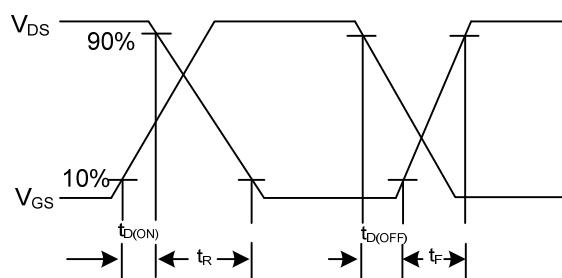


**Peak Diode Recovery dv/dt Waveforms**

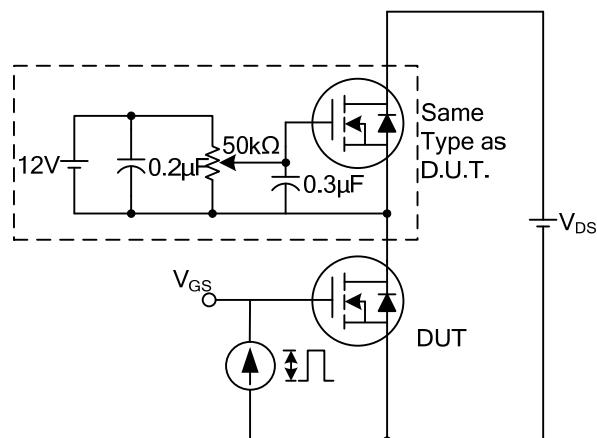
### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



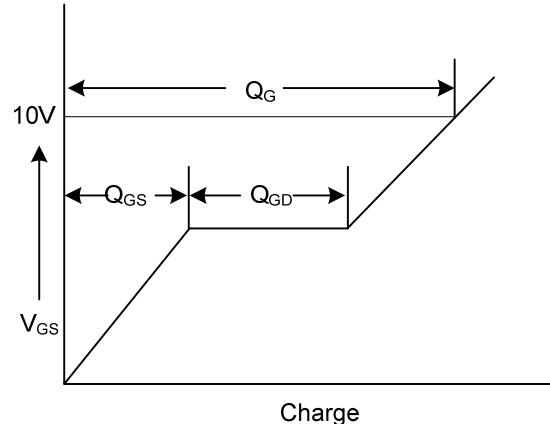
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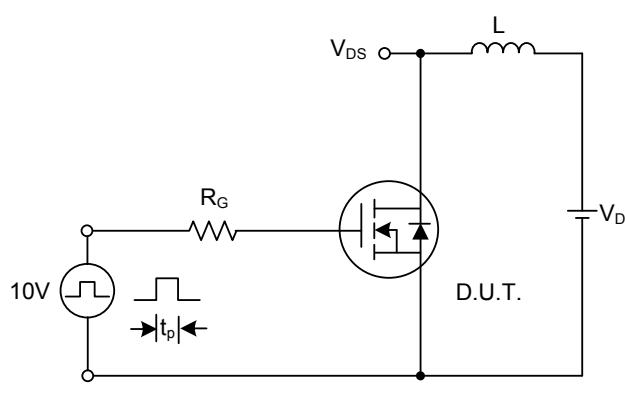
**Switching Waveforms**



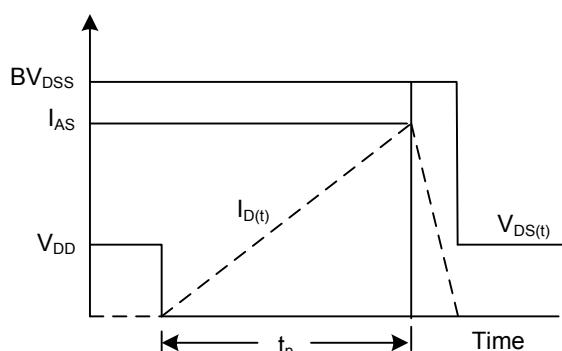
**Gate Charge Test Circuit**



**Gate Charge Waveform**

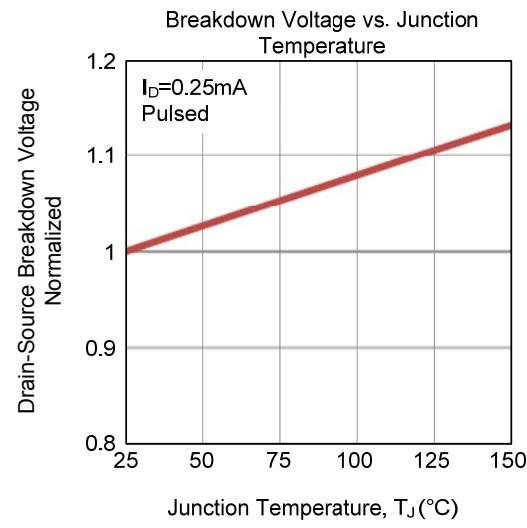
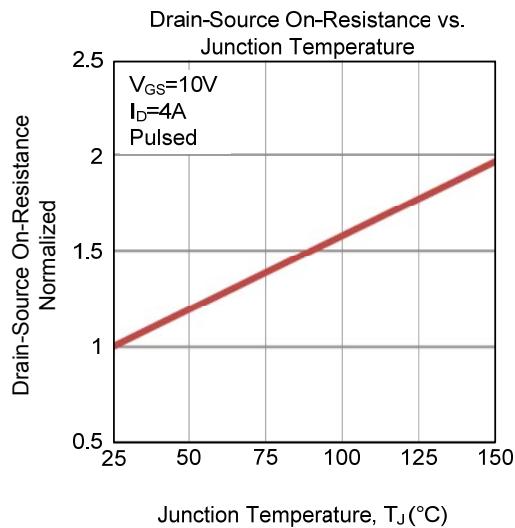
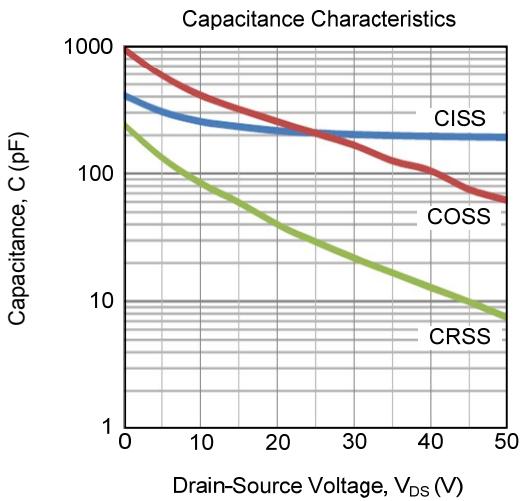
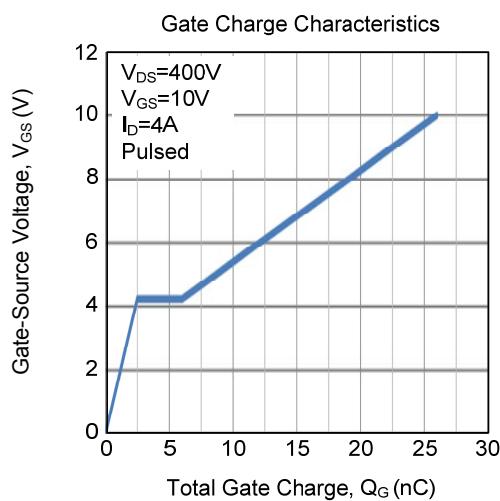
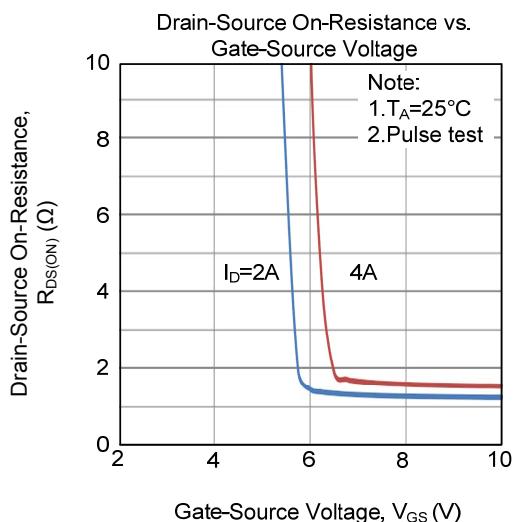
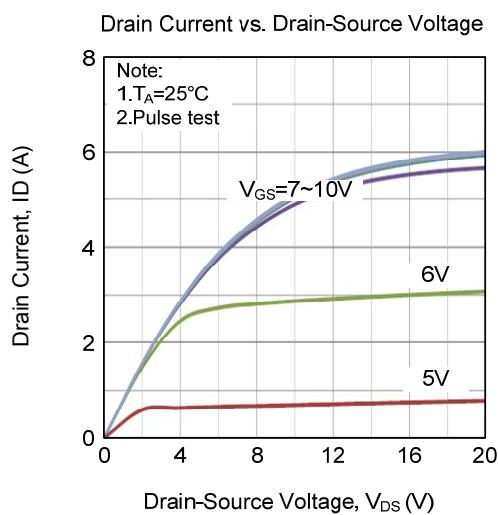


**Unclamped Inductive Switching Test Circuit**

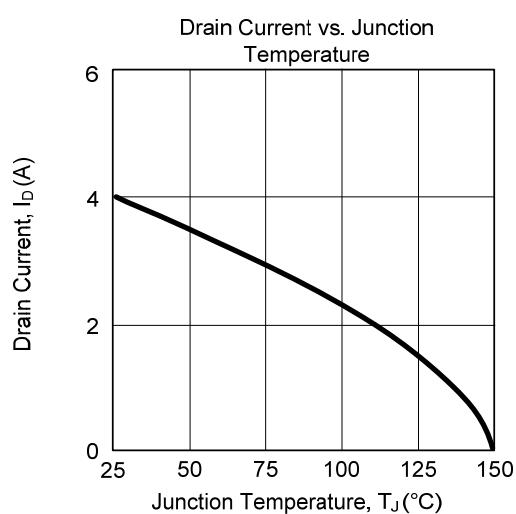
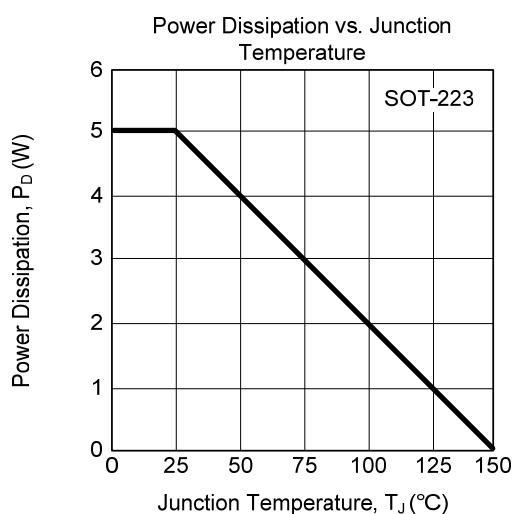
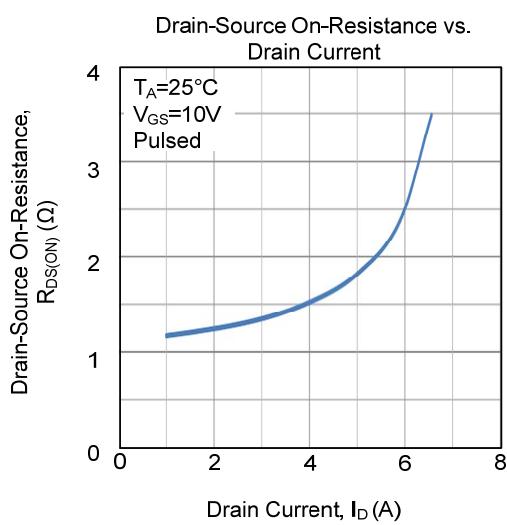
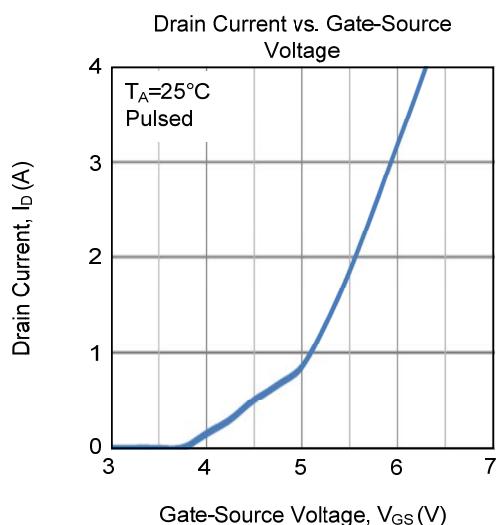
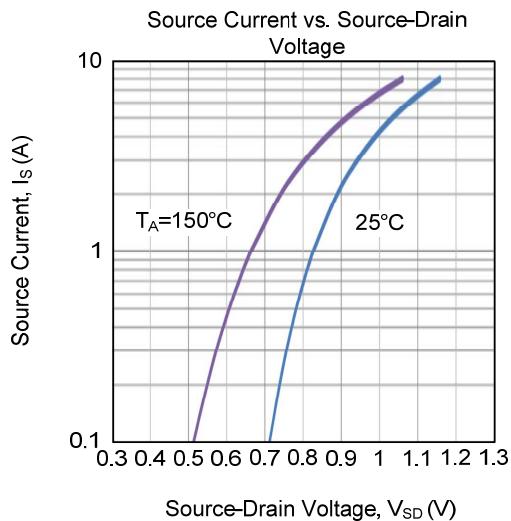
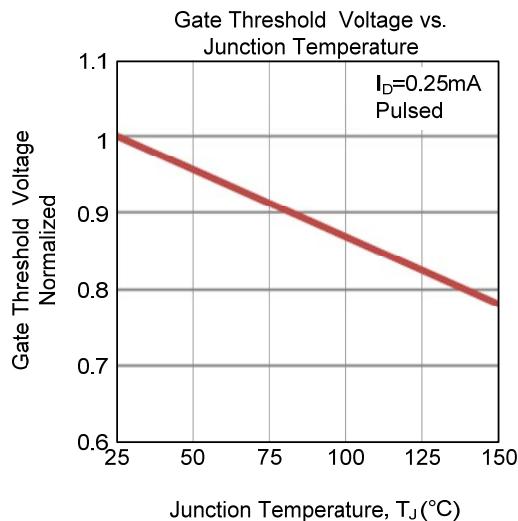


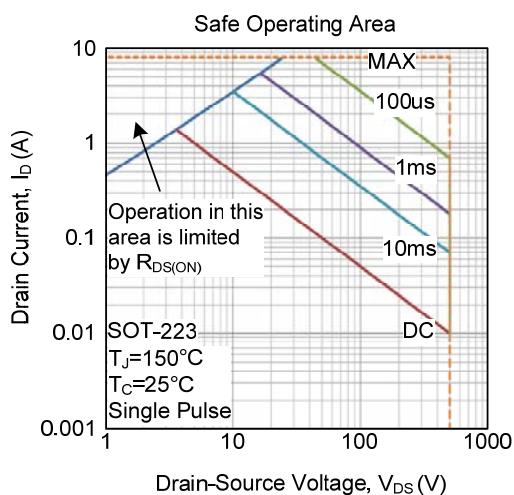
**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



**■ TYPICAL CHARACTERISTICS (Cont.)**

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