



# 10N65K-MTQ

*Power MOSFET*

## 10A, 650V N-CHANNEL POWER MOSFET

### DESCRIPTION

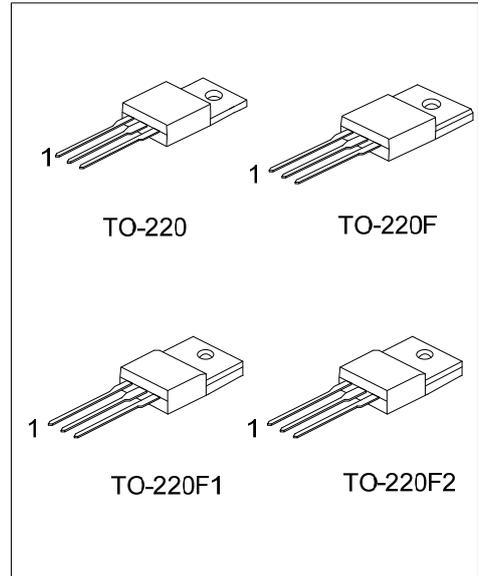
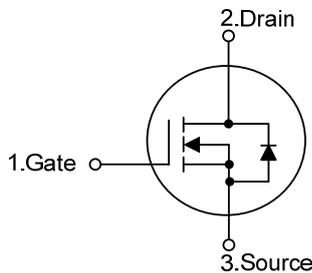
The UTC **10N65K-MTQ** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers planar stripe and DMOS technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **10N65K-MTQ** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.

### FEATURES

- \*  $R_{DS(ON)} \leq 1.0 \Omega @ V_{GS}=10V, I_D=5.0A$
- \* High Switching Speed
- \* Improved dv/dt Capability
- \* 100% Avalanche Tested

### SYMBOL



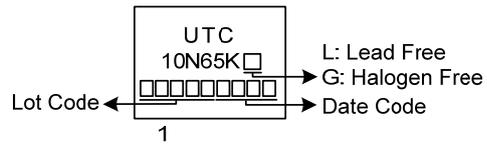
### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
10N65KL-TA3-T	10N65KG-TA3-T	TO-220	G	D	S	Tube
10N65KL-TF1-T	10N65KG-TF1-T	TO-220F1	G	D	S	Tube
10N65KL-TF2-T	10N65KG-TF2-T	TO-220F2	G	D	S	Tube
10N65KL-TF3-T	10N65KG-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>10N65KG-TA3-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube</p> <p>(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
--	--

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ , unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous ( $T_C=25^\circ\text{C}$ )	$I_D$	10	A
	Pulsed (Note 2)	$I_{DM}$	30	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	800	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.1	V/ns
Power Dissipation	TO-220	$P_D$	135	W
	TO-220F/TO-220F1		38	W
	TO-220F2			
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3.  $L = 100\text{mH}$ ,  $I_{AS} = 4.1\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 10\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

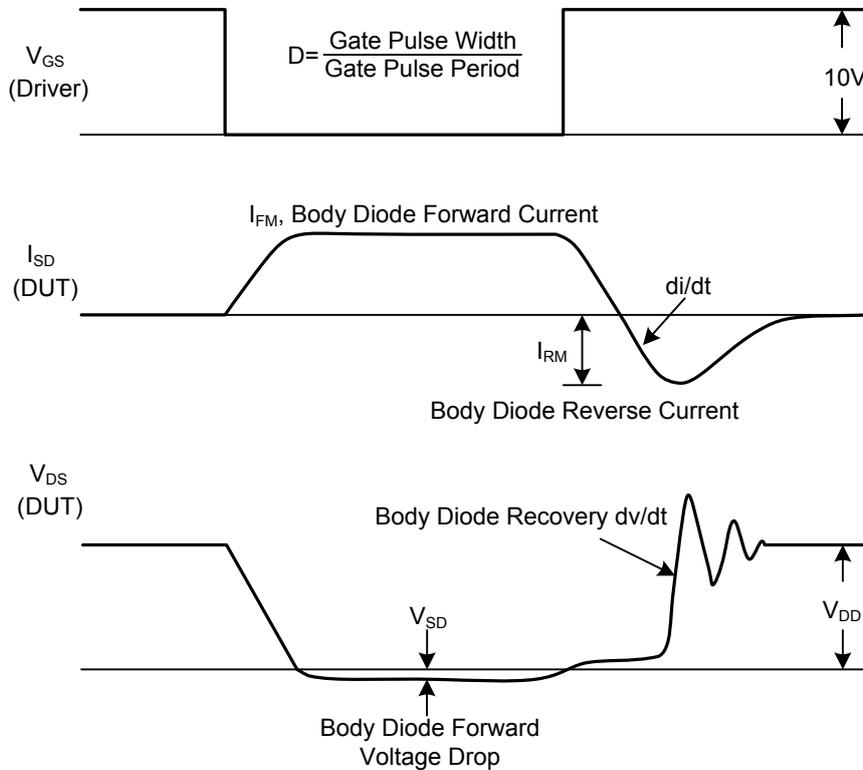
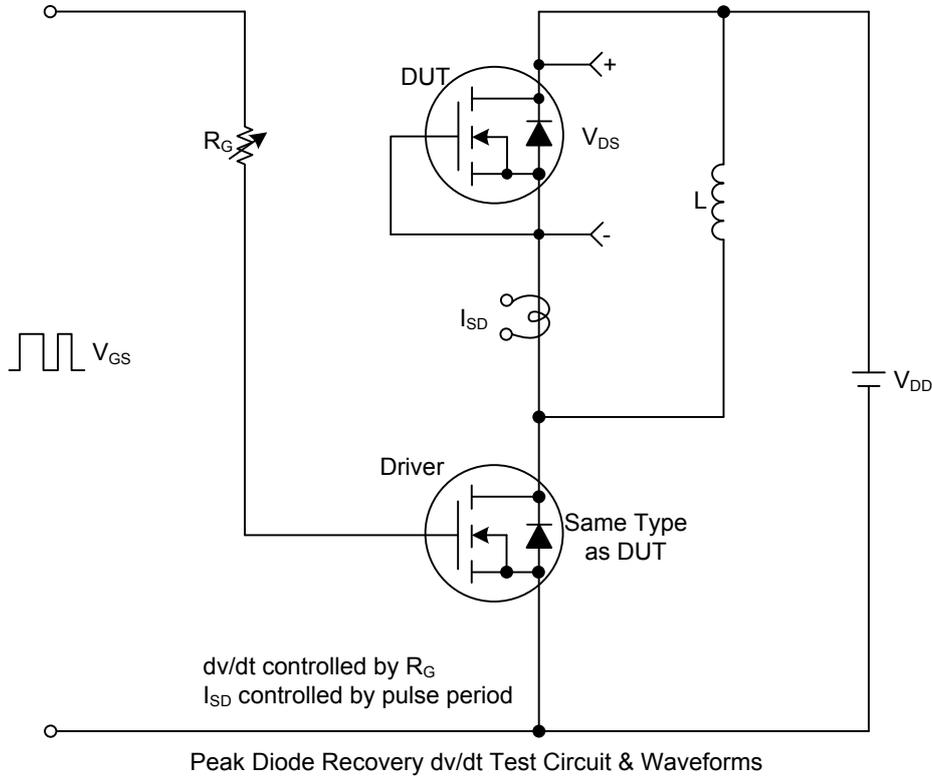
PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
	TO-220F1/TO-220F2			
Junction to Case	TO-220	$\theta_{JC}$	0.92	$^\circ\text{C}/\text{W}$
	TO-220F/TO-220F1		3.29	
	TO-220F2			

■ ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C, unless otherwise noted)

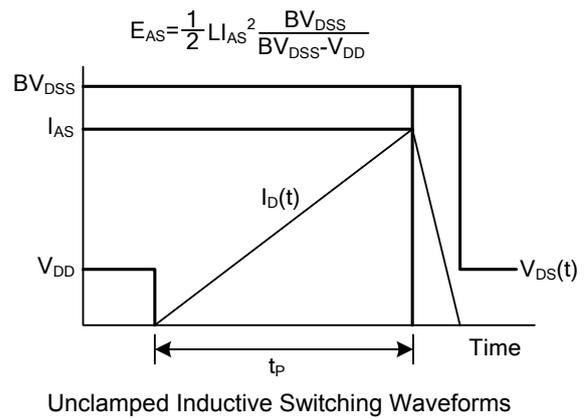
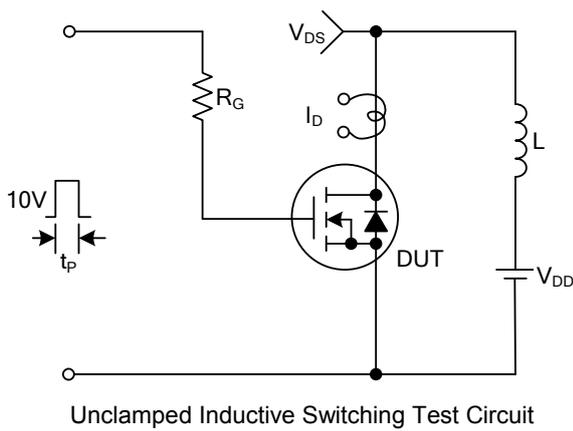
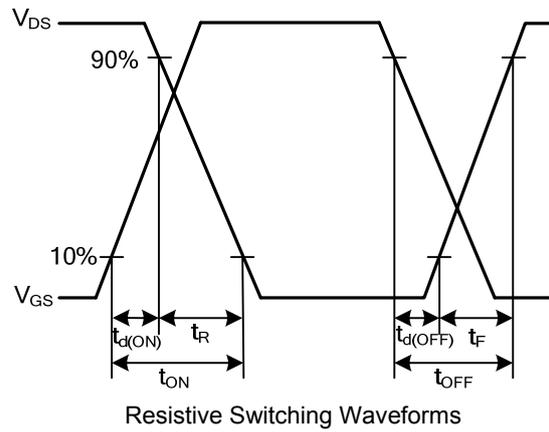
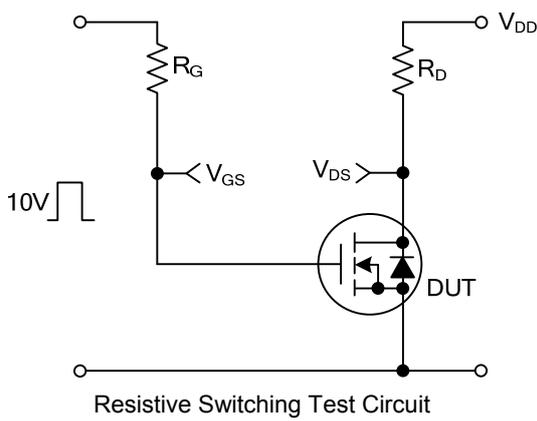
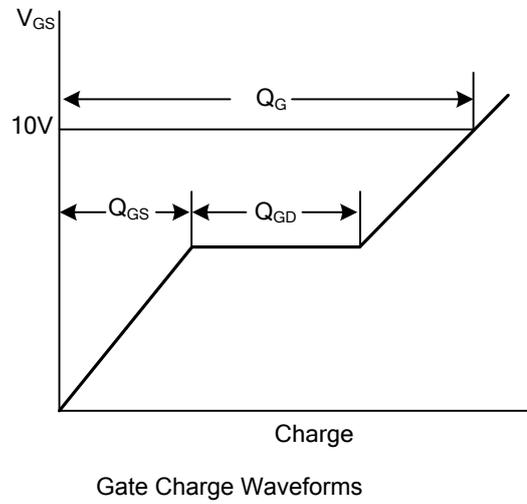
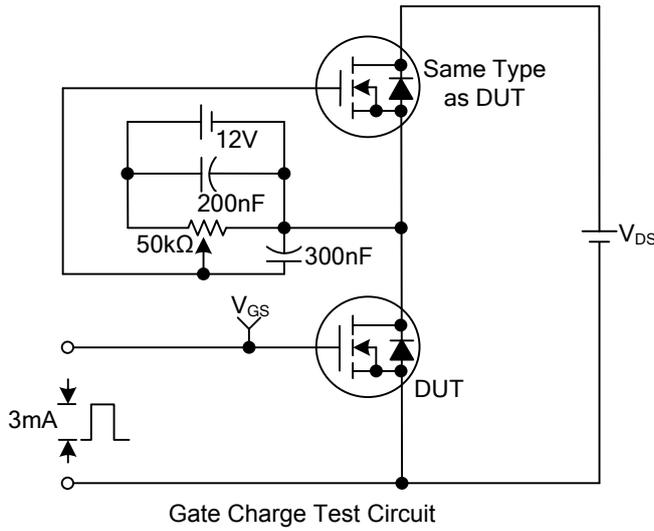
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	650			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			10	μA
Gate- Source Leakage Current	Forward	I <sub>GSS</sub>			+100	nA
	Reverse				-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5.0A			1.0	Ω
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		1280		pF
Output Capacitance	C <sub>OSS</sub>			125		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			9		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 1)	Q <sub>G</sub>	V <sub>DS</sub> =520V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A, (Note 1, 2)		32		nC
Gate to Source Charge	Q <sub>GS</sub>			8		nC
Gate to Drain Charge	Q <sub>GD</sub>			6.2		nC
Turn-ON Delay Time (Note 1)	t <sub>D(ON)</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A, R <sub>G</sub> =25Ω (Note 1, 2)		20		ns
Rise Time	t <sub>R</sub>			22.5		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			95		ns
Fall-Time	t <sub>F</sub>			34.8		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>				10	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>				30	A
Drain-Source Diode Forward Voltage (Note 1)	V <sub>SD</sub>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time (Note 1)	t <sub>rr</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 10A,		375		ns
Reverse Recovery Charge (Note 1)	Q <sub>rr</sub>	dI <sub>F</sub> /dt = 100 A/μs		8.2		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%  
 2. Essentially independent of operating temperature.

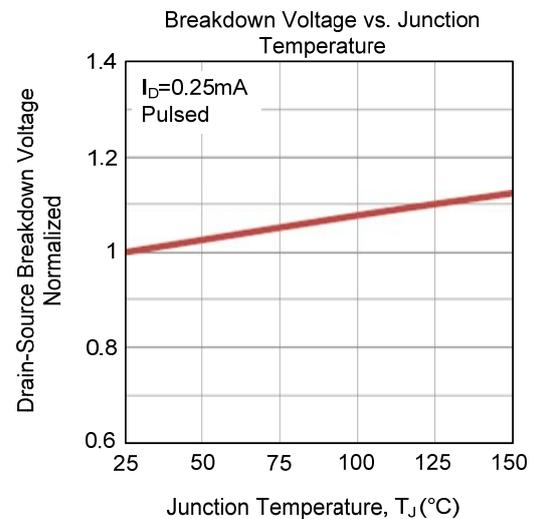
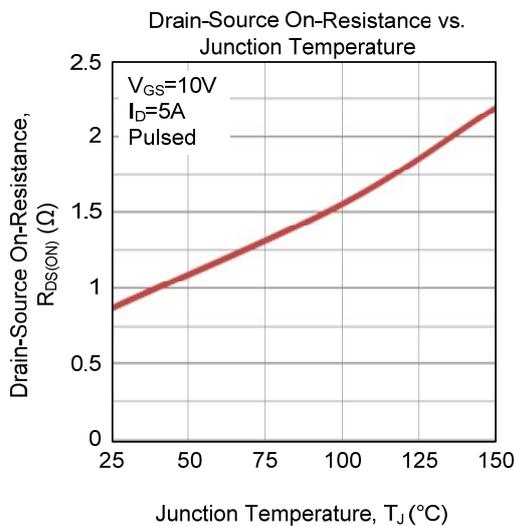
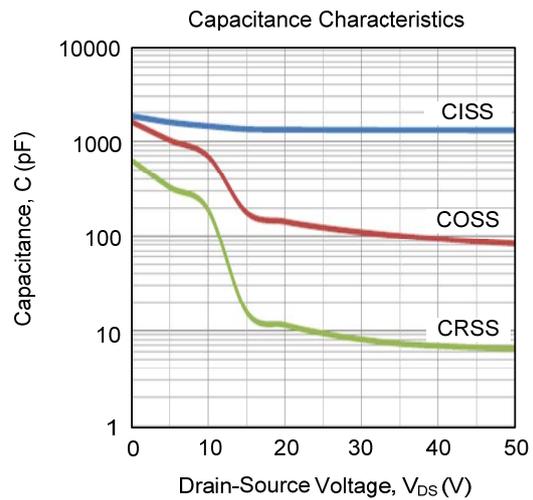
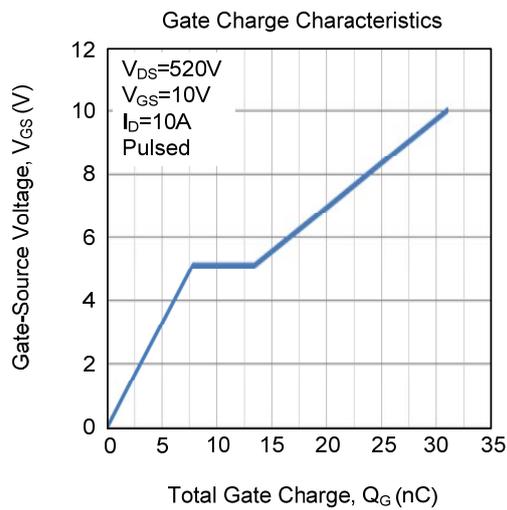
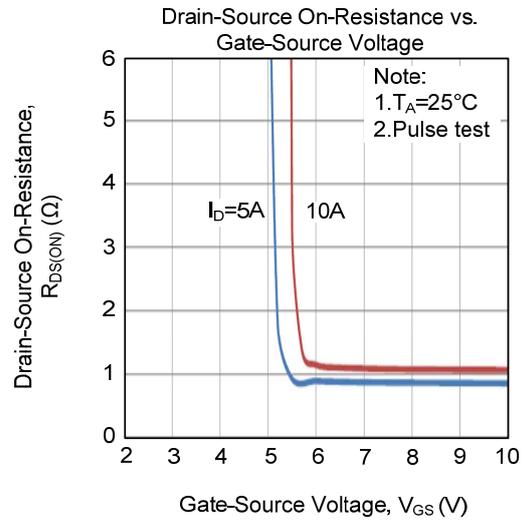
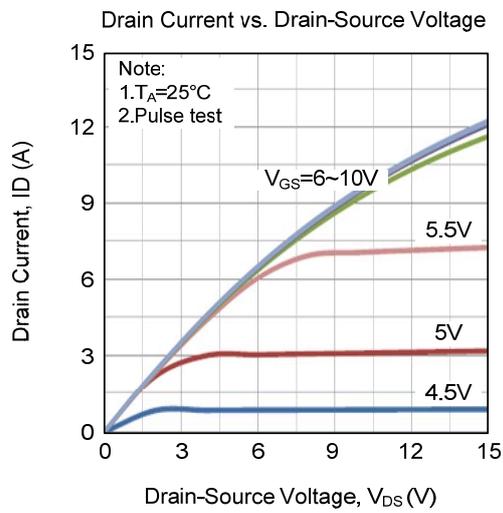
## TEST CIRCUITS AND WAVEFORMS



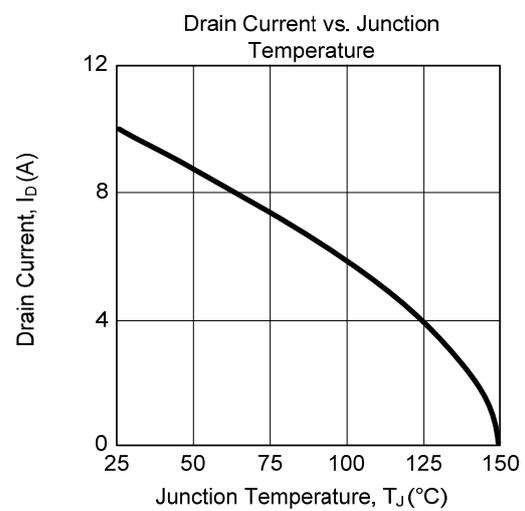
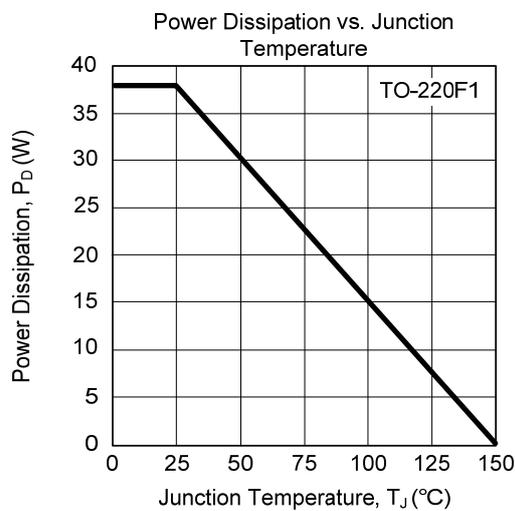
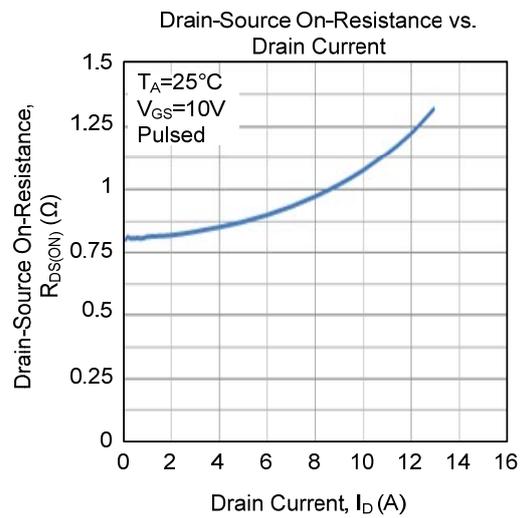
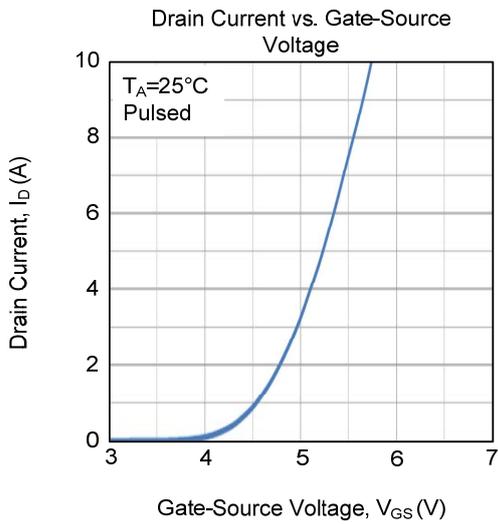
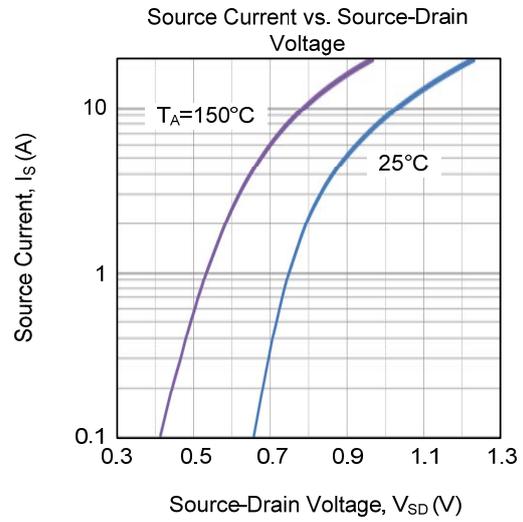
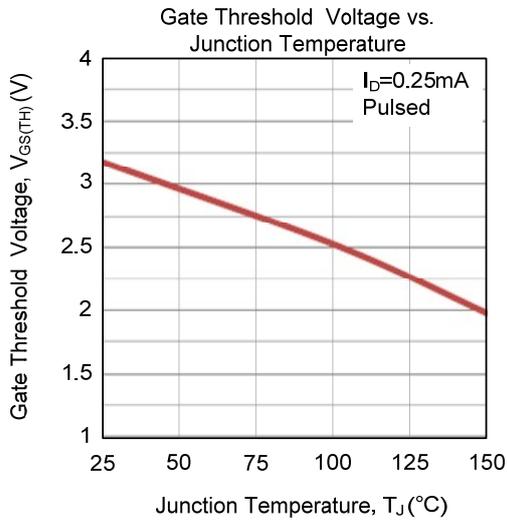
## TEST CIRCUITS AND WAVEFORMS



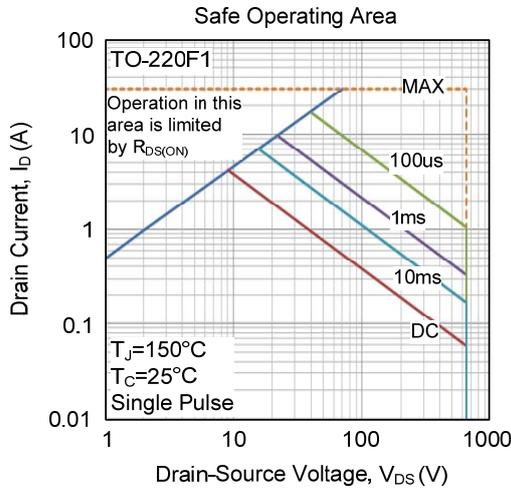
## TYPICAL CHARACTERISTICS



## TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.