

5NM70

Power MOSFET

5A, 700V N-CHANNEL
SUPER-JUNCTION MOSFET

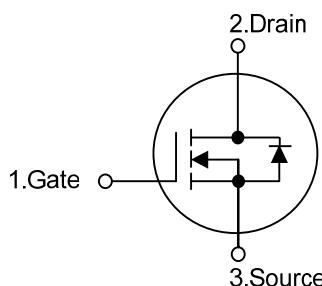
■ DESCRIPTION

The UTC **5NM70** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} \leq 1.3\Omega$ @ $V_{GS}=10V$, $I_D=2.5A$
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness

■ SYMBOL



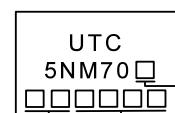
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5NM70L-AA3-R	5NM70G-AA3-R	SOT-223	G	D	S	Tape Reel
5NM70L-TF1-T	5NM70G-TF1-T	TO-220F1	G	D	S	Tube
5NM70L-TM3-T	5NM70G-TM3-T	TO-251	G	D	S	Tube
5NM70L-TMS-T	5NM70G-TMS-T	TO-251S	G	D	S	Tube
5NM70L-TMS2-T	5NM70G-TMS2-T	TO-251S2	G	D	S	Tube
5NM70L-TMN2-T	5NM70G-TMN2-T	TO-251NS2	G	D	S	Tube
5NM70L-TN3-R	5NM70G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

5NM70G-AA3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) AA3: SOT-223, TF1: TO-220F1, TM3: TO-251, TMS: TO-251S, TMS2: TO-251S2, TMN2: TO-251NS2, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

SOT-223	TO-220F1 / TO-251 / TO-251S TO-251S2 / TO-251NS2 / TO-252
 1	 1

■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	V_{DSS}	700	V	
Gate-Source Voltage	V_{GSS}	± 30	V	
Continuous Drain Current	I_D	5	A	
Pulsed Drain Current (Note 2)	I_{DM}	10	A	
Avalanche Current (Note 2)	I_{AR}	1.3	A	
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	mJ	
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.0	V/ns	
Power Dissipation	SOT-223	P_D	12	W
	TO-220F1		26	W
	TO-251/TO-251S		54	W
	TO-251S2/TO-251NS2			
	TO-252			
Junction Temperature	T_J	+150	$^\circ\text{C}$	
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L=145\text{mH}$, $I_{AS}=1.3\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 5.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	SOT-223	θ_{JA}	150	$^\circ\text{C/W}$
	TO-220F1		62.5	$^\circ\text{C/W}$
	TO-251/TO-251S		110	$^\circ\text{C/W}$
	TO-251S2/TO-251NS2			
	TO-252			
Junction to Case	SOT-223	θ_{JC}	10.4	$^\circ\text{C/W}$
	TO-220F1		4.8	$^\circ\text{C/W}$
	TO-251/TO-251S		2.3 (Note)	$^\circ\text{C/W}$
	TO-251S2/TO-251NS2			
	TO-252			

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

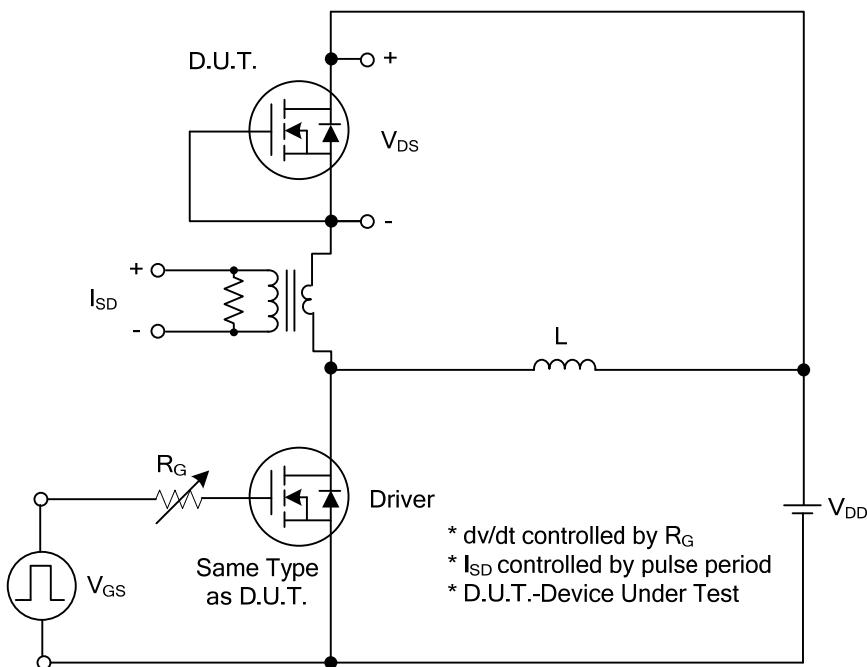
■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	700			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=700\text{V}, V_{\text{GS}}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}}=30\text{V}, V_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$V_{\text{GS}}=-30\text{V}, V_{\text{DS}}=0\text{V}$		-100		
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2.5\text{A}$			1.3	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		360		pF
Output Capacitance	C_{OSS}			270		pF
Reverse Transfer Capacitance	C_{RSS}			6		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$V_{\text{DS}}=560\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$, $I_G=1\text{mA}$ (Note 1, 2)		13		nC
Gate to Source Charge	Q_{GS}			6		nC
Gate to Drain Charge	Q_{GD}			2		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$			6		ns
Rise Time	t_R			17		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			24		ns
Fall-Time	t_F			23		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				5	A
Maximum Body-Diode Pulsed Current	I_{SM}				10	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=5\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=5\text{A}, V_{\text{GS}}=0\text{V}$		290		ns
Body Diode Reverse Recovery Charge	Q_{rr}	$dI_F/dt=100\text{A}/\mu\text{s}$			2.5	μC

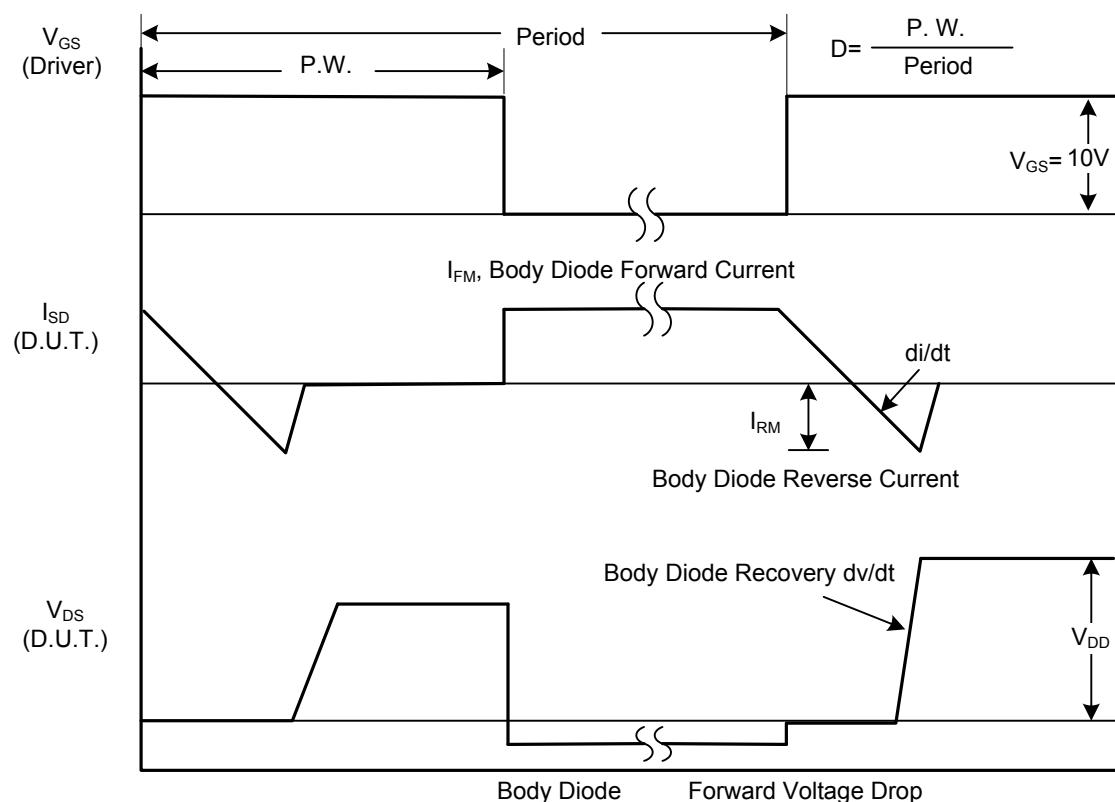
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

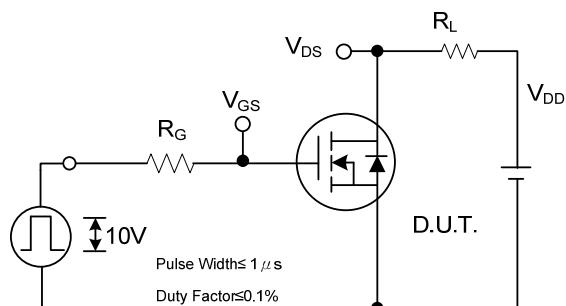
■ TEST CIRCUITS AND WAVEFORMS



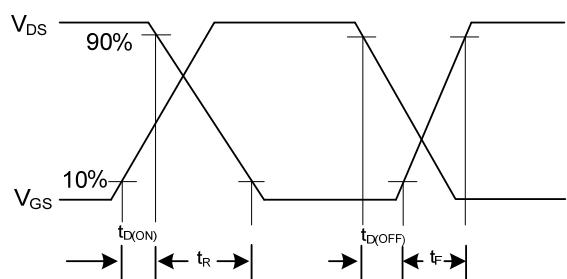
Peak Diode Recovery dv/dt Test Circuit



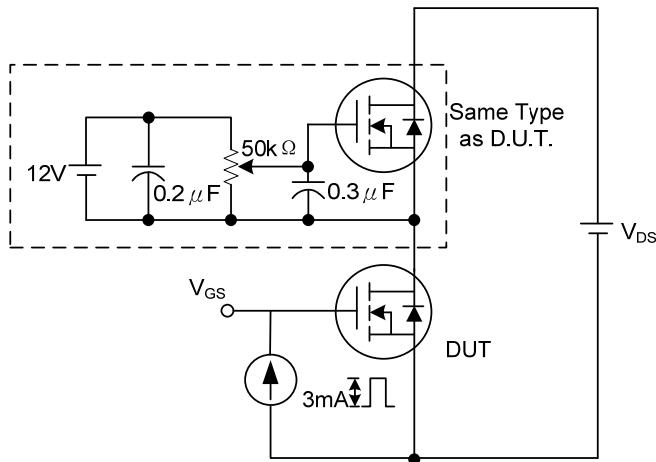
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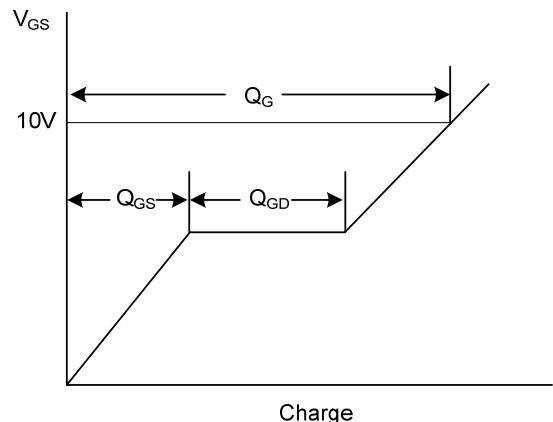
Switching Test Circuit



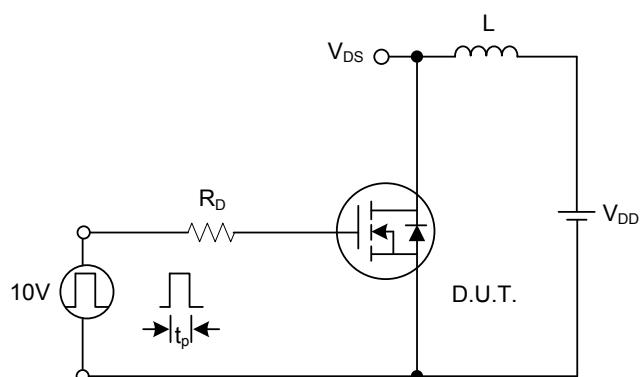
Switching Waveforms



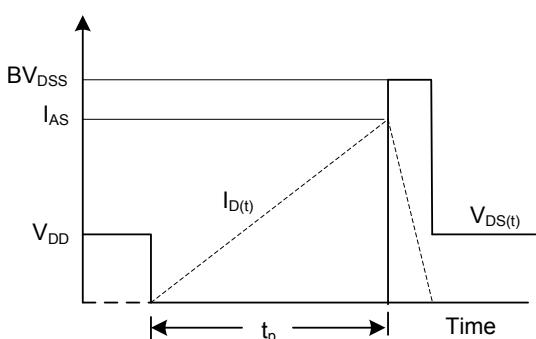
Gate Charge Test Circuit



Gate Charge Waveform

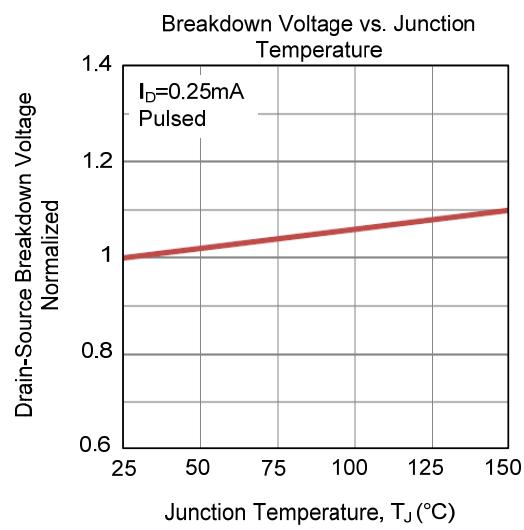
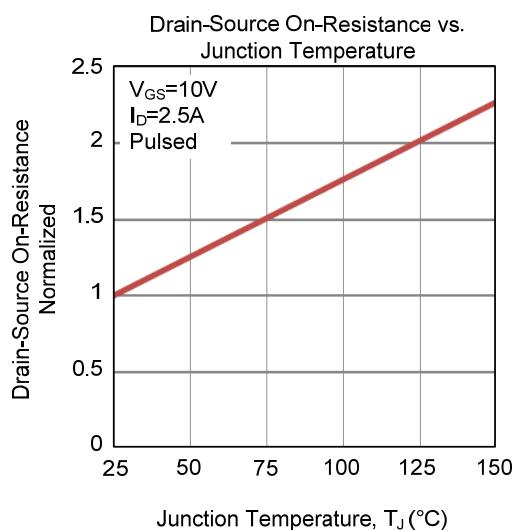
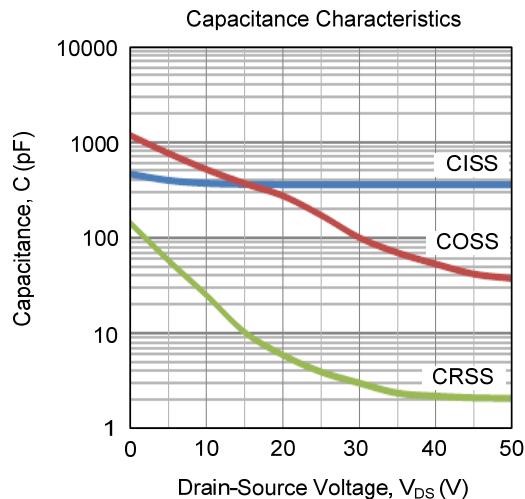
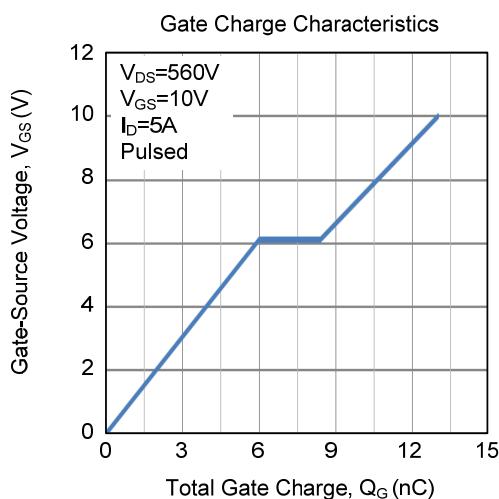
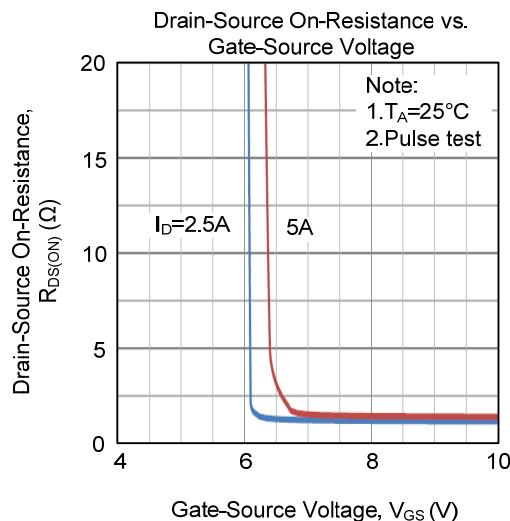
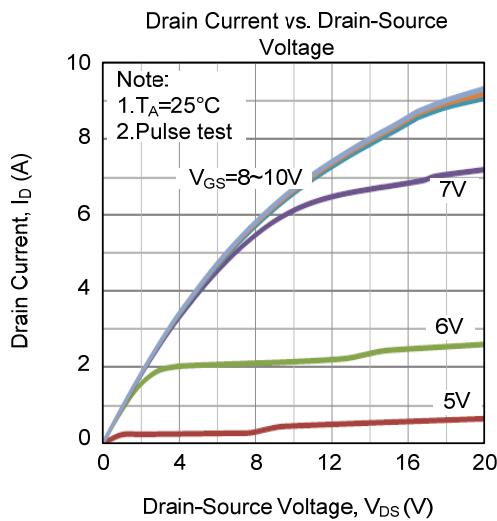


Unclamped Inductive Switching Test Circuit

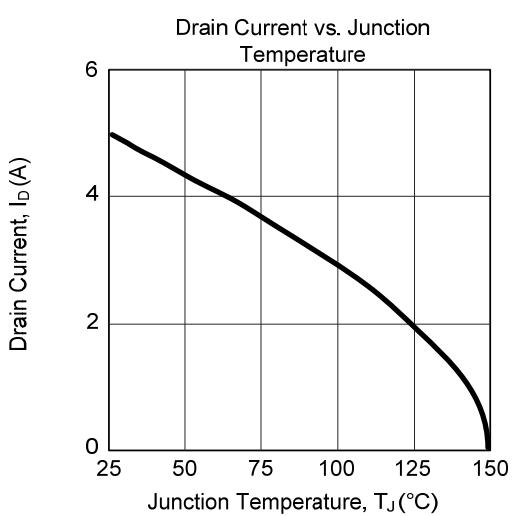
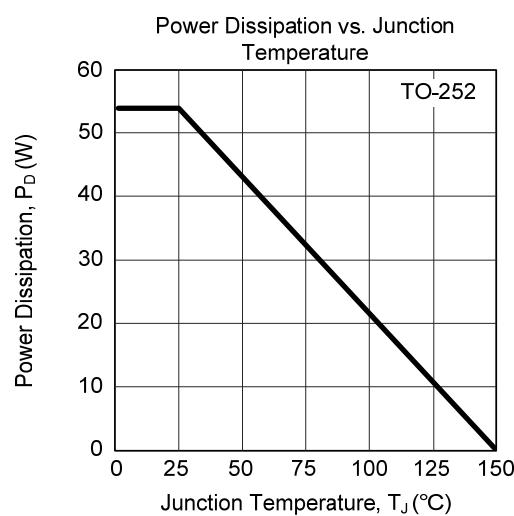
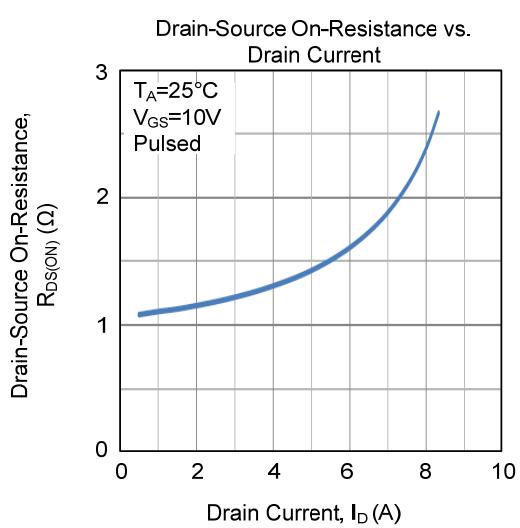
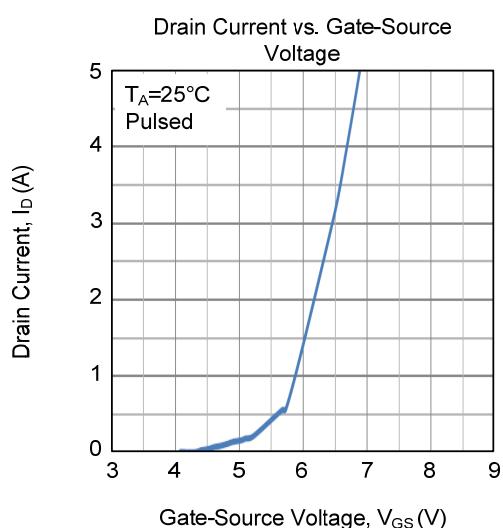
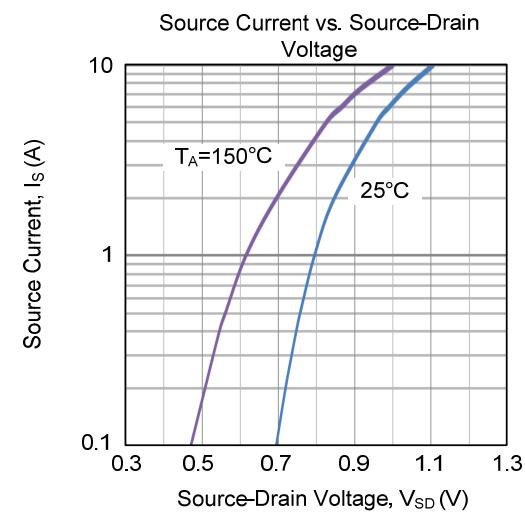
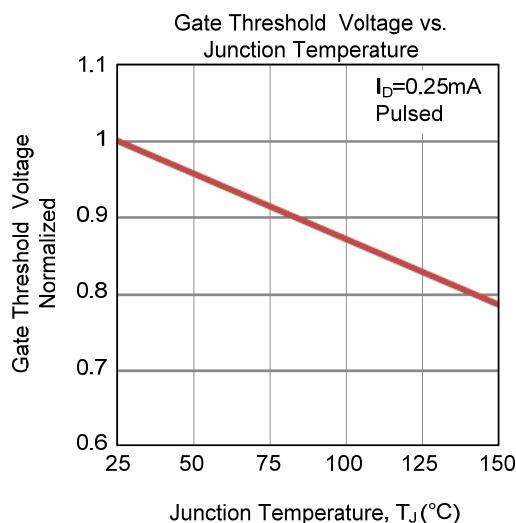


Unclamped Inductive Switching Waveforms

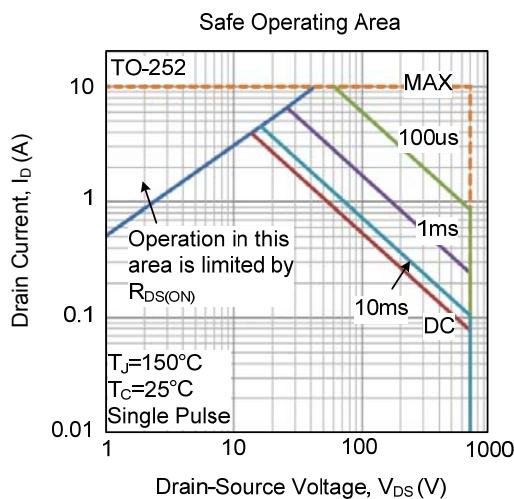
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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