



U74AHC1G132

CMOS IC

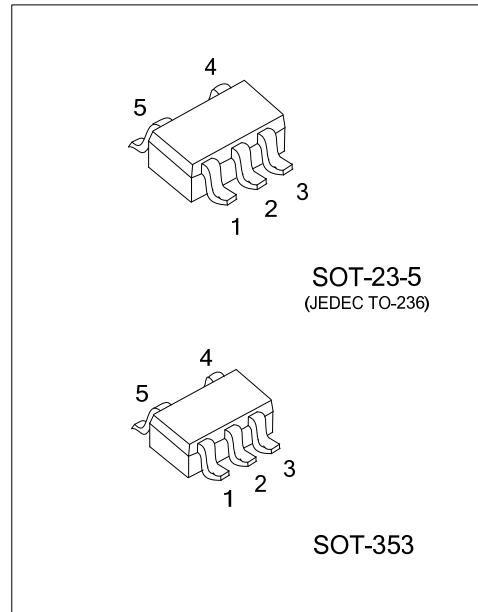
SINGLE 2-INPUT NAND GATE WITH SCHMITT-TRIGGER INPUTS

DESCRIPTION

The **U74AHC1G132** contains one 2-input NAND gate with Schmitt-trigger inputs designed for 2V to 5.5V V_{CC} operation and performs the Boolean function $Y = \overline{A \cdot B}$ or $Y = \overline{A} + \overline{B}$ in positive logic.

Because of Schmitt action, this device has different input threshold levels for positive-going (V_{T+}) and negative-going (V_{T-}) signals.

This device can be triggered from the slowest of input ramps and still give clean jitter-free output signals.



FEATURES

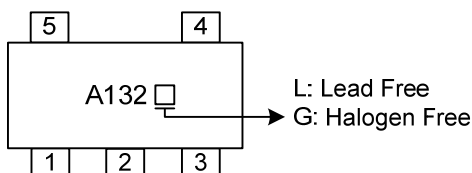
- * Operation voltage range: 2 ~ 5.5V
- * Max t_{pd} of 7.7 ns at 5 V
- * Low static power consumption; $I_{CC}=2\mu A$ (Max.)
- * $\pm 8mA$ output drive at 5 V

ORDERING INFORMATION

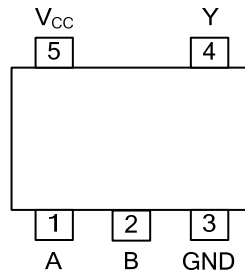
Ordering Number		Package	Packing
Lead Free	Halogen Free		
U74AHC1G132L-AE5-R	U74AHC1G132G-AE5-R	SOT-23-5	Tape Reel
U74AHC1G132L-AL5-R	U74AHC1G132G-AL5-R	SOT-353	Tape Reel

<p>U74AHC1G132G-AE5-R</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) R: Tape Reel (2) AE5: SOT-23-5, AL5: SOT-353 (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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MARKING



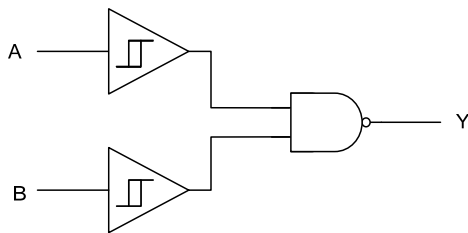
■ PIN CONFIGURATION



■ FUNCTION TABLE (each gate)

INPUTS		OUTPUT
A	B	Y
H	H	L
L	X	H
X	L	H

■ LOGIC DIAGRAM (positive logic)



■ ABSOLUTE MAXIMUM RATING (T_A=25°C , unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	RATINGS	UNIT
Supply Voltage	V _{CC}		-0.5 ~ +7.0	V
Input Voltage	V _{IN}		-0.5 ~ +7.0	V
Output Voltage	V _{OUT}		-0.5 ~ V _{CC} +0.5	V
Continuous V _{CC} or GND Current	I _{CC}		±50	mA
Continuous Output Current	I _{OUT}	V _{OUT} =0 ~ V _{CC}	±25	mA
Input Clamp Current	I _{IK}	V _{IN} < 0	-20	mA
Output Clamp Current	I _{OK}	V _{OUT} < 0 or V _{OUT} > V _{CC}	±20	mA
Storage Temperature Range	T _{STG}		-65 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	V _{CC}	Operating	2.0	5.0	5.5	V
Input Voltage	V _{IN}		0		5.5	V
Output Voltage	V _{OUT}	High or low state	0		V _{CC}	V
Operating Temperature	T _A		-40		+125	°C

■ ELECTRICAL CHARACTERISTICS (T_A=25°C , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Positive-going input threshold voltage	V _{T+}	V _{CC} =3.0V	1.2		2.2	V
		V _{CC} =4.5V	1.75		3.15	
		V _{CC} =5.5V	2.15		3.85	
Negative-going input threshold voltage	V _{T-}	V _{CC} =3.0V	0.9		1.9	V
		V _{CC} =4.5V	1.35		2.75	
		V _{CC} =5.5V	1.65		3.35	
Hysteresis (V _{T+} - V _{T-})	ΔV _T	V _{CC} =3V	0.3		1.2	V
		V _{CC} =4.5V	0.4		1.4	
		V _{CC} =5.5V	0.5		1.6	
High-Level Output Voltage	V _{OH}	V _{CC} =2.0V	I _{OH} =-50μA	1.9	2.0	V
		V _{CC} =3.0V		2.9	3.0	
		V _{CC} =4.5V		4.4	4.5	
		V _{CC} =3.0V, I _{OH} =-4mA	2.58			
		V _{CC} =4.5V, I _{OH} =-8mA	3.94			
Low-Level Output Voltage	V _{OL}	V _{CC} =2.0V	I _{OL} =50μA		0.1	V
		V _{CC} =3.0V			0.1	
		V _{CC} =4.5V			0.1	
		V _{CC} =3.0V, I _{OL} =4mA		0.36		
		V _{CC} =4.5V, I _{OL} =8mA		0.36		
Input Leakage Current	I _{I(LEAK)}	V _{CC} =0V ~ 5.5V, V _{IN} =5.5V or GND			±0.1	μA
Quiescent Supply Current	I _{CC}	V _{CC} =5.0V, V _{IN} =V _{CC} or GND, I _{OUT} =0			2	μA
Input Capacitance	C _I	V _{CC} =5.0V, V _{IN} =V _{CC} or GND		2	10	pF

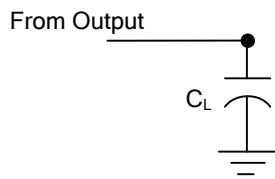
■ DYNAMIC CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified) (see Figure 1)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Propagation delay from input (A or B) to output(Y)	t_{PLH}/t_{PHL}	$V_{CC}=3.0V\pm 3.6V$	$C_L=15pF$		5.6	11.9	ns
			$C_L=50pF$		7.6	15.4	
		$V_{CC}=4.5V\pm 5.5V$	$C_L=15pF$		3.9	7.7	ns
			$C_L=50pF$		5.3	9.7	

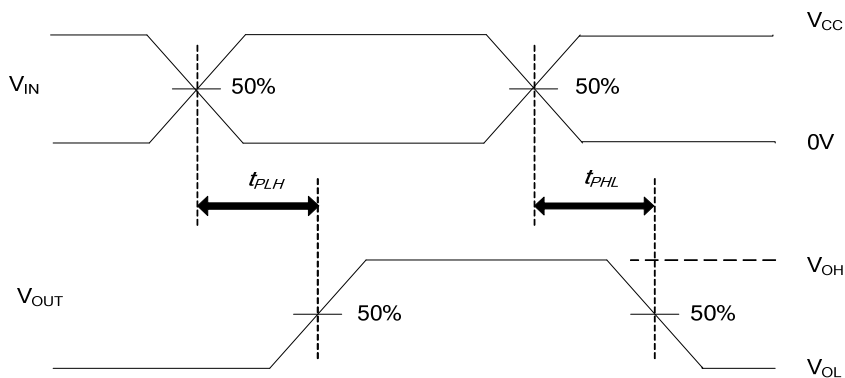
■ OPERATING CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	C_{PD}	$V_{CC}=5.0V$, $f=1\text{MHZ}$, No load.		11		pF

■ TEST CIRCUIT AND WAVEFORMS



TEST CIRCUIT



PROPAGATION DELAY TIMES

- Notes: 1. C_L includes probe and jig capacitance.
 2. All input pulses are supplied by generators having the following characteristics: PRR $\leq 1\text{MHz}$, $Z_o = 50\Omega$, $t_r \leq 3\text{ns}$, $t_f \leq 3\text{ns}$.

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